

# Exhibit B

**AMENDED EXHIBIT A TO JOINT CLAIM CONSTRUCTION AND PREHEARING STATEMENT****List of Constructions To Which Both Parties Have Agreed (U.S. Pat. No. 7,289,386)**

Claim	Term	Agreed-Upon Construction
1	memory devices	devices in which data is stored and retrieved
1	coupled to the printed circuit board	electrically connected to the printed circuit board
1	rank	a group of memory devices enabled to receive and transmit data by a common chip-select signal
1	command signal	a signal that initiates a predetermined type of computer operation, such as read, write, refresh, or precharge
1	number of ranks of memory modules	number of ranks of memory devices
11	chip-select signal	a control signal that enables the input and output of data to and/or from a memory device

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