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 16 GOOGLE INC.

17 UNITED STATES DISTRICT COURT  
 18 NORTHERN DISTRICT OF CALIFORNIA  
 19 OAKLAND DIVISION

20 GOOGLE INC.,

21 Plaintiff,

22 v.

23 NETLIST, INC.,

24 Defendant.

Case No. 08-04144 SBA

**[PROPOSED] CLAIM  
 CONSTRUCTION ORDER**

Date: November 12, 2009  
 Time: 9:00 a.m.  
 Place: Courtroom 3, 3<sup>rd</sup> Floor  
 Judge: Hon. Sandra Brown Armstrong

25 AND RELATED COUNTERCLAIMS.

1 On November 12, 2009, the Court held a hearing to construe disputed terms in Claims 1  
 2 and 11 of U.S. Patent No. 7,289,386. Plaintiff and Counterdefendant Google Inc. and Defendant  
 3 and Counterclaimant Netlist, Inc. appeared through their counsel of record. Upon full  
 4 consideration of all submitted papers and oral arguments in support of and in opposition to the  
 5 proposed claim constructions, and for good cause shown, the Court hereby construes the disputed  
 6 claim terms as follows:

| <u>Claim Term</u>   | <u>Construction</u>  |
|---|--|
| “logic element receiving a set of input control signals from the computer system”   | electronic circuitry operable to perform one or more particular functions and that receives input signals directly from the computer system  |
| “rank”  | row  |
| “signal”  | information presented on one or more pins of a device dedicated for that specific information  |
| “control signals”   | signals presented on control pins of the logic element   |
| “the set of input control signals corresponding to a second number of memory devices smaller than the first number of memory devices” | the set of input control signals generated by the computer system to control a memory module having the second number of memory devices, based on the computer system understanding the memory module to have the second number of devices |
| “command signal”  | a signal presented on command pins of the logic element  |
| “number of ranks of memory modules”   | no construction required   |
| “the first command signal corresponding to the second number of ranks”  | the first command signal generated by the computer system to command a memory module having the second number of ranks, based on the computer system understanding the memory module to have the second number of ranks                    |

1 "chip-select signal"

signal presented on chip-select pins of the logic  
2 element

3  
4 IT IS SO ORDERED.

5  
6 DATED: \_\_\_\_\_

7 \_\_\_\_\_  
8 The Hon. Sandra Brown Armstrong  
9 United States District Judge

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