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21 UNITED STATES DISTRICT COURT  
 22 NORTHERN DISTRICT OF CALIFORNIA  
 23 OAKLAND DIVISION

24 NETLIST, INC.,  
 25  
 26 Plaintiff,  
 27  
 28 v.  
 29  
 30 GOOGLE INC.,  
 31  
 32 Defendant.

CASE NO. CV-09-05718 SBA  
 [Related to Case No. CV-08-04144 SBA]

**PLAINTIFF NETLIST, INC.'S REPLY  
 CLAIM CONSTRUCTION BRIEF**



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*Andrew Corp. v. Gabriel Electronics, Inc.*, 847 F.2d 819, 822 (Fed. Cir. 1988)..... 11

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*Gart v. Logitech*, 254 F.3d 1334, 1339 (Fed. Cir. 2001)..... 4

*Georgia-Pacific Corp. v. U.S. Gypsum Co.*, 195 F.3d 1322, 1331 (Fed. Cir. 1999) .. 7

*Haemonetics Corp. v. Baxter Healthcare Corp.*, 607 F.3d 776 (Fed. Cir. 2010) ..... 7

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(Fed. Cir. 2004) ..... 10

*MBO Labs, Inc. v. Becton, Dickinson & Co.*, 474 F.3d 1323, 1333 (Fed. Cir. 2007) 9

*Oately Co. v. IPS Corp.*, 514 F.3d 1271, 1276 (Fed. Cir. 2008)..... 9

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1984)..... 11, 12

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1995)..... 7

*The Chamberlain Group, Inc. v. Lear Corp.*, 516 F.3d 1331, 1338 (Fed. Cir. 2008). 7

*Young v. Lumenis, Inc.*, 492 F.3d 1336 (Fed. Cir. 2007) ..... 11

Statutes

35 U.S.C. § 112..... 6

35 U.S.C. § 112(2) ..... 5

*Fed. R. Civ. P.* 56..... 1, 5

Rules

Patent Local Rule 3-3(d)..... 1, 5

1 **INTRODUCTION**

2 Google’s Responsive Claim Construction Brief confirms that each one of Google’s  
3 proposed constructions violates the Federal Circuit’s canons of claim construction. Google does  
4 not dispute that its indefiniteness challenges to the ‘912 Patent violate the requirements for  
5 moving for summary judgment under *Fed. R. Civ. P. 56* and the Local Rules of this Court.  
6 Google’s Brief also confirms that the grounds of Google’s indefiniteness challenges are not set  
7 forth in its Preliminary Invalidity Contentions, in violation of Patent Local Rule 3-3(d).

8 Google’s construction of “bank” violates the Federal Circuit’s prohibition against  
9 construing terms with broad dictionary definitions that are divorced from the intrinsic evidence,  
10 such as the specification and file history. Also, Google has now revealed its true purpose in  
11 seeking a construction of “bank,” which is to apply the Court’s definition to *the prior art* so it can  
12 argue that the term “bank” in prior art references means the same thing as “rank” in the ‘912  
13 Patent claims. The purpose of claim construction is to construe the patent claims at issue, not the  
14 prior art, and Google’s attempt to do so should be rejected.

15 Google’s constructions of the “input signal limitations” and “output signal limitations”  
16 violate the Federal Circuit’s prohibition against construing the same claim language differently in  
17 different claims unless the patent so provides. Google misleadingly suggests that the recitation of  
18 particular *species* of input and output control signals in various claims warrants defining the  
19 broader *genus* terms “set of input control signals” and “set of output control signals” (or their  
20 variants) differently in different claims. However, Google cites no case holding that such varying  
21 and inconsistent constructions are appropriate. Further, Google points to nothing in the text of the  
22 ‘912 Patent that calls for varying and claim-specific constructions of these terms.

23 Google’s construction of “operatively coupled” and “operationally coupled” cannot be  
24 squared with the Federal Circuit’s holding that the similar phrase “operatively connected” is a  
25 general term of patent drafting used to reflect a *functional* relationship between components. In  
26 addition, Google’s construction adds nothing to the meaning of “coupled,” running afoul of the  
27 Federal Circuit’s requirement that all claim terms should give meaning to the claim.

28

1 Google's construction of "at a time" as meaning "at the same time" violates the Federal  
2 Circuit's doctrine of claim differentiation by rendering claims 18 and 20 redundant. Google does  
3 not dispute the redundancy, but instead attempts to avoid the doctrine by arguing that no other  
4 construction is possible. However, Google fails to address the evidence presented by Netlist  
5 showing that Table 1 of the specification establishes a broader meaning for "at a time" than  
6 Google's construction would allow.

7 With respect to its indefiniteness challenges, Google does not dispute or even address the  
8 fact that its challenge to claim 45 necessarily relies on an implicit construction of claim 39, which  
9 limits the latter claim to those modules in which all three of a logic element, register, and phase  
10 lock loop device are combined in a single component. Instead, Google camouflages its implicit  
11 construction by seeking no construction of claim 39 and suggesting that it is merely applying the  
12 claim's "plain meaning." Google also mischaracterizes claim 45 as being *limited* to those modules  
13 in which *only two* (and not all three) of a logic element, register, and phase lock loop are combined  
14 in a single component. However, the claim expressly uses the phrase "two or more" and clearly  
15 embraces modules in which two or three of the logic element, register, and phase lock loop are  
16 provided in a single component.

17 Google's argument that "spaced from" in claims 10 and 11 is indefinite runs afoul of  
18 established Federal Circuit precedent indicating that numerical precision in patent claims is not  
19 required. Google's invalidity challenges to "spaced from" and "in a direction along the first  
20 side/in a direction along the second side" ignore Google's application of those terms to the alleged  
21 prior art in its Preliminary Invalidity Contentions. Google suggests that the Court should ignore  
22 those Contentions because Google is entitled to use "alternative pleading." However, its  
23 Preliminary Invalidity Contentions do not include alternative pleading. Google simply applied the  
24 disputed terms to the alleged prior art in an unqualified manner and made no mention of the  
25 alleged indefiniteness of the now-disputed phrases. Google's constructions and indefiniteness  
26 contentions are disingenuous and should be rejected.

1 ARGUMENT

2 I. THE COURT SHOULD REJECT GOOGLE’S CONSTRUCTIONS AND  
3 INDEFINITENESS ARGUMENTS AND ADOPT NETLIST’S CONSTRUCTIONS IN  
4 THEIR ENTIRETY

5 A. **Google’s Construction of “Bank” Improperly Relies on a Broad Dictionary**  
6 **Definition Divorced From the Specification**

7 As Google acknowledges, “A term’s plain meaning is not determined in a vacuum, but  
8 rather is given its meaning to the ordinary artisan after reading the entire patent.” Google’s  
9 Responsive Claim Construction Brief (Dkt. 49) (“Google’s Brief”) at 2, citing *Phillips v. AWH*  
10 *Corp.*, 415 F.3d 1303, 1312 (Fed. Cir. 2005)(*en banc*). Google’s definition of “bank” relies on an  
11 IEEE dictionary that broadly defines the term to cover *any* “addressable unit of memory cells.”  
12 Google’s Brief at 4-5. Nowhere does the ‘912 Patent use the term “bank” so broadly. In every  
13 instance, the term is used to describe a group of memory cells or locations inside a memory device.  
14 Google cites—and then ignores—the Federal Circuit’s pronouncement that “heavy reliance on the  
15 dictionary divorced from the intrinsic evidence risks transforming the meaning of the claim term to  
16 the artisan into the meaning of the term in the abstract, out of its particular context, which is the  
17 specification.” Google’s Brief at 3, citing *Phillips*, 415 F.3d at 1321. The context provided by the  
18 ‘912 Patent makes clear that “bank” refers to an internal memory cell or location.

19 The asserted claims recite memory devices, called “DDR memory devices.” DDR memory  
20 devices each include banks, which are internal memory locations, as shown in Tables 3A and 3B.  
21 The ‘912 Patent at 12:39-55 and 12:66-13:11 (Hansen Decl., Exh. A). In the asserted claims, the  
22 term “bank” appears only in the phrase “bank address signals.” *Id.* at 33:3-4, 34:6, 28:49-50, and  
23 39:56-57. Thus, the recitation of “DDR memory devices” makes clear that “bank address signals”  
24 refers to the *internal banks* associated with DDR memory devices. Google has not identified one  
25 usage of the term “bank” to refer to anything other than a memory location internal to a memory  
26 device.

27 Google can only point to a couple of isolated locations in the ‘912 Patent specification  
28 where the term “internal” precedes the word “bank.” Google’s Brief at 5. However, in numerous

1 other locations in the specification, “bank” is used without the modifier “internal” but nevertheless  
2 clearly refers to an internal location within a memory device. See the ‘912 Patent at 1:40-44 , 1:58-  
3 62, 10:59-62, 12:39-55 (Hansen Decl., Exh. A).

4 Google contends that its dictionary definition is appropriate because Netlist has not “acted  
5 as its own lexicographer” and because the ‘912 Patent “demands no exclusive limitations on the  
6 definition.” Google’s Brief at 4. However, if taken at face value, Google’s argument is that a  
7 broad dictionary definition should govern a claim construction unless there is an explicit definition  
8 or disclaimer to the contrary in the specification. This is the very methodology that the Federal  
9 Circuit rejected in *Phillips*. “[A]ssigning such a limited role to the specification, and in particular  
10 requiring that any definition of claim language in the specification be express, is inconsistent with  
11 our rulings that the specification is ‘the single best guide to the meaning of a disputed term,’ and  
12 that the specification ‘acts as a dictionary when it expressly defines terms used in the claims or  
13 when it defines terms by implication.’” *Phillips*, 415 F.3d at 1320-1321.

14 Google’s Brief reveals that Google seeks a construction of the term “bank” in the alleged  
15 prior art and not a construction of the term used in the ‘912 patent claim phrase “bank address  
16 signal.” Both Netlist’s and Google’s experts have pointed out that certain prior art references use  
17 the term “bank” to refer to collections of memory devices. Google’s Brief at 5-6. More  
18 specifically, Google argues that a particular prior art reference uses the term “‘bank’ to refer to  
19 addressable memory that spans multiple memory devices.” *Id.* at 6. Google seeks a judicial  
20 definition of “bank” so it can argue that the “banks” in these references are the same as the *ranks* of  
21 the asserted claims. However, the purpose of claim construction is to construe the meaning of  
22 claim terms, not to construe the prior art. See *Gart v. Logitech*, 254 F.3d 1334, 1339 (Fed. Cir.  
23 2001) (the purpose of claim construction is to elaborate *claim language*). Thus, the Court should  
24 apply the claim construction methodology mandated by *Phillips* and define “bank” to mean “a  
25 group of memory cells or locations inside a memory device.”

1           **B.       Google’s Indefiniteness Challenge to Claim 45 is Improper and Relies on an**  
2                           **Insupportable Construction of Claim 39**

3           Google makes no attempt to reconcile its request for a judgment of invalidity based on  
4 indefiniteness with the summary judgment requirements of *Fed. R. Civ. P.* 56 or the Rules of this  
5 Court. For this reason alone, Google’s indefiniteness argument should be rejected. Google  
6 similarly fails to show that it complied with Patent Local Rule 3-3(d) by including the alleged  
7 indefiniteness of claim 45 in its Preliminary Invalidity Contentions. In a footnote, Google  
8 contends that it “clearly and unequivocally stated that the asserted claims of the patent are invalid  
9 under § 112, ¶¶1-2.” Google’s Brief at 7 n.1. However, Patent Local Rule 3-3(d) requires Google  
10 to specify “[a]ny *grounds* of invalidity based on . . . indefiniteness under 35 U.S.C. § 112(2) . . . .”  
11 PLR 3-3(d)(emphasis added). Google’s Preliminary Invalidity Contentions state that “Exhibit 14  
12 [is] a claim chart that identifies exemplary grounds of invalidity for the asserted claims based on  
13 indefiniteness . . . .” Weingaertner Decl., Exh. D at 6 (Dkt. 49-5). However, the referenced  
14 Exhibit 14 (which Google did not provide with its Responsive Brief) makes *no reference to any*  
15 *grounds* of indefiniteness. Hansen Decl. Exh. G. For this reason as well, the Court should reject  
16 Google’s request for a finding of indefiniteness.

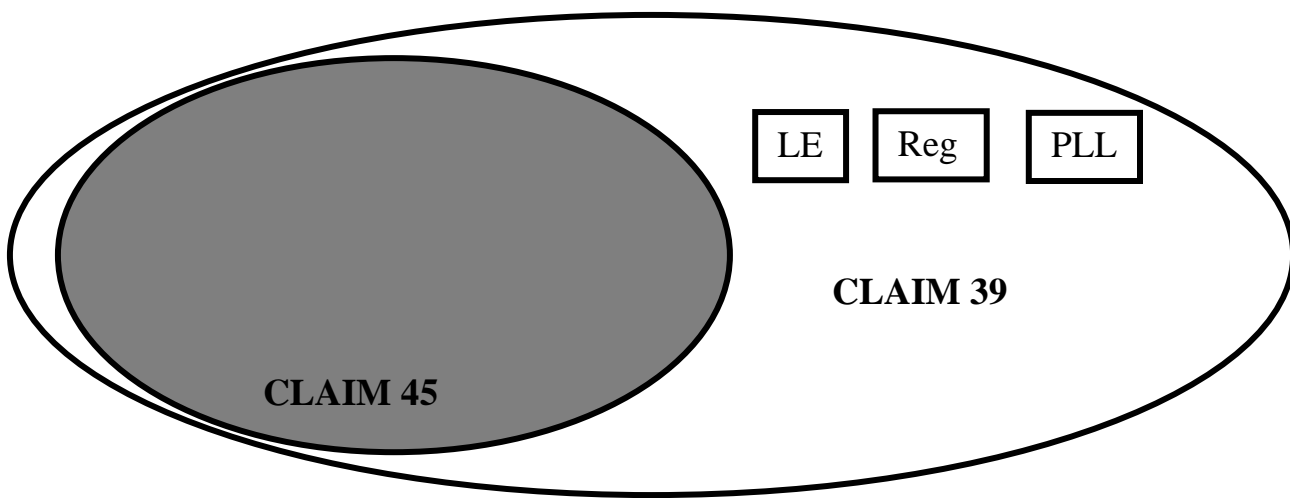
17           Regardless of whether the Court adjudicates Google’s indefiniteness argument now or  
18 later, the argument makes clear that the parties dispute the proper construction of “the at least one  
19 integrated circuit element comprising a logic element, a register and a phase lock loop” in claim  
20 39, from which claim 45 depends. Google does not dispute that its indefiniteness argument for  
21 claim 45 is necessarily based on an implicit construction of claim 39 which limits the latter claim  
22 to modules having a single circuit that includes all three of a logic element, register, and phase  
23 lock loop. Google asks the Court not to construe the phrase “the at least one integrated circuit” so  
24 it can conceal its implicit claim construction and asserts that in so doing, it is merely according the  
25 claim its “plain meaning.” Google’s Brief at 7.

26           Google effectively concedes that Netlist’s construction is correct by arguing that “Claim 39  
27 *includes within its scope* the possibility that the logic element, the register, and the phase lock loop  
28



1 are part of a single circuit (as well as two, three, or more integrated circuits).” Google’s Brief at 7  
2 (emphasis added). However, Google then argues that “claim 45 attempts to exclude the possibility  
3 that all three elements may be part of a single component by specifying that only two of them are  
4 part of a single component.” *Id.* Google is incorrect in its characterization of claim 45. Claim 45  
5 does not exclude embodiments in which the logic element, register, and phase lock loop are part of  
6 a single component. Instead, it includes those embodiments and *further includes* those  
7 embodiments in which only two of them are in a single component. This necessarily follows from  
8 the phrase “two or more” in claim 45. Google ignores the phrase “or more.” Claim 45 excludes  
9 those modules in which each of the logic element, register, and phase lock loop is provided in its  
10 own separate component, not those in which all three are combined in a single component.

11 Google boldly misstates the law when it argues that “because claim 39 includes the  
12 possibility that all three elements may be part of a single component, claim 45 has to be read to  
13 apply to all of the possible embodiments.” *Id.* That is simply not true. By limiting claim 39,  
14 claim 45 can exclude embodiments that fall within the scope of claim 39: “[A] claim in dependent  
15 form shall contain a reference to a claim previously set forth and then specify a further limitation  
16 of the subject matter claimed.” 35 U.S.C. § 112, ¶4. If properly construed, claim 39 encompasses  
17 one or more integrated circuit elements among which a logic element, register, and phase lock  
18 loop device are distributed. Claim 45 limits claim 39 by requiring that two or more of them be  
19 combined in a single component. A Venn diagram illustrates the fallacy in Google’s reasoning:



1 Each box represents a single component. “LE” is a logic element. “Reg” is a register, and  
2 “PLL” is a phase lock loop. As the foregoing indicates, as properly construed, claim 39 embraces  
3 everything in claim 45, while claim 45 embraces a subset of claim 39. When claim 39 is properly  
4 construed, claim 45 is definite.

5 **C. Google’s Constructions of the Input Signal Limitations and Output Signal**  
6 **Limitations Violate the Federal Circuit’s Prohibition Against Inconsistently**  
7 **Construing Claim Terms in Different Claims**

8 Google ignores and fails to address the Federal Circuit’s repeated prohibition against  
9 construing the same claim terms differently in different claims “unless the patent otherwise  
10 provides.” *Georgia-Pacific Corp. v. U.S. Gypsum Co.*, 195 F.3d 1322, 1331 (Fed. Cir. 1999); *The*  
11 *Chamberlain Group, Inc. v. Lear Corp.*, 516 F.3d 1331, 1338 (Fed. Cir. 2008); *Southwall*  
12 *Technologies, Inc. v. Cardinal IG Co.*, 54 F.3d 1570, 1579 (Fed. Cir. 1995). Instead, Google  
13 incorrectly suggests that that *Haemonetics Corp. v. Baxter Healthcare Corp.*, 607 F.3d 776 (Fed.  
14 Cir. 2010) sanctions inconsistent constructions of the same claim language in different claims  
15 based merely on “the plain language of the claim.” Google’s Brief at 8. Google ignores the  
16 Federal Circuit’s express holding that the specification of the patent at issue in *Haemonetics*  
17 defined the disputed claim term differently for two distinct embodiments, each of which tracked  
18 the language of different claims:

19 Furthermore, the specification defines "centrifugal unit" in the context of the height  
20 and radius limitations in two different embodiments, one that tracks the language of  
21 claim 1, in which the parties agree that "centrifugal unit" refers to the vessel alone,  
22 and one that tracks the language of claim 16.

23 *Haemonetics*, 607 F.3d at 782. The ‘912 Patent does not describe any of the Input Signal  
24 Limitations or Output Signal Limitations differently as between different claims. Nor has Google  
25 even suggested it does. *Haemonetics* does not support Google’s construction.

26 Google mistakes the fundamental difference between a genus, such as “input control  
27 signal” or “output control signal,” and species--such as row/column address signals, command  
28 signals, bank address signals, and chip-select signals--that fall within the broader genus. As  
Google points out, the various independent claims *inclusively* recite particular species of input and

1 output control signals and therefore provide varying scope and different points of novelty for those  
2 claims. However, that fact does not suggest that the definitions of the genus terms “input control  
3 signals,” input signals,” “output control signals,” or “output signals” differ from claim to claim.  
4 Google may argue that the various independent claims of the ‘912 Patent should be applied  
5 differently to the accused 4-Rank FBDIMMs or prior art because of the particular species of input  
6 and output signals that are recited in the claims. Whether the claims can be applied in such a  
7 manner is not a claim construction issue. The meaning of the genus input signal limitation terms  
8 and output signal limitation terms does not vary from claim to claim.

9 Referring to claims 1 and 15, Google asserts that “when ‘command signal’ is explicitly  
10 introduced as “a command signal” long after the term ‘set of input control signals’ is used, that  
11 command signal must be distinct from whatever the ‘set of input control signals covers.’”  
12 Google’s Brief at 9. Google cites no authority for this proposition and it is incorrect. Instead, the  
13 claims cover embodiments in which the “command signal” is part of the same set of input control  
14 signals as the listed control signal species as well as those in which it is not. Google does not  
15 dispute the fact that the word “comprising” means that the “set of input control signals” is an *open*  
16 *set* that requires the listed species (row/address signal, bank address signals, and at least one chip  
17 select signal) but which may include additional species. Google’s Brief at 8; ‘912 Patent at 33:2-4  
18 and 34:35-37 (Hansen Decl., Exh. A). Thus, the additionally recited “command signal” may be  
19 part of *or* distinct from the set of input control signals.

20 Nor does Google dispute the fact that the ‘912 Patent provides no implicit or explicit  
21 definition of any Input Signal Limitations or Output Signal Limitations which excludes command  
22 signals. Further, Google’s construction improperly reads out the embodiments of the ‘912 Patent  
23 from the scope of claims 1 and 15.

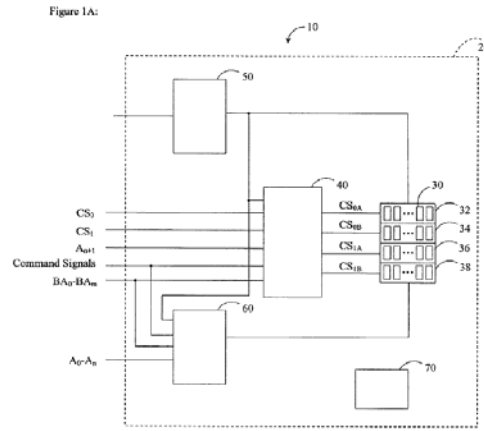
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The description of FIG. 1A in the '912 Patent makes clear that the chip select signals ( $CS_0$ ,  $CS_1$ ), row/column address signal ( $A_{n+1}$ ), bank address signals ( $BA_0$ - $BA_m$ ), and command signals, are all part of a set of input control signals received by logic element 40: "As schematically illustrated by FIGS. 1A and 1B, in certain embodiments, the logic element 40 receives a set of input control signals, which includes address signals (e.g., bank address signals, row address signals, column address signals, gated column address strobe signals, chip-select signals) and command signals (e.g., refresh, precharge) from the computer system." The '912 Patent at 6:55-61 (Hansen Decl., Exh. A). Claims 1 and 15 require, but are not limited to, those sets of input control signals that include specific control signal species, namely, "at least one row/column address signal, bank address signals, and at least one chip select signal." *Id.* at 33:2-4; 34:35-37. Because of the term "comprising," a command signal may or may not be received with the set that includes the row/column address signal, bank address signals, and at least one chip select signal. However, under Google's construction, FIG. 1A would be excluded from claims 1 and 15, a result which reveals the impropriety of Google's effort to limit the term "set of input control signals" to exclude command signals. *See Oatley Co. v. IPS Corp.*, 514 F.3d 1271, 1276 (Fed. Cir. 2008); *see also MBO Labs, Inc. v. Becton, Dickinson & Co.*, 474 F.3d 1323, 1333 (Fed. Cir. 2007). The '912 Patent makes clear that the output control signals generated in FIG. 1A also include a command signal, a structure that would be excluded by Google's construction. *Id.* at 6:61-63.



1 In addition, Google mischaracterizes the '912 Patent Specification to support its  
2 construction. Citing column 5, lines 28-31, Google quotes the patent as stating that “the phase-  
3 lock loop device transmits clock signals to the plurality of memory devices 30, the logic element  
4 50, and the register 60.” Google’s Brief at 17. Google then states that the quoted passage refers to  
5 “this electrical connection.” *Id.* The quoted passage says nothing about direct or indirect  
6 electrical connections. Nowhere does the '912 Patent impose a requirement of direct or indirect  
7 electrical connections between the phase lock loop, logic element, and/or register. Google’s  
8 construction is inaccurate and should be rejected.

9  
10 **E. Google’s Indefiniteness Challenge to “Spaced From” Violates Federal Circuit**  
11 **Precedent Holding that Numerical Specificity in Claim Drafting is Not**  
12 **Required**

13 Google does not dispute the fact that its indefiniteness challenge to the term “spaced from”  
14 is premised on an alleged requirement of numerical specificity patent claims. However, Google  
15 ignores and cannot reconcile its position with *Andrew Corp. v. Gabriel Electronics, Inc.*, 847 F.2d  
16 819, 822 (Fed. Cir. 1988) which Netlist cited in its opening brief and which rejected that position.  
17 Similarly, in *Young v. Lumenis, Inc.*, 492 F.3d 1336 (Fed. Cir. 2007), the Federal Circuit  
18 considered an indefiniteness challenge to the term “near” in a claim directed to a laser-assisted  
19 method of declawing cats. The Federal Circuit found the challenged claim term definite, holding  
20 that “[a]s used in the claim, ‘near’ is not insolubly ambiguous and does not depart from the  
21 ordinary and customary meaning of the phrase ‘near’ as meaning ‘close to or at’ the edge of the  
22 unguis crest [recited in the claim].” *Id.* at 1346. *See also, Rosemount, Inc. v. Beckman*  
23 *Instruments*, 727 F.2d 1540, 1546-1547 (Fed. Cir. 1984) (affirming trial court’s finding that the  
24 phrase “close proximity” did not render an asserted patent claim indefinite even though it was not  
25 “specifically or precisely defined”).

26 Google also ignores the fact that it did not assert that “spaced from” is indefinite in its  
27 Preliminary Invalidity Contentions. As stated above in Section I.B., Google made a cursory  
28 reference to indefiniteness in the body of its Preliminary Invalidity Contentions Document.

1 However, nowhere did it explain that the grounds for indefiniteness included the phrase “spaced  
2 from.”

3 Google’s application of the term “spaced from” to the alleged prior art cited in its  
4 Preliminary Invalidity Contentions is also fatal to its indefiniteness attack. Google does not  
5 dispute that it applied the term to the prior art, but attempts to dodge the issue by asserting that it  
6 “can plead invalidity in the alternative.” Google’s Brief at 19 n.4. Nevertheless, Google *did not*  
7 alternatively argue that “spaced apart” was indefinite. It made no mention of it whatsoever.  
8 Google’s application of “spaced apart” to the alleged prior art is *unqualified*, Hansen Decl., Exh.  
9 G, and is fatal to its indefiniteness challenge. *Rosemount, Inc.*, 727 F.2d at 1547 (rejecting  
10 defendant Beckman’s indefiniteness argument and noting that “Beckman is confronted also with  
11 its own ease in applying ‘close proximity’ to the prior art at trial and in its briefs”); *see also*,  
12 *Chiron Corp. v. Genentech, Inc.*, 2002 U.S. Dist. LEXIS 19150 at \*15 (“Genentech’s use of similar  
13 terminology without apparent difficulty in arguing that the ‘561 Patent is invalid in light of the  
14 prior art . . . is another yet another indication that what is meant by ‘useful degree of affinity’ is  
15 not indefinite”).

16 **F. Google’s Indefiniteness Challenge to “In a direction along the first side/ in a**  
17 **direction along the second side” Ignores the Intrinsic Evidence**

18 Google does not dispute that its indefiniteness challenge to “in a direction along the first  
19 side/ in a direction along the second side” in claim 11 is premised on a misreading of claim 10,  
20 from which claim 11 depends. Google asserts that claim 11 is indefinite because “The  
21 specification provides no guidance regarding the placement of *ranks* in a direction. Google’s  
22 Brief at 19 (third column of table at the bottom of the page). Claims 10 and 11 recite the spacing  
23 of *sets*--not *ranks*-- of DDR memory devices in directions along the first and second sides of the  
24 memory module. Google fails to address this flawed premise in its indefiniteness contention.

25 Google also fails to explain why common words such as “in a direction” would not be  
26 understood by those skilled in the art. In its Opening Brief, Netlist clearly illustrated an example  
27 of these terms with reference to Figures 11A and 11B of the ‘912 Patent. Google made no attempt  
28 to explain how those figures are not sufficiently illustrative of the meaning of the disputed terms.

1 Google similarly does not dispute that it readily applied “in a direction along the first side/in a  
2 direction along the second side” to the alleged prior art in its Preliminary Invalidation Contentions.  
3 Google did not even mention this particular ground of alleged indefiniteness, much less argue it  
4 “in the alternative.” Thus, Google’s indefiniteness challenge should be rejected.

5 **G. Google’s Construction of “At a Time” Violates the Doctrine of Claim**  
6 **Differentiation and Renders Claims 18 and 20 Redundant**

7 Google does not dispute the fact that its construction of “at a time” as meaning “at the  
8 same time” in the claim 18 phrase “transmitted to two ranks . . . at a time” would render claim 20  
9 redundant due to the latter claim’s recitation of the term “concurrently.” Nor does Google dispute  
10 that the Federal Circuit’s doctrine of claim differentiation, if applied, would preclude its  
11 construction. Instead, Google argues that the doctrine is inapplicable, asserting that “[t]he only  
12 reference in the specification to what ‘at a time’ means suggests that it happens concurrently, and  
13 thus at the same time.” Google’s Brief at 20. However, Google selectively quotes one passage  
14 from column 8 of the ‘912 Patent and ignores Netlist’s discussion of Table I from the Patent.  
15 Netlist explained that Table I illustrates how the phrase “at a time” refers to the selection of two  
16 ranks of memory during a particular time period, which in the case of Table I is a time period  
17 during which a given input signal state is present and two ranks are selected. Netlist’s Opening  
18 Brief at 24-25. Thus, Google cannot escape the doctrine of claim differentiation and the resulting  
19 impropriety of its construction.

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**CONCLUSION**

For the reasons provided above, the Court should adopt Netlist’s claim constructions in their entirety and reject Google’s claim constructions and indefiniteness challenges to the asserted claims.

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**LEE TRAN & LIANG APLC**

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