

**Table 40—Requirements in Spain
(values specified in GHz)**

Channel #	Value	Channel #	Value	Channel #	Value
47	2.447	56	2.456	65	2.465
48	2.448	57	2.457	66	2.466
49	2.449	58	2.458	67	2.467
50	2.450	59	2.459	68	2.468
51	2.451	60	2.460	69	2.469
52	2.452	61	2.461	70	2.470
53	2.453	62	2.462	71	2.471
54	2.454	63	2.463	72	2.472
55	2.455	64	2.464	73	2.473

**Table 41—Requirements in France
(values specified in GHz)**

Channel #	Value	Channel #	Value	Channel #	Value
48	2.448	60	2.460	72	2.472
49	2.449	61	2.461	73	2.473
50	2.450	62	2.462	74	2.474
51	2.451	63	2.463	75	2.475
52	2.452	64	2.464	76	2.476
53	2.453	65	2.465	77	2.477
54	2.454	66	2.466	78	2.478
55	2.455	67	2.467	79	2.479
56	2.456	68	2.468	80	2.480
57	2.457	69	2.469	81	2.481
58	2.458	70	2.470	82	2.482
59	2.459	71	2.471	—	—

14.6.6 Occupied channel bandwidth

Occupied channel bandwidth shall meet all applicable local geographic regulations for 1 MHz channel spacing. The rate at which the PMD entity will hop is governed by the MAC. The hop rate is an attribute with a maximum dwell time subject to local geographic regulations.

14.6.7 Minimum hop rate

The minimum hop rate shall be governed by the regulatory authorities.

14.6.8 Hop sequences

The hopping sequence of an individual PMD entity is used to create a pseudorandom hopping pattern utilizing uniformly the designated frequency band. Sets of hopping sequences are used to co-locate multiple PMD entities in similar networks in the same geographic area and to enhance the overall efficiency and throughput capacity of each individual network.

An FH pattern, F_x , consists of a permutation of all frequency channels defined in Table 38 and Table 39. For a given pattern number, x , the hopping sequence can be written as follows:

$$F_x = \{f_x(1), f_x(2), \dots, f_x(p)\}$$

where

- $f_x(i)$ is the channel number (as defined in 14.6.4) for i^{th} frequency in x^{th} hopping pattern;
- p is the number of frequency channels in hopping pattern (79 for North America and most of Europe, 23 for Japan, 27 for France, 35 for Spain)

Given the hopping pattern number, x , and the index for the next frequency, i (in the range 1 to p), the channel number shall be defined to be as follows:

$$\begin{aligned}
 f_x(i) &= [b(i) + x] \bmod (79) + 2 && \text{in North America and most of Europe, with } b(i) \text{ defined in Table 42.} \\
 &= [(i - 1) \times x] \bmod (23) + 73 && \text{in Japan.} \\
 &= [b(i) + x] \bmod (27) + 47 && \text{in Spain with } b(i) \text{ defined in Table 43.} \\
 &= [b(i) + x] \bmod (35) + 48 && \text{in France with } b(i) \text{ defined in Table 44.}
 \end{aligned}$$

Table 42—Base-Hopping sequence $b(i)$ for North America and most of Europe

i	$b(i)$	i	$b(i)$	i	$b(i)$	i	$b(i)$	i	$b(i)$	i	$b(i)$	i	$b(i)$	i	$b(i)$
1	0	11	76	21	18	31	34	41	14	51	20	61	48	71	55
2	23	12	29	22	11	32	66	42	57	52	73	62	15	72	35
3	62	13	59	23	36	33	7	43	41	53	64	63	5	73	53
4	8	14	22	24	72	34	68	44	74	54	39	64	17	74	24
5	43	15	52	25	54	35	75	45	32	55	13	65	6	75	44
6	16	16	63	26	69	36	4	46	70	56	33	66	67	76	51
7	71	17	26	27	21	37	60	47	9	57	65	67	49	77	38
8	47	18	77	28	3	38	27	48	58	58	50	68	40	78	30
9	19	19	31	29	37	39	12	49	78	59	56	69	1	79	46
10	61	20	2	30	10	40	25	50	45	60	42	70	28	—	—

Table 43—Base-Hopping sequence $b(i)$ for Spain

i	$b(i)$	i	$b(i)$	i	$b(i)$
1	13	10	19	19	14
2	4	11	8	20	1
3	24	12	23	21	20
4	18	13	15	22	7
5	5	14	22	23	16
6	12	15	9	24	2
7	3	16	21	25	11
8	10	17	0	26	17
9	25	18	6	27	26

Table 44—Base-Hopping sequence $b(i)$ for France

i	$b(i)$	i	$b(i)$	i	$b(i)$
1	17	13	31	25	15
2	5	14	20	26	3
3	18	15	29	27	11
4	32	16	22	28	30
5	23	17	12	29	24
6	7	18	6	30	9
7	16	19	28	31	27
8	4	20	14	32	19
9	13	21	25	33	2
10	33	22	0	34	21
11	26	23	8	35	34
12	10	24	1	—	—

The sequences are designed to ensure some minimum distance in frequency between contiguous hops. The minimum hop size is 6 MHz for North America and Europe, including Spain and France, and 5 MHz for Japan.

The hopping pattern numbers x are divided into three sets. The sets are designed to avoid prolonged collision periods between different hopping sequences in a set. Hopping sequence sets contain 26 sequences for North America and Europe, and 4 sequences per set for Japan:

For North America and most of Europe:

$$x = \{0,3,6,9,12,15,18,21,24,27,30,33,36,39,42,45,48,51,54,57,60,63,66,69,72,75\}$$

Set 1

$$x = \{1,4,7,10,13,16,19,22,25,28,31,34,37,40,43,46,49,52,55,58,61,64,67,70,73,76\}$$

Set 2

$$x = \{2,5,8,11,14,17,20,23,26,29,32,35,38,41,44,47,50,53,56,59,62,65,68,72,74,77\} \quad \text{Set 3}$$

For Japan:

$$\begin{aligned} x &= \{6,9,12,15\} && \text{Set 1} \\ x &= \{7,10,13,16\} && \text{Set 2} \\ x &= \{8,11,14,17\} && \text{Set 3} \end{aligned}$$

For Spain:

$$\begin{aligned} x &= \{0,3,6,9,12,15,18,21,24\} && \text{Set 1} \\ x &= \{1,4,7,10,13,16,19,22,25\} && \text{Set 2} \\ x &= \{2,5,8,11,14,17,20,23,26\} && \text{Set 3} \end{aligned}$$

For France:

$$\begin{aligned} x &= \{0,3,6,9,12,15,18,21,24,27,30\} && \text{Set 1} \\ x &= \{1,4,7,10,13,16,19,22,25,28,31\} && \text{Set 2} \\ x &= \{2,5,8,11,14,17,20,23,26,29,32\} && \text{Set 3} \end{aligned}$$

The three sets of hopping sequences for North America and most of Europe, of 26 patterns each, are listed Tables B.1, B.2, and B.3 in Annex B. Similarly, there are three sets for Japan of four patterns each. The three sets for Spain have nine patterns each. The three sets for France have 11 patterns each. The channel numbers listed under each pattern refer to the actual frequency values listed in Table 38 and Table 39.

14.6.9 Unwanted emissions

Conformant PMD implementations of this FHSS standard shall limit the emissions that fall outside of the operating frequency range, defined in Table 36 of 14.6.3, to the geographically applicable limits.

14.6.10 Modulation

The minimum set of requirements for a PMD to be compliant with the IEEE 802.11 FHSS PHY shall be as follows.

The PMD shall be capable of operating using two-level Gaussian frequency shift key (GFSK) modulation with a nominal bandwidth bit-period (BT)=0.5. The PMD shall accept symbols from the set $\{\{1\},\{0\}\}$ from the PLCP. The symbol $\{1\}$ shall be encoded with a peak deviation of $(+f_d)$, giving a peak transmit frequency of (F_c+f_d) , which is greater than the carrier center frequency (F_c). The symbol $\{0\}$ shall be encoded with a peak frequency deviation of $(-f_d)$, giving a peak transmit frequency of (F_c-f_d) .

An incoming bit stream at 1 Mbit/s will be converted to symbols at $F_{clk} = 1 \text{ Msymbols/s}$, as shown in Table 45.

Table 45—Symbol encoding into carrier deviation (1 Mbit/s, 2-GFSK)

Symbol	Carrier deviation
1	$1/2 \times h_2 \times F_{clk}$
0	$-1/2 \times h_2 \times F_{clk}$
NOTE—These deviation values are measured using the center symbol of 7 consecutive symbols of the same value. The instantaneous deviation will vary due to Gaussian pulse shaping.	

The deviation factor h_2 for 2GFSK (measured as difference between frequencies measured in the middle of 0000 and 1111 patterns encountered in the SFD, divided by 1 MHz) will nominally be 0.32.

The minimum frequency deviation, as shown in Figure 83, shall be greater than 110 kHz relative to the nominal center frequency F_c . F_d is the average center frequency of the last 8 bits of the Preamble Sync field, measured as the deviation at the midsymbol. Midsymbol is defined as the point that is midway between the zero crossings derived from a best fit to the last 8 bits of the Sync field. Maximum deviation is not specified, but modulation is subject to the occupied bandwidth limits of 14.6.5.

The zero crossing error shall be less than $\pm 1/8$ of a symbol period. The zero crossing error is the time difference between the ideal symbol periods and measured crossings of F_c . This is illustrated in Figure 83.

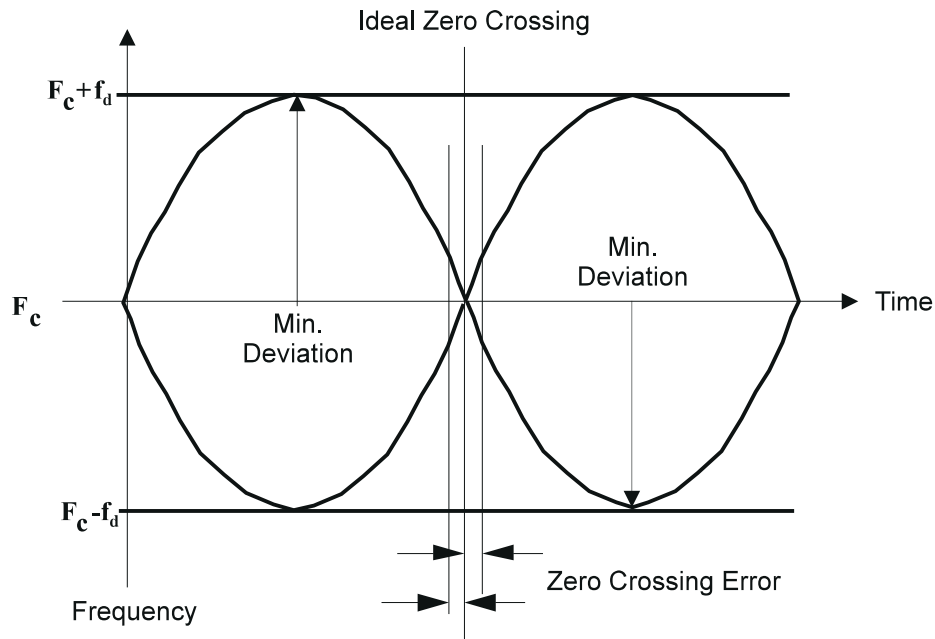


Figure 83—Transmit modulation mask

14.6.11 Channel data rate

A compliant IEEE 802.11 FHSS PMD shall be capable of transmitting and receiving at a nominal data rate of 1.0 Mbit/s \pm 50 ppm.

14.6.12 Channel switching/settling time

The time to change from one operating channel frequency, as specified in 14.6.3, is defined as 224 μ s. A conformant PMD meets this switching time specification when the operating channel center frequency has settled to within ± 60 kHz of the nominal channel center frequency as outlined in 14.6.3.

14.6.13 Receive to transmit switch time

The maximum time for a conformant PMD to switch the radio from the receive state to the transmit state and place the start of the first bit on the air shall be 19 μ s. At the end of this 19 μ s, the RF carrier shall be within the nominal transmit power level range, and within the described modulation specifications.

14.6.14 PMD transmit specifications

The following portion of this subclause describes the transmit functions and parameters associated with the PMD sublayer. In general, these are specified by primitives from the PLCP, and the transmit PMD entity provides the actual means by which the signals required by the PLCP primitives are imposed onto the medium.

14.6.14.1 Nominal transmit power

The nominal transmit power of a frame is defined as the power averaged between the start of the first symbol in the PLCP Header to the end of the last symbol in the PLCP Header. When in the transmit state, the transmit power shall be within 2 dB of the nominal transmit power from the start of the Preamble SYNC field to the last symbol at the end of the frame.

14.6.14.2 Transmit power levels

Unless governed by more stringent local geographic regulations, the radiated emissions from compliant devices shall meet IEEE Std C95.1-1991 limits for controlled or uncontrolled environments, in accordance with their intended usage. In addition, all conformant PMD implementations shall support at least one power level with a minimum equivalent isotropically radiated power (EIRP) of 10 mW.

14.6.14.3 Transmit power level control

If a conformant PMD implementation has the ability to transmit in a manner that results in the EIRP of the transmit signal exceeding the level of 100 mW, at least one level of transmit power control shall be implemented. This transmit power control shall be such that the level of the emission is reduced to a level at or below 100 mW under the influence of said power control.

14.6.14.4 Transmit spectrum shape

Within the operational frequency band the transmitter shall pass a spectrum mask test. The duty cycle between Tx and Rx is nominally 50% and the transmit frame length is nominally 400 μ s. The adjacent channel power is defined as the sum of the power measured in a 1 MHz band. For a pseudorandom data pattern, the adjacent channel power shall be a function of the offset between channel number N and the assigned transmitter channel M, where M is the actual transmitted center frequency and N a channel separated from it by an integer number of MHz.

Channel offset:

$|N-M|=2$ -20 dBm or -40 dBc, whichever is the lower power.

$|N-M|\geq 3$ -40 dBm or -60 dBc, whichever is the lower power.

The levels given in dBc are measured relative to the transmitter power measured in a 1 MHz channel centered on the transmitter center frequency. The adjacent channel power and the transmitter power for this subclause of the specification shall be measured with a resolution bandwidth of 100 kHz, a video bandwidth of 300 kHz, and a peak detector, and with the measurement device set to maximum hold.

For any transmit center frequency M, two exceptions to the spectrum mask requirements are permitted within the operational frequency band, provided the exceptions are less than -50 dBc, where each offset channel exceeded counts as a separate exception. An exception occurs when the total energy within a given 1 MHz channel as defined in 14.6.5 exceeds the levels specified above.

14.6.14.5 Transmit center frequency tolerance

The PMD transmit center frequency shall be within ± 60 kHz of the nominal center frequency as specified in 14.6.5.

14.6.14.6 Transmitter ramp periods

The transmitter shall go from off to within 2 dB of the nominal transmit power in 8 μ s or less. The transmitter shall go from within 2 dB of the nominal transmit power to off (less than -50 dBm) in 8 μ s or less.

14.6.15 PMD receiver specifications

The following portion of this subclause describes the receive functions and parameters associated with the PMD sublayer. In general, these are specified by primitives from the PLCP. The Receive PMD entity provides the actual means by which the signals required by the PLCP primitives are recovered from the medium. The PMD sublayer monitors signals on the medium and will return symbols from the set $\{\{1\},\{0\}\}$ to the PLCP sublayer.

14.6.15.1 Input signal range

The PMD shall be capable of recovering a conformant PMD signal from the medium, as described in related subclauses, with a frame error ratio (FER) $\leq 3\%$ for PSDUs of 400 octets generated with pseudorandom data, for receiver input signal levels in the range from -20 dBm to the receiver sensitivity (as specified in 14.6.15.4), across the frequency band of operation.

14.6.15.2 Receive center frequency acceptance range

An IEEE 802.11 FHSS compliant PMD shall meet all specifications with an input signal having a center frequency range of ± 60 kHz from nominal.

14.6.15.3 CCA power threshold

In the presence of any IEEE 802.11-compliant 1 Mbit/s FH PMD signal above -85 dBm that starts synchronously with respect to slot times as specified in 14.3.3.2.1, the PHY shall signal busy, with a 90% probability of detection, during the preamble within the CCA assessment window. In the presence of any IEEE 802.11-compliant 1 Mbit/s FH PMD signal above -85 dBm that starts asynchronously with respect to slot times as specified in 14.3.3.2.1, the PHY shall signal busy, with a 70% probability of detection, during the preamble within the CCA window. In the presence of any IEEE 802.11 compliant 1 Mbit/s FH PMD signal above -65 dBm, the PHY shall signal busy, with a 70% probability of detection, during random data within the CCA window. This specification applies to a PMD operating with a nominal EIRP of < 100 mW. A compliant PMD operating at a nominal output power greater than 100 mW shall use the following equation to define the CCA threshold, where P_t represents transmit power.

$$\text{CCA threshold (preamble)} = -85 \text{ dBm} - \left[5 \times \log_{10} \left(\frac{P_t}{100 \text{ mW}} \right) \right] \text{ dBm}$$

$$\text{CCA threshold (random data)} = \text{CCA threshold (preamble)} + 20 \text{ dB}$$

14.6.15.4 Receiver sensitivity

The sensitivity is defined as the minimum signal level required for an FER of 3% for PSDUs of 400 octets generated with pseudorandom data. The sensitivity shall be less than or equal to -80 dBm. The reference sensitivity is defined as -80 dBm for the 1 Mbit/s FH PHY specifications.

14.6.15.5 Intermodulation

Intermodulation protection (IMp) is defined as the ratio of the minimum amplitude of one of two equal interfering signals to the desired signal amplitude, where the interfering signals are spaced 4 and 8 MHz removed from the center frequency of the desired signal, both on the same side of center frequency. The IMp protection ratio is established at the interfering signal level that causes the FER of the receiver to be increased to 3% for PSDUs of 400 octets generated with pseudorandom data, when the desired signal is -77 dBm. Each interfering signal is modulated with the FH PMD modulation uncorrelated in time to each other or the desired signal. The PMD shall have the IMp for the interfering signal at 4 and 8 MHz be ≥ 30 dB.

14.6.15.6 Desensitization

Desensitization (Dp) is defined as the ratio to measured sensitivity of the minimum amplitude of an interfering signal that causes the FER at the output of the receiver to be increased to 3% for PSDUs of 400 octets generated with pseudorandom data, when the desired signal is -77 dBm. The interfering signal shall be modulated with the FHSS PMD modulation uncorrelated in time to the desired signal. The minimum Dp shall be as given in Table 46. The spectral purity of the interferer shall be sufficient to ensure that the measurement is limited by the receiver performance.

Table 46—1 Mbit/s Dp

Interferer frequency ^a	Dp minimum
$M = N \pm 2$	30 dB
$M = N \pm 3$ or more	40 dB

^aWhere M is the interferer frequency and N is the desired channel frequency.

14.6.15.7 Receiver radiation

The signal leakage when receiving shall not exceed -50 dBm EIRP in the operating frequency range.

14.6.16 Operating temperature range

Two temperature ranges for full operation compliance to the FH PHY are specified. Type 1 is defined as 0 °C to 40 °C and is designated for office environments. Type 2 is defined as -30 °C to $+70$ °C and is designated for industrial environments.

14.7 FHSS PMD sublayer, 2.0 Mbit/s

14.7.1 Overview

This subclause details the RF specification differences of the optional 2 Mbit/s operation from the baseline 1 Mbit/s PMD as contained in 14.6. Unless otherwise specified in this subclause, the compliant PMD shall also meet all requirements of 14.6 when transmitting at 2 Mbit/s. When implementing the 2 Mbit/s option, the preamble and PHY Header shall be transmitted at 1 Mbit/s. STAs implementing the 2 Mbit/s option shall also be capable of transmitting and receiving PPDU's at 1 Mbit/s.

14.7.2 Four-Level GFSK modulation

For an FHSS 2 Mbit/s PMD, the modulation scheme shall be four-level Gaussian frequency shift keying (4GFSK), with a nominal symbol-period bandwidth product (BT) of 0.5. The four-level deviation factor, defined as the frequency separation of adjacent symbols divided by symbol rate, h_4 , shall be related to the deviation factor of the 2GFSK modulation, h_2 , by the following equation:

$$h_4/h_2 = 0.45 \pm 0.01$$

An incoming bit stream at 2 Mbit/s will be converted to 2-bit words or symbols, with a rate of $F_{clk} = 1 \text{ Msymbol/s}$. The first received bit will be encoded as the LMB of the symbol in Table 47. The bits will be encoded into symbols as shown in Table 47.

Table 47—Symbol encoding into carrier deviation

1 Mbit/s, 2GFSK	
Symbol	Carrier deviation
1	$1/2 \times h_2 \times F_{clk}$
0	$-1/2 \times h_2 \times F_{clk}$
2 Mbit/s, 4GFSK	
Symbol	Carrier deviation
10	$3/2 \times h_4 \times F_{clk}$
11	$1/2 \times h_4 \times F_{clk}$
01	$-1/2 \times h_4 \times F_{clk}$
00	$-3/2 \times h_4 \times F_{clk}$
NOTE—These deviation values are measured using the center symbol of 7 consecutive symbols of the same value. The instantaneous deviation will vary due to Gaussian pulse shaping.	

The deviation factor h_2 for 2GFSK (measured as the difference between frequencies measured in the middle of 0000 and 1111 patterns encountered in the SFD, divided by 1 MHz) will nominally be 0.32. The deviation factor h_2 will be no less than 0.30 (with maximum dictated by regulatory bandwidth requirement). Accordingly, h_4 (measured as a difference between the outermost frequencies, divided by 3, divided by 1 MHz) is nominally $0.45 \times 0.32 = 0.144$, and it will be no less than $0.45 \times 0.3 = 0.135$.

The modulation error shall be less than $\pm 15 \text{ kHz}$ at the midsymbol time for 4GFSK, from the frequency deviations specified above, for a symbol surrounded by identical symbols, and less than $\pm 25 \text{ kHz}$ for any symbol. The deviation is relative to the actual center frequency of the RF carrier. For definition purposes, the actual center frequency is the midfrequency between symbols 11 and 01. The actual center frequency shall be within $\pm 60 \text{ kHz}$ of the nominal channel center frequency defined in 14.6.5 and shall not vary by more than $\pm 10 \text{ kHz/ms}$, from the start to end of the PPDU. The peak-to-peak variation of the actual center frequency over the PPDU shall not exceed 15 kHz. Symbols and terms used within this subclause are illustrated in Figure 84.

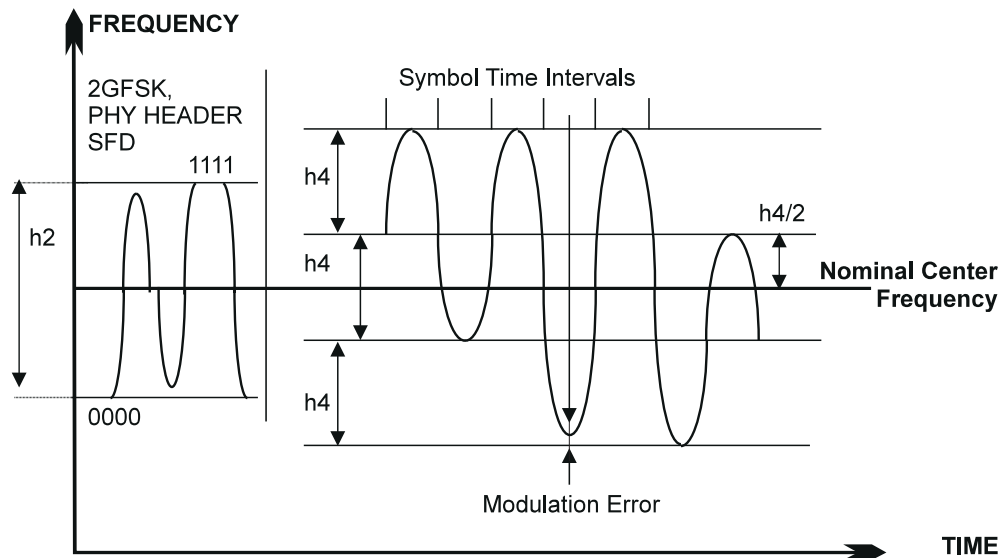


Figure 84—Four-Level GFSK transmit modulation

14.7.2.1 Frame structure for HS FHSS PHY

The high rate FHSS PPDU consists of PLCP Preamble, PLCP Header, and whitened PSDU. The PLCP Preamble and PLCP Header format are identical to 1 Mbit/s PHY, as described in 14.3.2. The whitened PSDU is transmitted in 2GFSK, 4GFSK, or potentially a higher-rate format, according to the rate chosen. The rate is indicated in a 3-bit field in a PLCP Header, having a value of 1 or 2 bits/symbol (or Mbit/s).

The PPDU is transmitted as four-level symbols, with the amount determined by $\text{number_of_symbols} = (\text{number_of_PSDU_octets} \times 8) / \text{rate}$.

The input bits are scrambled according to the method in 14.3.2.3.

The scrambled bit stream is divided into groups of rate (1 or 2) consecutive bits. The bits are mapped into symbols according to Table 47.

A bias suppression algorithm is applied to the resulting symbol stream. The bias suppression algorithm is defined in 14.3.2.3, Figure 71, and Figure 74. A polarity control symbol is inserted prior to each block of 32 symbols (or less for the last block). The polarity control signals are 4GFSK symbols 10 or 00. The algorithm is equivalent to the case of 2GFSK, with the polarity symbol 2GFSK “1” replaced with 4GFSK symbol “10,” and the 2GFSK polarity symbol “0” replaced with a 4GFSK symbol “00.”

14.7.3 Channel data rate

The data rate for the whitened PSDU at the optional rate shall be 2.0 Mbit/s \pm 50 ppm.

14.7.3.1 Input dynamic range

The PMD shall be capable of recovering a conformant PMD signal from the medium, as described in related subclauses, with an FER $\leq 3\%$ for PSDUs of 400 octets generated with pseudorandom data, for receiver input signal levels in the range from -20 dBm to the receiver sensitivity (as specified in 14.7.3.2), across the frequency band of operation.

14.7.3.2 Receiver sensitivity

The sensitivity is defined as the minimum signal level required for an FER of 3% for PSDUs of 400 octets generated with pseudorandom data. The sensitivity shall be less than or equal to -75 dBm. The reference sensitivity is defined as -75 dBm for the 2 Mbit/s FH PHY specifications.

14.7.3.3 IMp

IMp is defined as the ratio to -77 dBm of the minimum amplitude of one of the two equal level interfering signals at 4 and 8 MHz removed from center frequency, both on the same side of center frequency, that cause the FER of the receiver to be increased to 3% for PSDUs of 400 octets generated with pseudorandom data, when the desired signal is -72 dBm (3 dB above the specified sensitivity specified in 14.7.3.2). Each interfering signal is modulated with the FH 1 Mbit/s PMD modulation uncorrelated in time to each other or the desired signal. The FHSS optional 2 Mbit/s rate IMp shall be ≥ 25 dB.

14.7.3.4 Dp

Dp is defined as the ratio to measured sensitivity of the minimum amplitude of an interfering signal that causes the FER of the receiver to be increased to 3% for PSDUs of 400 octets generated with pseudorandom data, when the desired signal is -72 dB (3 dB above sensitivity specified in 14.7.3.2). The interfering signal shall be modulated with the FHSS PMD modulation uncorrelated in time to the desired signal. The minimum Dp shall be as given in Table 48.

Table 48—2 Mbit/s Dp

Interferer frequency ^a	DP minimum
$M = N \pm 2$	20 dB
$M = N \pm 3$ or more	30 dB

^aWhere M is the interferer frequency and N is the desired channel frequency.

14.8 FHSS PHY management information base (MIB)

14.8.1 Overview

The following is the MIB for the FHSS PHY.

14.8.2 FH PHY attributes

This subclause defines the attributes for the FHSS MIB. Table 49 lists these attributes and the default values. Following the table is a description of each attribute.

Table 49—FHSS PHY attributes

Attribute	Default value	Operational semantics	Operational behavior
aPHYType	FHSS = X'01'	Static	Identical for all FH PHYs
aRegDomainsSupported	FCC = X'10' IC = X'20' ETSI = X'30' Spain = X'31' France = X'32' MKK = X'40'	Static	Implementation dependent
aCurrentRegDomain	X'00'	Dynamic LME	Implementation dependent
aSlotTime	50 μ s	Static	Identical for all FH PHYs
aCCATime	27 μ s	Static	Identical for all FH PHYs
aRxTxTurnaroundTime	20 μ s	Static	Identical for all FH PHYs
aTxPLCPDelay	1 μ s	Static	Identical for all FH PHYs
aRxTxSwitchTime	10 μ s	Static	Identical for all FH PHYs
aTxRampOnTime	8 μ s	Static	Identical for all FH PHYs
aTxRFDelay	1 μ s	Static	Identical for all FH PHYs
aSIFSTime	28 μ s	Static	Identical for all FH PHYs
aRxRFDelay	4 μ s	Static	Identical for all FH PHYs
aRxPLCPDelay	2 μ s	Static	Identical for all FH PHYs
aMACProcessingDelay	2 μ s	Static	Identical for all FH PHYs
aTxRampOffTime	8 μ s	Static	Identical for all FH PHYs
aPreambleLength	96 μ s	Static	Identical for all FH PHYs
aPLCPHdrLength	32 μ s	Static	Identical for all FH PHYs
aMPDUDurationFactor	1.03125	Static	Identical for all FH PHYs
aAirPropagationTime	1 μ s	Static	Identical for all FH PHYs
aTempType	Type 1 = X'01' Type 2 = X'02' Type 3 = X'03'	Static	Implementation dependent
aCWmin	15	Static	Identical for all FH PHYs
aCWmax	1023	Static	Identical for all FH PHYs
aSupportedDataRatesTX	1 Mbit/s = X'02' mandatory 2 Mbit/s = X'04' optional	Static	Identical for all FH PHYs
aSupportedDataRatesRX	1 Mbit/s = X'02' mandatory 2 Mbit/s = X'04' optional	Static	Identical for all FH PHYs
aMPDUMaxLength	4095 octets	Static	Identical for all FH PHYs
aSupportedTxAntennas	Ant 1 = X'01' Ant 2 = X'02' Ant 3 = X'03' Ant n = n	Static	Implementation dependent
aCurrentTxAntenna	Ant 1 = default	Dynamic LME	Implementation dependent

Table 49—FHSS PHY attributes (continued)

Attribute	Default value	Operational semantics	Operational behavior
aSupportedRxAntennas	Ant 1 = X'01' Ant 2 = X'02' Ant 3 = X'03' Ant n = n	Static	Implementation dependent
aDiversitySupport	Available = X'01' Not avail. = X'02' Control avail. = X'03'	Static	Implementation dependent
aDiversitySelectionRx	Ant 1 = X'01' Ant 2 = X'02' Ant 3 = X'03' Ant 4 = X'04' Ant 5 = X'05' Ant 6 = X'06' Ant 7 = X'07' Ant 8 = X'08'	Dynamic LME	Implementation dependent
aNumberSupportedPowerLevels	Lvl1 = X'01' Lvl2 = X'02' Lvl3 = X'03' Lvl4 = X'04' Lvl5 = X'05' Lvl6 = X'06' Lvl7 = X'07' Lvl8 = X'08'	Static	Implementation dependent
aTxPowerLevel1	Factory def. default	Static	Implementation dependent
aTxPowerLevel2	Factory def.	Static	Implementation dependent
aTxPowerLevel3	Factory def.	Static	Implementation dependent
aTxPowerLevel4	Factory def.	Static	Implementation dependent
aTxPowerLevel5	Factory def.	Static	Implementation dependent
aTxPowerLevel6	Factory def.	Static	Implementation dependent
aTxPowerLevel7	Factory def.	Static	Implementation dependent
aTxPowerLevel8	Factory def.	Static	Implementation dependent
aCurrentTxPowerLevel	TxPowerLevel1	Dynamic LME	Implementation dependent
aHopTime	224 μ s	Static	Identical for all FH PHYs
aCurrentChannelNumber	X'00'	Dynamic PLME	
aMaxDwellTime	390 TU	Static	Regulatory domain dependent
aCurrentSet	X'00'	Dynamic PLME	
aCurrentPattern	X'00'	Dynamic PLME	
aCurrentIndex	X'00'	Dynamic PLME	
aCurrentPowerState	X'01' off X'02' on	Dynamic LME	
NOTE—The column titled “Operational semantics” contains two types: static and dynamic. Static MIB attributes are fixed and cannot be modified for a given PHY implementation. MIB attributes defined as dynamic can be modified by some management entity. Whenever an attribute is defined as dynamic, the column also shows which entity has control over the attribute. LME refers to the MAC sublayer management entity (MLME), while PHY refers to the physical layer management entity (PLME).			

14.8.2.1 FH PHY attribute definitions

14.8.2.1.1 aPHYType

The aPHYType is FHSS. The LME uses this attribute to determine what PLCP and PMD is providing services to the MAC. It also is used by the MAC to determine what MAC sublayer management state machines must be invoke to support the PHY. The value of this attribute is defined as the integer 01 to indicate the FHSS PHY.

14.8.2.1.2 aRegDomainsSupported

Operational requirements for FHSS PHY are defined by agencies representing certain geographical regulatory domains. These regulatory agencies may define limits on various parameters that differ from region to region. This parameters may include aTxPowerLevels, and aMaxDwellTime, as well as the total number of frequencies in the hopping pattern. The values shown in Table 50 indicate regulatory agencies supported by this document.

Table 50—Regulatory domain codes

Code point	Regulatory agency	Region
X'10'	FCC	United States
X'20'	IC	Canada
X'30'	ETSI	Most of Europe
X'31'	Spain	Spain
X'32'	France	France
X'40'	MKK	Japan
X'00'	Null terminator	—

Since a PLCP and PMD might be designed to support operation in more than one regulatory domain, this attribute can actually represent a list of agencies. This list can be one or more of the above agencies and must be terminated using the null terminator. Upon activation of the PLCP and PMD, the information in this list must be used to set the value of the aCurrentRegDomain attribute.

14.8.2.1.3 aCurrentRegDomain

The aCurrentRegDomain attribute for the FHSS PHY is defined as the regulatory domain under which the PMD is currently operating. This value must be one of the values listed in the aRegDomainsSupported list. This MIB attribute is managed by the LME.

14.8.2.1.4 aSlotTime

The aSlotTime is a PHY dependent attribute used by the MAC sublayer to determine the PIFS and DIFS periods. It is defined using the following equation:

$$aCCATime + aRxTxTurnaroundTime + aAirPropagationTime + aMACProcessingDelay$$

For the FHSS PHY, the aCCATime is 27 μ s, and the aRxTxTurnaroundTime is 20 μ s. The aAirPropagationTime is fixed at 1 μ s. The aMACProcessingDelay is nominally 2 μ s. The value of this attribute is 50 μ s.

14.8.2.1.5 aCCATime

The aCCATime for the FHSS PHY is defined as the time the receiver must use to evaluate the medium at the antenna to determine the state of the channel. This time period for the FHSS PHY is 27 μs . This period includes the aRxRFDelay and the aRxPLCPDelay.

14.8.2.1.6 aRxTxTurnaround Time

The aRxTxTurnaroundTime for the FHSS PHY is defined as the time it takes a STA to place a valid symbol on the medium after a PHY-TXSTART.request. The aRxTxTurnaroundTime is determined using the following equation.

$$\text{aTxPLCPDelay} + \text{aRxTxSwitchTime} + \text{aTxRampOnTime} + \text{aTxRFDelay}$$

For the FHSS PHY, the aTxPLCPDelay is 1 μs , the aRxTxSwitchTime is 10 μs , the aTxRampOnTime is 8 μs , and the aTxRFDelay is 1 μs , for a total of 20 μs . This is the maximum time for getting valid data on the medium. STAs can use less time but not more than 20 μs .

14.8.2.1.7 aTxPLCPDelay

The aTxPLCPDelay for the FHSS PHY is defined as the delay the PLCP introduces in getting data onto the air in the transmit direction. This value for the FHSS PHY is nominally 1 μs . Implementors may choose to increase or decrease this delay as long as the requirements of aRxTxTurnaroundTime are met.

14.8.2.1.8 aRxTxSwitchTime

The aRxTxSwitchTime for the FHSS PHY is defined as the delay the PMD requires to change from receive to transmit. This value for the FHSS PHY is nominally 10 μs . Implementors may choose to increase or decrease this delay as long as the requirements of aRxTxTurnaroundTime are met.

14.8.2.1.9 aTxRampOnTime

The aTxRampOnTime for the FHSS PHY is defined as the delay the PMD requires to turn on the transmit power amplifier. This value for the FHSS PHY is nominally 8 μs . Implementors may choose to increase or decrease this delay as long as the requirements of aRxTxTurnaroundTime are met.

14.8.2.1.10 aTxRFDelay

The aTxRFDelay for the FHSS PHY is defined as the nominal time in microseconds between the issuance of a PMDDATA.request to the PMD and the start of the corresponding symbol at the air interface. The start of a symbol is defined to be 1/2 symbol period prior to the center of the symbol. This value for the FHSS PHY is nominally 1 μs . Implementors may choose to increase or decrease this delay as long as the requirements of aRxTxTurnaroundTime are met.

14.8.2.1.11 aSIFSTime

The aSIFSTime for the FHSS PHY is defined as the time the MAC and PHY sublayers will require to receive the last symbol of a frame at the air interface, process the frame, and respond with the first symbol of a preamble on the air interface. The aSIFSTime is determined using the following equation:

$$\text{aRxRFDelay} + \text{aRxPLCPDelay} + \text{aMACProcessingDelay} + \text{aRxTxTurnaroundTime}$$

For the FHSS PHY, the aRxRFDelay is 4 μ s, the aRxPLCPDelay is 2 μ s, the aMACProcessingDelay is 2 μ s, and the aRxTxTurnaroundTime is 20 μ s, for a total of 28 μ s. This is the nominal value for aSIFSTime. In order to account for variations between implementations, this value has a tolerance as specified in 9.2.3.1.

14.8.2.1.12 aRxRFDelay

The aRxRFDelay for the FHSS PHY is defined as the nominal time in microseconds between the end of a symbol at the air interface to the issuance of a PMDDATA.indicate to the PLCP. The end of a symbol is defined to be 1/2 symbol period after the center of the symbol. This value for the FHSS PHY is nominally 4 μ s. Implementors may choose to increase or decrease this delay as long as the requirements of aSIFSTime and aCCATime are met.

14.8.2.1.13 aRxPLCPDelay

The aRxPLCPDelay for the FHSS PHY is defined as the delay the PLCP introduces in the data path between the PMD and the MAC sublayer. This value for the FHSS PHY is nominally 2 μ s. Implementors may choose to increase or decrease this delay as long as the requirements of aSIFSTime and aCCATime are met.

14.8.2.1.14 aMACProcessingDelay

The aMACProcessingDelay for the FHSS PHY is defined as the delay between when a PHY-RXEND.indicate is issued by the PHY till a corresponding PHY-TXSTART.request is issued by the MAC. This value for the FHSS PHY is nominally 2 μ s. Implementors may choose to increase or decrease this delay as long as the requirements of aSIFSTime are met.

14.8.2.1.15 aTxRampOffTime

The aTxRampOffTime for the FHSS PHY is defined as the delay the PMD requires to turn off the transmit power amplifier. This value for the FHSS PHY is a maximum of 8 μ s.

14.8.2.1.16 aPreambleLength

The parameter aPreambleLength defines the time required by the FHSS PHY to transmit the PLCP Preamble. For both the 1 and 2 Mbit/s FHSS PHYs, this value is 96 μ s.

14.8.2.1.17 aPLCPHdrLength

The parameter aPLCPHdrLength defines the time required by the FHSS PHY to transmit the PLCP Header. For both the 1 and 2 Mbit/s FHSS PHYs, this value is 32 μ s.

14.8.2.1.18 aMPDUDurationFactor

The parameter aMPDUDurationFactor defines the overhead added by the PHY to the PSDU as it is transmitted over the air. For the FHSS PHY, this factor is 1.03125. This factor is calculated as 33/32 to account for the expansion due to the data whitener encoding algorithm. The total time to transmit a PPDU over the air is the following equation rounded up to the next integer microsecond:

$$\text{aPreambleLength} + \text{aPLCPHdrLength} + \text{aMPDUDurationFactor} \times 8 \times \text{PSDU length (octets)/data rate}$$

The total time in microseconds to the beginning of any octet in an PPDU from the first symbol of the preamble can be calculated using the duration factor in the following equation:

$$\text{Truncate}[\text{aPreambleLength} + \text{aPLCPHdrLength} + \text{aMPDUDurationFactor} \times 8 \times N/\text{data rate}] + 1$$

where N is the number of octets prior to the desired octet.

14.8.2.1.19 aAirPropagationTime

The parameter aAirPropagationTime is the time it takes a transmitted signal to go from the transmitting STA to the receiving STA. A nominal value of 1 μ s has been allocated for this parameter. Variations in the actual propagation time are accounted for in the allowable range of aSIFSTime.

14.8.2.1.20 aTempType

The parameter aTempType defines the temperature range supported by the PHY. Type 1 equipment (X'01') supports a temperature range of 0 °C to 40 °C. Type 2 equipment (X'02') supports a temperature range of –20 °C to +55 °C. Type 3 equipment (X'03') supports a temperature range of –30 °C to +70 °C.

14.8.2.1.21 aCWmin

The parameter aCWmin defines the minimum size of the contention window, in slots. For the FH PHY, this number is 15 decimal.

14.8.2.1.22 aCWmax

The parameter aCWmax defines the maximum size of the contention window, in slots. For the FH PHY, this number is 1023 decimal.

14.8.2.1.23 aCurrentPowerState

The aCurrentPowerState attribute for the FHSS PHY allows the MAC sublayer management entity to control the power state of the PHY. This attribute can be updated using the PLMESET.request. The permissible values are ON and OFF.

14.8.2.1.24 aSupportedDataRatesTX

The aSupportedDataRatesTX attribute for the FHSS PHY is defined as a null terminated list of supported data rates in the transmit mode for this implementation. Table 51 shows the possible values appearing in the list.

Table 51 – Supported data rate codes (aSupportedDataRatesTX)

Code point	Data rate
X'02'	1 Mbit/s
X'04'	2 Mbit/s
X'00'	Null terminator

14.8.2.1.25 aSupportedDataRatesRX

The aSupportedDataRatesRX attribute for the FHSS PHY is defined as a null terminated list of supported data rates in the receive mode for this implementation. Table 52 shows the possible values appearing in the list.

Table 52—Supported data rate codes (aSupportedDataRatesRX)

Code point	Data rate
X'02'	1 Mbit/s
X'04'	2 Mbit/s
X'00'	Null terminator

14.8.2.1.26 aMPDUMaxLength

The aMPDUMaximumLength attribute for the FHSS PHY is defined as the maximum PSDU, in octets, that the PHY shall ever be capable of accepting. This value for the FHSS PHY is set at 4095 octets. The recommended value for maximum PSDU length in an FHSS PHY system is 400 octets at 1 Mbit/s and 800 octets at 2 Mbit/s, which corresponds to a frame duration less than 3.5 ms. These values are optimized to achieve high performance in a variety of RF channel conditions, particularly with respect to indoor multipath, channel stability for moving STAs, and interference in the 2.4 GHz band.

14.8.2.1.27 aSupportedTxAntennas

The aSupportedTxAntennas attribute for the FHSS PHY is defined as a null terminated list of antennas that this implementation can use to transmit data. Table 53 shows the possible values appearing in the list, where $N \leq 255$.

Table 53—Number of transmit antennas

Code point	Antenna number
X'01'	Tx Antenna 1
X'02'	Tx Antenna 2
X'03'	Tx Antenna 3
...	...
N	Tx Antenna N
X'00'	Null terminator

14.8.2.1.28 aCurrentTxAntenna

The CurrentTxAntenna attribute for the FHSS PHY is used to describe the current antenna the implementation is using for transmission. This value should represent one of the antennas appearing in the SupportedTxAntennas list.

14.8.2.1.29 aSupportedRxAntenna

The aSupportedRxAntennas attribute for the FHSS PHY is defined as a null terminated list of antennas that this implementation can use to receive data. In the FHSS PHY primitives, one of these values is passed as part of

the PHY-RXSTART.indicate to the MAC sublayer for every received packet. Table 54 shows the possible values appearing in the list, where $N \leq 255$.

Table 54—Number of receive antennas

Code point	Antenna number
X'01'	Rx Antenna 1
X'02'	Rx Antenna 2
X'03'	Rx Antenna 3
...	...
N	Rx Antenna N
X'00'	Null terminator

14.8.2.1.30 aDiversitySupport

The aDiversitySupport attribute for the FHSS PHY is used to describe the implementation's diversity support. Table 55 shows the possible values appearing in the list.

Table 55—Diversity support codes

Code point	Diversity support
X'01'	Diversity available
X'02'	No diversity
X'03'	Control available

The value X'01' indicates that this implementation uses two or more antennas for diversity. The value X'02' indicates that the implementation has no diversity support. The value X'03' indicates that the choice of antennas used during diversity is programmable. (See 14.8.2.1.31.)

14.8.2.1.31 aDiversitySelectionRx

The aDiversitySelectionRx attribute for the FHSS PHY is a null terminated list describing the receive antenna or antennas currently in use during diversity and packet reception. Table 56 below shows the possible values appearing in the list, where $N \leq 255$.

Table 56—Diversity select antenna codes

Code point	Antenna number
X'01'	Rx Antenna 1
X'02'	Rx Antenna 2
X'03'	Rx Antenna 3
N	Rx Antenna N
X'00'	Null terminator

The null terminated list can consist of one or more of the receive antennas listed in the `aSupportedRxAntennas` attribute. This attribute can be changed dynamically by the LME.

14.8.2.1.32 `aNumberSupportedPowerLevels`

The `aNumberSupportedPowerLevels` attribute for the FHSS PHY describes the number of power levels this implementation supports. This attribute can be an integer of value 1 through 8, inclusive.

14.8.2.1.33 `aTxPowerLevel1-8`

Some implementations may provide up to eight different transmit power levels. The `aTxPowerLevels` attribute for the FHSS PHY is a list of up to eight power levels supported. Table 57 describes the list.

Table 57—Transmit power levels

Attribute	Power level
<code>TxPowerLevel1</code>	Default setting
<code>TxPowerLevel2</code>	Level 2
<code>TxPowerLevel3</code>	Level 3
<code>TxPowerLevel4</code>	Level 4
<code>TxPowerLevel5</code>	Level 5
<code>TxPowerLevel6</code>	Level 6
<code>TxPowerLevel7</code>	Level 7
<code>TxPowerLevel8</code>	Level 8

14.8.2.1.34 `aCurrentTxPowerLevel`

The `aCurrentTxPowerLevel` attribute for the FHSS PHY is defined as the current transmit output power level. This level shall be one of the levels implemented in the list of attributes called `aTxPowerLevelN` (where N is 1–8). This MIB attribute is also used to define the sensitivity of the CCA mechanism when the output power exceeds 100 mW. This MIB attribute is managed by the LME.

14.8.2.1.35 `aHopTime`

The `aHopTime` attribute for the FHSS PHY describes the time allocated for the PHY to change to a new frequency. For the FHSS PHY, this time period is 224 μ s.

14.8.2.1.36 `aCurrentChannelNumber`

The `aCurrentChannelNumber` attribute for the FHSS PHY is defined as the current operating channel number of the PMD. The values of this attribute correspond to the values shown in Table 38. This MIB attribute is managed by the PLME and is updated as the result of a `PLMESET.request` to `aCurrentSet`, `aCurrentPattern`, or `aCurrentIndex`.

14.8.2.1.37 `aMaxDwellTime`

The `aMaxDwellTime` attribute for the FHSS PHY is defined as the maximum time the PMD can dwell on a channel and meet the requirements of the current regulatory domain. For the FCC regulatory domain, this number is 390 TU (FCC = 400 ms). The recommended dwell time for the FHSS PHY is 19 TU.

14.8.2.1.38 aCurrentSet

The FHSS PHY contains three sets of hopping patterns. The aCurrentSet attribute for the FHSS PHY defines what set the STA is using to determine the hopping pattern. Its value can be 1, 2, or 3. This attribute is managed by the PLME.

14.8.2.1.39 aCurrentPattern

There are up to 26 patterns in each hopping set used by the FHSS PHY. The aCurrentPattern attribute for the FHSS PHY defines what pattern the STA is using to determine the hopping sequence. Its value has various ranges, always within the overall range of 1 to 26, depending on the aCurrentRegDomain. This attribute is managed by the PLME.

14.8.2.1.40 aCurrentIndex

The FHSS PHY addresses each channel in the selected hopping pattern through an index. The aCurrentIndex attribute for the FHSS PHY defines what index the STA will use to determine the next hop-channel number. Its value has various ranges, always within the overall range of 1 to 26, depending on the aCurrentRegDomain. This attribute is managed by the PLME.

14.8.2.1.41 aCurrentPowerState

The parameter aCurrentPowerState defines the operational state of the FHSS PHY. When this attribute has a value of X'01', the PHY is "OFF." When this attribute has a value of X'02', the PHY is "ON." This attribute is managed by the PLME.

15. Direct sequence spread spectrum (DSSS) PHY specification for the 2.4 GHz band designated for ISM applications

15.1 Overview

The PHY for the direct sequence spread spectrum (DSSS) system is described in this clause. The RF LAN system is initially aimed for the 2.4 GHz band designated for ISM applications as provided in the USA according to FCC 15.247, in Europe by ETS 300–328, and in other countries according to 15.4.6.2.

The DSSS system provides a wireless LAN with both a 1 Mbit/s and a 2 Mbit/s data payload communication capability. According to the FCC regulations, the DSSS system shall provide a processing gain of at least 10 dB. This shall be accomplished by chipping the baseband signal at 11 MHz with an 11-chip PN code. The DSSS system uses baseband modulations of differential binary phase shift keying (DBPSK) and differential quadrature phase shift keying (DQPSK) to provide the 1 and 2 Mbit/s data rates, respectively.

15.1.1 Scope

The PHY services provided to the IEEE 802.11 wireless LAN MAC by the 2.4 GHz DSSS system are described in this clause. The DSSS PHY layer consists of two protocol functions:

- a) A physical layer convergence function, which adapts the capabilities of the physical medium dependent (PMD) system to the PHY service. This function shall be supported by the physical layer convergence procedure (PLCP), which defines a method of mapping the IEEE 802.11 MAC sublayer protocol data units (MPDU) into a framing format suitable for sending and receiving user data and management information between two or more STAs using the associated PMD system.
- b) A PMD system, whose function defines the characteristics of, and method of transmitting and receiving data through, a wireless medium (WM) between two or more STAs each using the DSSS system.

15.1.2 DSSS PHY functions

The 2.4 GHz DSSS PHY architecture is depicted in the reference model shown in Figure 11. The DSSS PHY contains three functional entities: the PMD function, the physical layer convergence function, and the layer management function. Each of these functions is described in detail in the following subclauses.

The DSSS PHY service shall be provided to the MAC through the PHY service primitives described in Clause 12.

15.1.2.1 PLCP sublayer

To allow the IEEE 802.11 MAC to operate with minimum dependence on the PMD sublayer, a physical layer convergence sublayer is defined. This function simplifies the PHY service interface to the IEEE 802.11 MAC services.

15.1.2.2 PMD sublayer

The PMD sublayer provides a means to send and receive data between two or more STAs. This clause is concerned with the 2.4 GHz ISM bands using direct sequence modulation.

15.1.2.3 Physical layer management entity (PLME)

The PLME performs management of the local PHY functions in conjunction with the MAC management entity.

15.1.3 Service specification method and notation

The models represented by figures and state diagrams are intended to be illustrations of functions provided. It is important to distinguish between a model and a real implementation. The models are optimized for simplicity and clarity of presentation; the actual method of implementation is left to the discretion of the IEEE 802.11 DSSS PHY compliant developer.

The service of a layer or sublayer is a set of capabilities that it offers to a user in the next-higher layer (or sublayer). Abstract services are specified here by describing the service primitives and parameters that characterize each service. This definition is independent of any particular implementation.

15.2 DSSS PLCP sublayer

15.2.1 Overview

This clause provides a convergence procedure in which MPDUs are converted to and from PPDUs. During transmission, the MPDU shall be prepended with a PLCP Preamble and Header to create the PPDU. At the receiver, the PLCP Preamble and header are processed to aid in demodulation and delivery of the MPDU.

15.2.2 PLCP frame format

Figure 85 shows the format for the PPDU including the DSSS PLCP Preamble, the DSSS PLCP Header, and the MPDU. The PLCP Preamble contains the following fields: Synchronization (Sync) and Start Frame Delimiter (SFD). The PLCP Header contains the following fields: IEEE 802.11 Signaling (Signal), IEEE 802.11 Service (Service), LENGTH (Length), and CCITT CRC-16. Each of these fields is described in detail in 15.2.3.

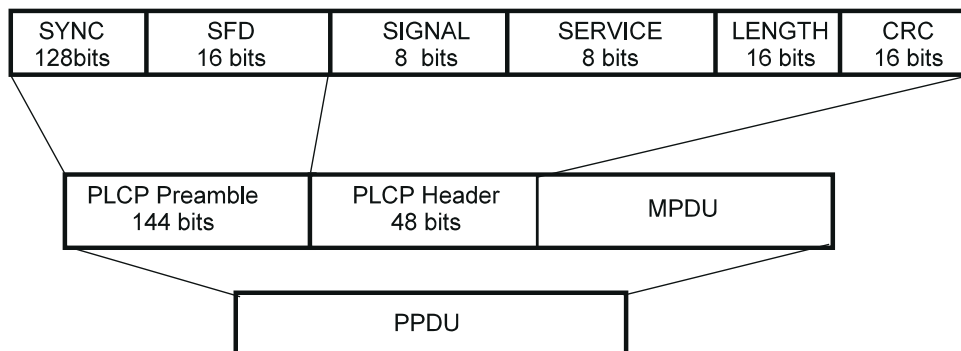


Figure 85—PLCP frame format

15.2.3 PLCP field definitions

The entire PLCP Preamble and Header shall be transmitted using the 1 Mbit/s DBPSK modulation described in 15.4.7. All transmitted bits shall be scrambled using the feedthrough scrambler described in 15.2.4.

15.2.3.1 PLCP Synchronization (SYNC) field

The SYNC field shall consist of 128 bits of scrambled 1 bit. This field shall be provided so that the receiver can perform the necessary operations for synchronization.

15.2.3.2 PLCP Start Frame Delimiter (SFD)

The SFD shall be provided to indicate the start of PHY dependent parameters within the PLCP Preamble. The SFD shall be a 16-bit field, X'F3A0' (msb to lsb). The lsb shall be transmitted first in time.

15.2.3.3 PLCP IEEE 802.11 Signal (SIGNAL) field

The 8-bit IEEE 802.11 signal field indicates to the PHY the modulation that shall be used for transmission (and reception) of the MPDU. The data rate shall be equal to the Signal field value multiplied by 100 kbit/s. The DSSS PHY currently supports two mandatory modulation services given by the following 8-bit words, where the lsb shall be transmitted first in time:

- a) X'0A' (msb to lsb) for 1 Mbit/s DBPSK
- b) X'14' (msb to lsb) for 2 Mbit/s DQPSK

The DSSS PHY rate change capability is described in 15.2.5. This field shall be protected by the CCITT CRC-16 frame check sequence described in 15.2.3.6.

15.2.3.4 PLCP IEEE 802.11 Service (SERVICE) field

The 8-bit IEEE 802.11 service field shall be reserved for future use. The value of X'00' signifies IEEE 802.11 device compliance. The lsb shall be transmitted first in time. This field shall be protected by the CCITT CRC-16 frame check sequence described in 15.2.3.6.

15.2.3.5 PLCP Length (LENGTH) field

The PLCP Length field shall be an unsigned 16-bit integer that indicates the number of microseconds (16 to $2^{16}-1$ as defined by `aMPDUMaxLength`) required to transmit the MPDU. The transmitted value shall be determined from the LENGTH parameter in the TXVECTOR issued with the PHY-TXSTART.request primitive described in 12.3.5.4. The Length field provided in the TXVECTOR is in bytes and is converted to microseconds for inclusion in the PLCP LENGTH field. The lsb shall be transmitted first in time. This field shall be protected by the CCITT CRC-16 frame check sequence described in 15.2.3.6.

15.2.3.6 PLCP CRC (CCITT CRC-16) field

The IEEE 802.11 SIGNAL, IEEE 802.11 SERVICE, and LENGTH fields shall be protected with a CCITT CRC-16 FCS (frame check sequence). The CCITT CRC-16 FCS shall be the one's complement of the remainder generated by the modulo 2 division of the protected PLCP fields by the polynomial:

$$x^{16} + x^{12} + x^5 + 1$$

The protected bits shall be processed in transmit order. All FCS calculations shall be made prior to data scrambling.

As an example, the SIGNAL, SERVICE, and LENGTH fields for a DBPSK signal with a packet length of 192 μ s (24 bytes) would be given by the following:

0101 0000 0000 0000 0000 0011 0000 0000 (leftmost bit transmitted first in time)

The one's complement FCS for these protected PLCP Preamble bits would be the following:

0101 1011 0101 0111 (leftmost bit transmitted first in time)

Figure 86 depicts this example.

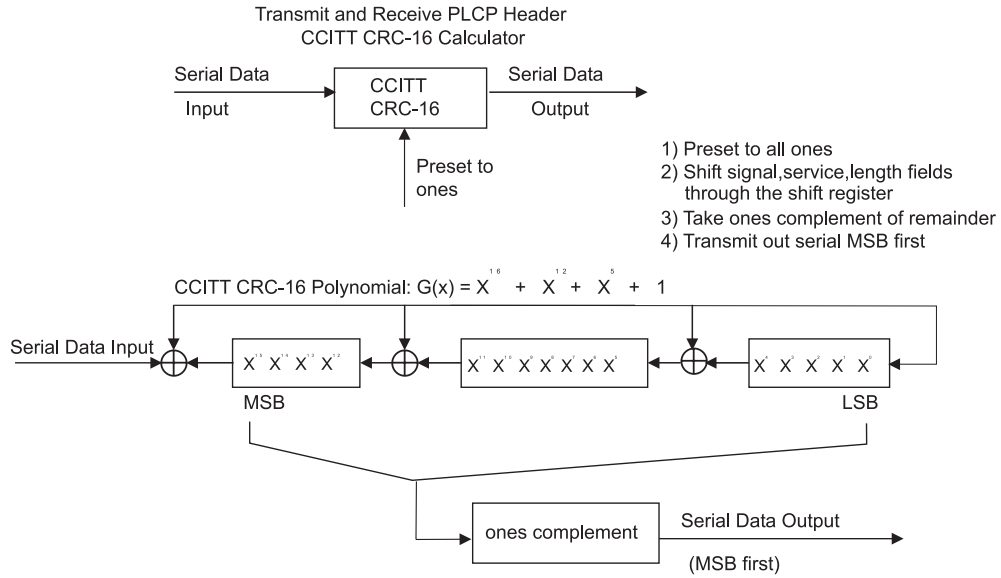


Figure 86—CCITT CRC-16 implementation

An illustrative example of the CCITT CRC-16 FCS using the information from Figure 86 follows in Figure 87.

Data	CRC registers		
	msb	lsb	
	1111111111111111		; initialize preset to 1's
0	1110111111011111		
1	1101111111011110		
0	10101111101011101		
1	0101111010111010		
0	1011110101110100		
0	0110101011001001		
0	1101010110010010		
0	1011101100000101		
0	0110011000101011		
0	1100110001010110		
0	1000100010001101		
0	0000001001110111		
0	0000010011101110		
0	0000010011101100		
0	0001001110110000		
0	0010011101100000		
0	0100111011000000		
0	1001110110000000		
0	0010101100100001		
0	0101011001000010		
0	1010110010000100		
1	0101100100001000		
1	1010001000110001		
0	0101010001000011		
0	1010100010000110		
0	0100000100101101		
0	1000001001011010		
0	0001010010010101		
0	0010100100101010		
0	0101001001010100		
0	1010010010101000		
	0101101101010111		; one's complement, result = CRC FCS parity

Figure 87—Example CRC calculation

15.2.4 PLCP/DSSS PHY data scrambler and descrambler

The polynomial $G(z) = z^{-7} + z^{-4} + 1$ shall be used to scramble *all* bits transmitted by the DSSS PHY. The feedthrough configuration of the scrambler and descrambler is self-synchronizing, which requires no prior knowledge of the transmitter initialization of the scrambler for receive processing. Figure 88 and Figure 89 show typical implementations of the data scrambler and descrambler, but other implementations are possible.

The scrambler should be initialized to any state except all ones when transmitting.

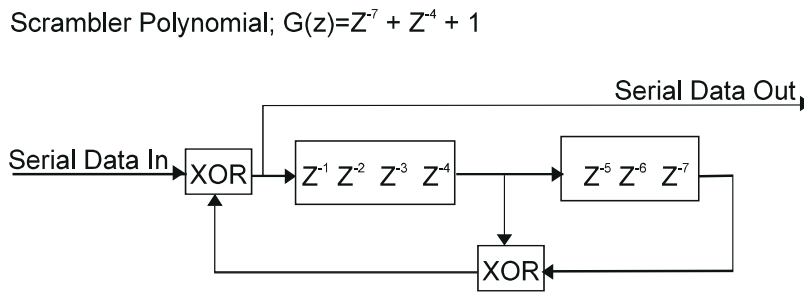


Figure 88—Data scrambler

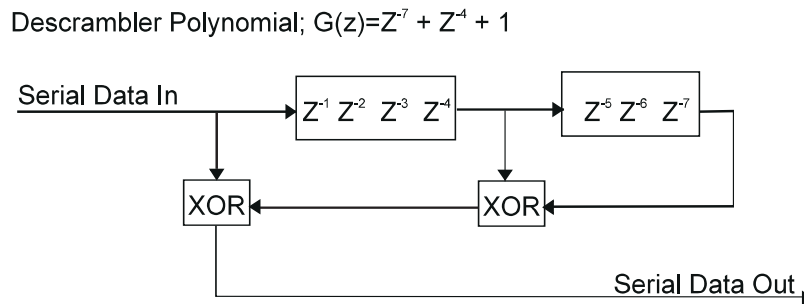


Figure 89—Data descrambler

15.2.5 PLCP data modulation and modulation rate change

The PLCP Preamble shall be transmitted using the 1 Mbit/s DBPSK modulation. The IEEE 802.11 SIGNAL field shall indicate the modulation that shall be used to transmit the MPDU. The transmitter and receiver shall initiate the modulation indicated by the IEEE 802.11 SIGNAL field starting with the first symbol (1 bit for DBPSK or 2 bits for DQPSK) of the MPDU. The MPDU transmission rate shall be set by the DATARATE parameter in the TXVECTOR issued with the PHY-TXSTART.request primitive described in 15.4.4.1.

15.2.6 PLCP transmit procedure

The PLCP transmit procedure is shown in Figure 90.

In order to transmit data, PHY-TXSTART.request shall be enabled so that the PHY entity shall be in the transmit state. Further, the PHY shall be set to operate at the appropriate channel through station management via the PLME. Other transmit parameters such as DATARATE, TX antenna, and TX power are set via the PHY-SAP with the PHY-TXSTART.request(TXVECTOR) as described in 15.4.4.2.

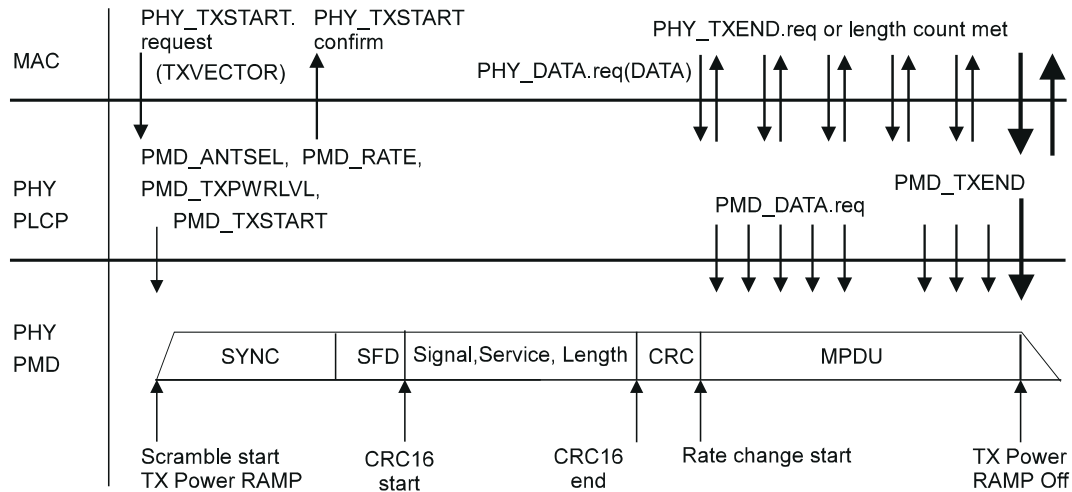


Figure 90—PLCP transmit procedure

Based on the status of clear channel assessment (CCA) indicated by PHY-CCA.indicate, the MAC will assess that the channel is clear. A clear channel shall be indicated by PHY-CCA.indicate(IDLE). If the channel is clear, transmission of the PPDU shall be initiated by issuing the PHY-TXSTART.request (TXVECTOR) primitive. The TXVECTOR elements for the PHY-TXSTART.request are the PLCP Header parameters SIGNAL (DATARATE), SERVICE, and LENGTH, and the PMD parameters of TX_ANTENNA and TX_PWR_LEVEL. The PLCP Header parameter LENGTH is calculated from the TXVECTOR element by multiplying by 8 for 1 Mbit/s and by 4 for 2 Mbit/s.

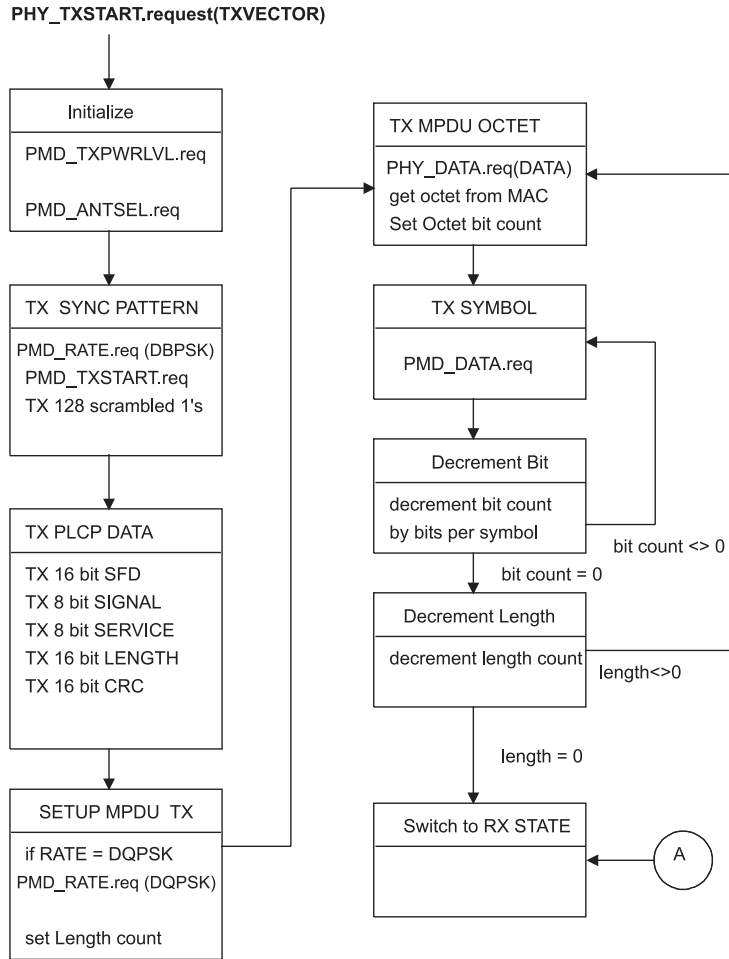
The PLCP shall issue PMD_ANTSEL, PMD_RATE, and PMD_TXPWRLVL primitives to configure the PHY. The PLCP shall then issue a PMD_TXSTART.request and the PHY entity shall immediately initiate data scrambling and transmission of the PLCP Preamble based on the parameters passed in the PHY-TXSTART.request primitive. The time required for TX power on ramp described in 15.4.7.7 shall be included in the PLCP synchronization field. Once the PLCP Preamble transmission is complete, data shall be exchanged between the MAC and the PHY by a series of PHY-DATA.request(DATA) primitives issued by the MAC and PHY-DATA.confirm primitives issued by the PHY. The modulation rate change, if any, shall be initiated with the first data symbol of the MPDU as described in 15.2.5. The PHY proceeds with MPDU transmission through a series of data octet transfers from the MAC. At the PMD layer, the data octets are sent in lsb to msb order and presented to the PHY layer through PMD_DATA.request primitives. Transmission can be prematurely terminated by the MAC through the primitive PHY-TXEND.request. PHY-TXSTART shall be disabled by the issuance of the PHY-TXEND.request. Normal termination occurs after the transmission of the final bit of the last MPDU octet according to the number supplied in the DSSS PHY preamble LENGTH field. The packet transmission shall be completed and the PHY entity shall enter the receive state (i.e., PHY-TXSTART shall be disabled). It is recommended that chipping continue during power-down. Each PHY-TXEND.request is acknowledged with a PHY-TXEND.confirm primitive from the PHY.

A typical state machine implementation of the PLCP transmit procedure is provided in Figure 91.

15.2.7 PLCP receive procedure

The PLCP receive procedure is shown in Figure 92.

In order to receive data, PHY-TXSTART.request shall be disabled so that the PHY entity is in the receive state. Further, through station management via the PLME, the PHY is set to the appropriate channel and the CCA method is chosen. Other receive parameters such as receive signal strength indication (RSSI), signal quality (SQ), and indicated DATARATE may be accessed via the PHY-SAP.



A At any stage in the above flow diagram, if a PHY_THEND. Request is received.

Figure 91 – PLCP transmit state machine

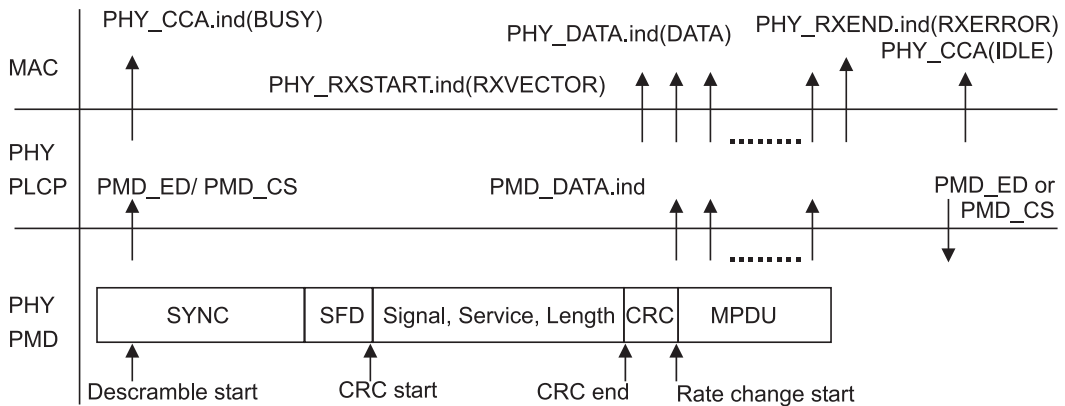


Figure 92 – PLCP receive procedure

Upon receiving the transmitted energy, according to the selected CCA mode, the PMD_ED shall be enabled (according to 15.4.8.4) as the RSSI strength reaches the ED_THRESHOLD and/or PMD_CS shall be enabled after code lock is established. These conditions are used to indicate activity to the MAC via PHY-CCA.indicate according to 15.4.8.4. PHY-CCA.indicate(BUSY) shall be issued for energy detection and/or code lock prior to correct reception of the PLCP frame. The PMD primitives PMD_SQ and PMD_RSSI are issued to update the RSSI and SQ parameters reported to the MAC.

After PHY-CCA.indicate is issued, the PHY entity shall begin searching for the SFD field. Once the SFD field is detected, CCITT CRC-16 processing shall be initiated and the PLCP IEEE 802.11 SIGNAL, IEEE 802.11 SERVICE and LENGTH fields are received. The CCITT CRC-16 FCS shall be processed. If the CCITT CRC-16 FCS check fails, the PHY receiver shall return to the RX Idle state as depicted in Figure 93. Should the status of CCA return to the IDLE state during reception prior to completion of the full PLCP processing, the PHY receiver shall return to the RX Idle state.

If the PLCP Header reception is successful (and the SIGNAL field is completely recognizable and supported), a PHY-RXSTART.indicate(RXVECTOR) shall be issued. The RXVECTOR associated with this primitive includes the SIGNAL field, the SERVICE field, the MPDU length in bytes (calculated from the LENGTH field in microseconds), the antenna used for receive (RX_ANTENNA), RSSI, and SQ.

The received MPDU bits are assembled into octets and presented to the MAC using a series of PHY-DATA.indicate(DATA) primitive exchanges. The rate change indicated in the IEEE 802.11 SIGNAL field shall be initiated with the first symbol of the MPDU as described in 15.2.5. The PHY proceeds with MPDU reception. After the reception of the final bit of the last MPDU octet indicated by the PLCP Preamble LENGTH field, the receiver shall be returned to the RX Idle state as shown in Figure 93. A PHY-RXEND.indicate(NoError) primitive shall be issued. A PHY-CCA.indicate(IDLE) primitive shall be issued following a change in PHYCS (PHY carrier sense) and/or PHYED (PHY energy detection) according to the selected CCA method.

In the event that a change in PHYCS or PHYED would cause the status of CCA to return to the IDLE state before the complete reception of the MPDU as indicated by the PLCP LENGTH field, the error condition PHY-RXEND.indicate(CarrierLost) shall be reported to the MAC. The DSSS PHY will ensure that the CCA will indicate a busy medium for the intended duration of the transmitted packet.

If the PLCP Header is successful, but the indicated rate in the SIGNAL field is not receivable, a PHY-RXSTART.indicate will not be issued. The PHY shall issue the error condition PHY-RXEND.indicate(UnsupportedRate). If the PLCP Header is successful, but the SERVICE field is out of IEEE 802.11 DSSS specification, a PHY-RXSTART.indicate will not be issued. The PHY shall issue the error condition PHY-RXEND.indicate(FormatViolation). Also, in both cases, the DSSS PHY will ensure that the CCA shall indicate a busy medium for the intended duration of the transmitted frame as indicated by the Length field. The intended duration is indicated by the Length field ($\text{length} \times 1 \mu\text{s}$).

A typical state machine implementation of the PLCP receive procedure is provided in Figure 93.

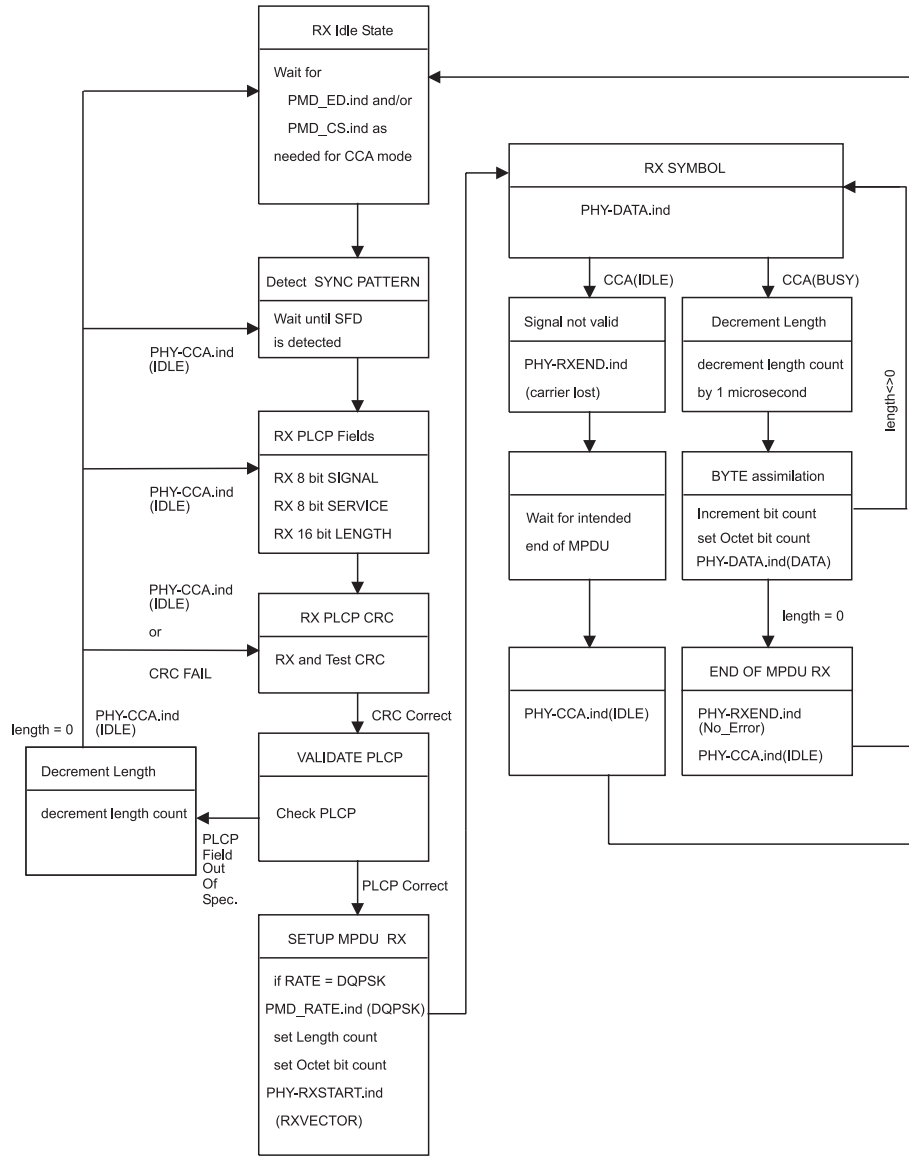


Figure 93—PLCP receive state machine

15.3 DSSS physical layer management entity (PLME)

15.3.1 PLME_SAP sublayer management primitives

Table 58 lists the MIB attributes that may be accessed by the PHY sublayer entities and intralayer of higher layer management entities (LME). These attributes are accessed via the PLME-GET, PLME-SET, and PLME-RESET primitives defined in Clause 10.

15.3.2 DSSS PHY MIB

All DSSS PHY MIB attributes are defined in Clause 12, with specific values defined in Table 58.

Table 58—MIB attribute default values/ranges

Managed object	Default value/range	Operational semantics
agPhyOperationGroup		
aPHYType	DSSS–2.4 (02)	Static
aTempType	Implementation dependent	Static
aCWmin	31	Static
aCWmax	1023	Static
aRegDomainsSupported	Implementation dependent	Static
aCurrentRegDomain	Implementation dependent	Static
aSlotTime	20 μ s	Static
aCCATime	≤ 15 μ s	Static
aRxTxTurnaroundTime	≤ 5 μ s	Static
aTxPLCPDelay	Implementation dependent	Static
aRxTxSwitchTime	≤ 5 μ s	Static
aTxRampOnTime	Implementation dependent	Static
aTxRFDelay	Implementation dependent	Static
aSIFSTime	10 μ s	Static
aRxRFDelay	Implementation dependent	Static
aRxPLCPDelay	Implementation dependent	Static
aMACProcessingDelay	Not applicable	n/a
aTxRampOffTime	Implementation dependent	Static
aPreambleLength	144 bits	Static
aPLCPHeaderLength	48 bits	Static
agPhyRateGroup		
aSupportedDataRatesTx	X'02', X'04'	Static
aSupportedDataRatesRx	X'02', X'04'	Static
aMPDUMaxLength	$4 \leq x \leq (2^{13} - 1)$	Static
agPhyAntennaGroup		
aCurrentTxAntenna	Implementation dependent	Dynamic
aDiversitySupport	Implementation dependent	Static
agPhyTxPowerGroup		
aNumberSupportedPowerLevels	Implementation dependent	Static
aTxPowerLevel1	Implementation dependent	Static
aTxPowerLevel2	Implementation dependent	Static
aTxPowerLevel3	Implementation dependent	Static
aTxPowerLevel4	Implementation dependent	Static
aTxPowerLevel5	Implementation dependent	Static
aTxPowerLevel6	Implementation dependent	Static
aTxPowerLevel7	implementation dependent	Static
aTxPowerLevel8	Implementation dependent	Static
aCurrentTxPowerLevel	Implementation dependent	Dynamic

Table 58—MIB attribute default values/ranges (continued)

Managed object	Default value/range	Operational semantics
agPhyStatusGroup		
aSynthesizerLocked	Implementation dependent	Dynamic
agPhyDSSSGroup		
aCurrentChannel	Implementation dependent	Dynamic
aCCAModeSupport	Implementation dependent	Static
aCurrentCCAMode	Implementation dependent	Dynamic
aEDThreshold	Implementation dependent	Dynamic
agPhyPwrSavingGroup		
aDozeTurnonTime	Implementation dependent	Static
aCurrentPowerState	Implementation dependent	Dynamic
agAntennasListGroup		
aSupportTxAntennas	Implementation dependent	Static
aSupportRxAntennas	Implementation dependent	Static
aDiversitySelectRx	Implementation dependent	Dynamic
NOTE—The column titled “Operational semantics” contains two types: static and dynamic. Static MIB attributes are fixed and cannot be modified for a given PHY implementation. MIB attributes defined as dynamic can be modified by some management entities.		

15.4 DSSS PMD sublayer

15.4.1 Scope and field of application

This subclause describes the PMD services provided to the PLCP for the DSSS PHY. Also defined in this subclause are the functional, electrical, and RF characteristics required for interoperability of implementations conforming to this specification. The relationship of this specification to the entire DSSS physical layer is shown in Figure 94.

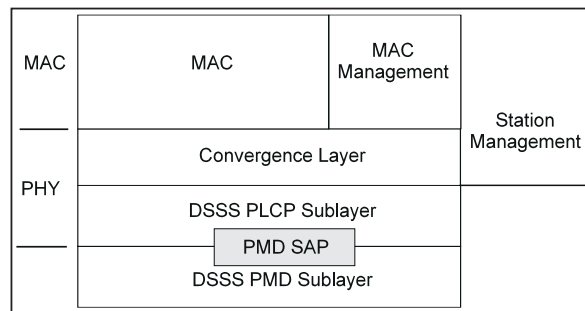


Figure 94—PMD layer reference model

15.4.2 Overview of service

The DSSS PMD sublayer accepts PLCP sublayer service primitives and provides the actual means by which data shall be transmitted or received from the medium. The combined function of DSSS PMD sublayer primitives and parameters for the receive function results in a data stream, timing information, and associ-

ated received signal parameters being delivered to the PLCP sublayer. A similar functionality shall be provided for data transmission.

15.4.3 Overview of interactions

The primitives associated with the IEEE 802.11 PLCP sublayer to the DSSS PMD fall into two basic categories:

- a) Service primitives that support PLCP peer-to-peer interactions, and
- b) Service primitives that have local significance and that support sublayer-to-sublayer interactions.

15.4.4 Basic service and options

All of the service primitives described in this clause are considered mandatory unless otherwise specified.

15.4.4.1 PMD_SAP peer-to-peer service primitives

Table 59 indicates the primitives for peer-to-peer interactions.

Table 59—PMD_SAP peer-to-peer service primitives

Primitive	Request	Indicate	Confirm	Response
PHY-RXSTART		X		
PHY-RXEND		X		
PHY-CCA		X		
PHY-TXSTART	X		X	
PHY-TXEND	X		X	
PHY-DATA	X	X	X	

15.4.4.2 PMD_SAP peer-to-peer service primitive parameters

Several service primitives include a parameter vector. This vector shall be actually a list of parameters that may vary depending on PHY type. Table 60 indicates the parameters required by the MAC or DSSS PHY in each of the parameter vectors used for peer-to-peer interactions.

Table 60—DSSS PMD_SAP peer-to-peer service primitives

Parameter	Associated primitive	Value
LENGTH	RXVECTOR, TXVECTOR	4 to $2^{16} - 1$
DATARATE	RXVECTOR, TXVECTOR	PHY dependent
SERVICE	RXVECTOR, TXVECTOR	PHY dependent
TXPWR_LEVEL	TXVECTOR	PHY dependent
TX_ANTENNA	TXVECTOR	PHY dependent
RSSI	RXVECTOR	PHY dependent
SQ	RXVECTOR	PHY dependent
RX_ANTENNA	RXVECTOR	PHY dependent

15.4.4.3 PMD_SAP sublayer-to-sublayer service primitives

Table 61 indicates the primitives for sublayer-to-sublayer interactions.

Table 61 – PMD_SAP sublayer-to-sublayer service primitives

Primitive	Request	Indicate	Confirm	Response
PMD_TXSTART	X			
PMD_TXEND	X			
PMD_ANTSEL	X	X		
PMD_TXPWRLVL	X			
PMD_RATE	X	X		
PMD_RSSI		X		
PMD_SQ		X		
PMD_CS		X		
PMD_ED	X	X		

15.4.4.4 PMD_SAP service primitive parameters

Table 62 indicates the parameters for the PMD primitives.

Table 62 – List of parameters for the PMD primitives

Parameter	Associate primitive	Value
DATA	PHY-DATA.request PHY-DATA.indicate	Octet value: X'00'–X'FF'
TXVECTOR	PHY-DATA.request	A set of parameters
RXVECTOR	PHY-DATA.indicate	A set of parameters
TXD_UNIT	PMD_DATA.request	One(1), Zero(0): DBPSK dibit combinations 00,01,11,10: DQPSK
RXD_UNIT	PMD_DATA.indicate	One(1), Zero(0): DBPSK dibit combinations 00,01,11,10: DQPSK
RF_STATE	PMD_TXE.request	Receive, Transmit
ANT_STATE	PMD_ANTSEL.indicate PMD_ANTSEL.request	1 to 256
TXPWR_LEVEL	PHY-TXSTART	0, 1, 2, 3 (max of 4 levels)
RATE	PMD_RATE.indicate PMD_RATE.request	X'0A' for 1 Mbit/s DBPSK X'14' for 2 Mbit/s DQPSK
RSSI	PMD_RSSI.indicate	0–8 bits of RSSI
SQ	PMD_SQ.indicate	0–8 bits of SQ

15.4.5 PMD_SAP detailed service specification

The following subclauses describe the services provided by each PMD primitive.

15.4.5.1 PMD_DATA.request

15.4.5.1.1 Function

This primitive defines the transfer of data from the PLCP sublayer to the PMD entity.

15.4.5.1.2 Semantics of the service primitive

The primitive shall provide the following parameters:

PMD_DATA.request(TXD_UNIT)

The TXD_UNIT parameter takes on the value of either one(1) or zero(0) for DBPSK modulation or the dibit combination 00, 01, 11, or 10 for DQPSK modulation. This parameter represents a single block of data, which, in turn, shall be used by the PHY to be differentially encoded into a DBPSK or DQPSK transmitted symbol. The symbol itself shall be spread by the PN code prior to transmission.

15.4.5.1.3 When generated

This primitive shall be generated by the PLCP sublayer to request transmission of a symbol. The data clock for this primitive shall be supplied by PMD layer based on the PN code repetition.

15.4.5.1.4 Effect of receipt

The PMD performs the differential encoding, PN code modulation and transmission of the data.

15.4.5.2 PMD_DATA.indicate

15.4.5.2.1 Function

This primitive defines the transfer of data from the PMD entity to the PLCP sublayer.

15.4.5.2.2 Semantics of the service primitive

The primitive shall provide the following parameters:

PMD_DATA.indicate(RXD_UNIT)

The RXD_UNIT parameter takes on the value of one(1) or zero(0) for DBPSK modulation or as the dibit 00, 01, 11, or 10 for DQPSK modulation. This parameter represents a single symbol that has been demodulated by the PMD entity.

15.4.5.2.3 When generated

This primitive, which is generated by the PMD entity, forwards received data to the PLCP sublayer. The data clock for this primitive shall be supplied by PMD layer based on the PN code repetition.

15.4.5.2.4 Effect of receipt

The PLCP sublayer either interprets the bit or bits that are recovered as part of the PLCP convergence procedure or passes the data to the MAC sublayer as part of the MPDU.

15.4.5.3 PMD_TXSTART.request

15.4.5.3.1 Function

This primitive, which is generated by the PHY PLCP sublayer, initiates PPDU transmission by the PMD layer.

15.4.5.3.2 Semantics of the service primitive

The primitive shall provide the following parameters:

PMD_TXSTART.request

15.4.5.3.3 When generated

This primitive shall be generated by the PLCP sublayer to initiate the PMD layer transmission of the PPDU. The PHY-DATA.request primitive shall be provided to the PLCP sublayer prior to issuing the PMD_TXSTART command.

15.4.5.3.4 Effect of receipt

PMD_TXSTART initiates transmission of a PPDU by the PMD sublayer.

15.4.5.4 PMD_TXEND.request

15.4.5.4.1 Function

This primitive, which is generated by the PHY PLCP sublayer, ends PPDU transmission by the PMD layer.

15.4.5.4.2 Semantics of the service primitive

The primitive shall provide the following parameters:

PMD_TXEND.request

15.4.5.4.3 When generated

This primitive shall be generated by the PLCP sublayer to terminate the PMD layer transmission of the PPDU.

15.4.5.4.4 Effect of receipt

PMD_TXEND terminates transmission of a PPDU by the PMD sublayer.

15.4.5.5 PMD_ANTSEL.request

15.4.5.5.1 Function

This primitive, which is generated by the PHY PLCP sublayer, selects the antenna used by the PHY for transmission or reception (when diversity is disabled).

15.4.5.5.2 Semantics of the service primitive

The primitive shall provide the following parameters:

PMD_ANTSEL.request(ANT_STATE)

ANT_STATE selects which of the available antennas should be used for transmit. The number of available antennas shall be determined from the MIB table parameters aSuprtRxAntennas and aSuprtTxAntennas.

15.4.5.5.3 When generated

This primitive shall be generated by the PLCP sublayer to select a specific antenna for transmission (or reception when diversity is disabled).

15.4.5.5.4 Effect of receipt

PMD_ANTSEL immediately selects the antenna specified by ANT_STATE.

15.4.5.6 PMD_ANTSEL.indicate

15.4.5.6.1 Function

This primitive, which is generated by the PHY PLCP sublayer, reports the antenna used by the PHY for reception of the most recent packet.

15.4.5.6.2 Semantics of the service primitive

The primitive shall provide the following parameters:

PMD_ANTSEL.indicate(ANT_STATE)

ANT_STATE reports which of the available antennas was used for reception of the most recent packet.

15.4.5.6.3 When generated

This primitive shall be generated by the PLCP sublayer to report the antenna used for the most recent packet reception.

15.4.5.6.4 Effect of receipt

PMD_ANTSEL immediately reports the antenna specified by ANT_STATE.

15.4.5.7 PMD_TXPWRLVL.request

15.4.5.7.1 Function

This primitive, which is generated by the PHY PLCP sublayer, selects the power level used by the PHY for transmission.

15.4.5.7.2 Semantics of the service primitive

The primitive shall provide the following parameters:

PMD_TXPWRLVL.request(TXPWR_LEVEL)

TXPWR_LEVEL selects which of the optional transmit power levels should be used for the current packet transmission. The number of available power levels shall be determined by the MIB parameter aNumber-SupportedPowerLevels. Subclause 15.4.7.3 provides further information on the optional DSSS PHY power level control capabilities.

15.4.5.7.3 When generated

This primitive shall be generated by the PLCP sublayer to select a specific transmit power. This primitive shall be applied prior to setting PMD_TXSTART into the transmit state.

15.4.5.7.4 Effect of receipt

PMD_TXPWRLVL immediately sets the transmit power level given by TXPWR_LEVEL.

15.4.5.8 PMD_RATE.request

15.4.5.8.1 Function

This primitive, which is generated by the PHY PLCP sublayer, selects the modulation rate that shall be used by the DSSS PHY for transmission.

15.4.5.8.2 Semantics of the service primitive

The primitive shall provide the following parameters:

PMD_RATE.request(RATE)

RATE selects which of the DSSS PHY data rates shall be used for MPDU transmission. Subclause 15.4.6.4 provides further information on the DSSS PHY modulation rates. The DSSS PHY rate change capability is fully described in 15.2.

15.4.5.8.3 When generated

This primitive shall be generated by the PLCP sublayer to change or set the current DSSS PHY modulation rate used for the MPDU portion of a PPDU.

15.4.5.8.4 Effect of receipt

The receipt of PMD_RATE selects the rate that shall be used for all subsequent MPDU transmissions. This rate shall be used for transmission only. The DSSS PHY shall still be capable of receiving all the required DSSS PHY modulation rates.

15.4.5.9 PMD_RATE.indicate

15.4.5.9.1 Function

This primitive, which is generated by the PMD sublayer, indicates which modulation rate was used to receive the MPDU portion of the PPDU. The modulation shall be indicated in the PLCP Preamble IEEE 802.11 SIGNALING field.

15.4.5.9.2 Semantics of the service primitive

The primitive shall provide the following parameters:

PMD_RATE.indicate(RATE)

In receive mode, the RATE parameter informs the PLCP layer which of the DSSS PHY data rates was used to process the MPDU portion of the PPDU. Subclause 15.4.6.4 provides further information on the DSSS PHY modulation rates. The DSSS PHY rate change capability is fully described in 15.2.

15.4.5.9.3 When generated

This primitive shall be generated by the PMD sublayer when the PLCP Preamble IEEE 802.11 SIGNALING field has been properly detected.

15.4.5.9.4 Effect of receipt

This parameter shall be provided to the PLCP layer for information only.

15.4.5.10 PMD_RSSI.indicate

15.4.5.10.1 Function

This optional primitive, which is generated by the PMD sublayer, provides to the PLCP and MAC entity the received signal strength.

15.4.5.10.2 Semantics of the service primitive

The primitive shall provide the following parameters:

PMD_RSSI.indicate(RSSI)

The RSSI shall be a measure of the RF energy received by the DSSS PHY. RSSI indications of up to 8 bits (256 levels) are supported.

15.4.5.10.3 When generated

This primitive shall be generated by the PMD when the DSSS PHY is in the receive state. It shall be continuously available to the PLCP, which, in turn, provides the parameter to the MAC entity.

15.4.5.10.4 Effect of receipt

This parameter shall be provided to the PLCP layer for information only. The RSSI may be used in conjunction with SQ as part of a CCA scheme.

15.4.5.11 PMD_SQ.indicate

15.4.5.11.1 Function

This optional primitive, which is generated by the PMD sublayer, provides to the PLCP and MAC entity the signal quality (SQ) of the DSSS PHY PN code correlation. The SQ shall be sampled when the DSSS PHY achieves code lock and held until the next code lock acquisition.

15.4.5.11.2 Semantics of the service primitive

The primitive shall provide the following parameters:

PMD_SQ.indicate(SQ)

The SQ shall be a measure of the PN code correlation quality received by the DSSS PHY. SQ indications of up to 8 bits (256 levels) are supported.

15.4.5.11.3 When generated

This primitive shall be generated by the PMD when the DSSS PHY is in the receive state and code lock is achieved. It shall be continuously available to the PLCP, which, in turn, provides the parameter to the MAC entity.

15.4.5.11.4 Effect of receipt

This parameter shall be provided to the PLCP layer for information only. The SQ may be used in conjunction with RSSI as part of a CCA scheme.

15.4.5.12 PMD_CS.indicate

This primitive, which is generated by the PMD, shall indicate to the PLCP layer that the receiver has acquired (locked) the PN code and data is being demodulated.

15.4.5.12.1 Function

This primitive, which is generated by the PMD, shall indicate to the PLCP layer that the receiver has acquired (locked) the PN code and data is being demodulated.

15.4.5.12.2 Semantics of the service primitive

The PMD_CS (carrier sense) primitive in conjunction with PMD_ED provide CCA status through the PLCP layer PHYCCA primitive. PMD_CS indicates a binary status of ENABLED or DISABLED. PMD_CS shall be ENABLED when the correlator SQ indicated in PMD_SQ is greater than the CS_THRESHOLD parameter. PMD_CS shall be DISABLED when the PMD_SQ falls below the correlation threshold.

15.4.5.12.3 When generated

This primitive shall be generated by the PHY sublayer when the DSSS PHY is receiving a PPDU and the PN code has been acquired.

15.4.5.12.4 Effect of receipt

This indicator shall be provided to the PLCP for forwarding to the MAC entity for information purposes through the PHYCCA indicator. This parameter shall indicate that the RF medium is busy and occupied by a

DSSS PHY signal. The DSSS PHY should not be placed into the transmit state when PMD_CS is ENABLED.

15.4.5.13 PMD_ED.indicate

15.4.5.13.1 Function

This optional primitive, which is generated by the PMD, shall indicate to the PLCP layer that the receiver has detected RF energy indicated by the PMD_RSSI primitive that is above a predefined threshold.

15.4.5.13.2 Semantics of the service primitive

The PMD_ED (energy detect) primitive, along with the PMD_SQ, provides CCA status at the PLCP layer through the PHYCCA primitive. PMD_ED indicates a binary status of ENABLED or DISABLED. PMD_ED shall be ENABLED when the RSSI indicated in PMD_RSSI is greater than the ED_THRESHOLD parameter. PMD_ED shall be DISABLED when the PMD_RSSI falls below the energy detect threshold.

15.4.5.13.3 When generated

This primitive shall be generated by the PHY sublayer when the PHY is receiving RF energy from any source that exceeds the ED_THRESHOLD parameter.

15.4.5.13.4 Effect of receipt

This indicator shall be provided to the PLCP for forwarding to the MAC entity for information purposes through the PMD_ED indicator. This parameter shall indicate that the RF medium may be busy with an RF energy source that is not DSSS PHY compliant. If a DSSS PHY source is being received, the PMD_CS function shall be enabled shortly after the PMD_ED function is enabled.

15.4.5.14 PMD_ED.request

15.4.5.14.1 Function

This optional primitive, which is generated by the PHY PLCP, sets the energy detect ED_THRESHOLD value.

15.4.5.14.2 Semantics of the service primitive

The primitive shall provide the following parameters:

PMD_ED.request(ED_THRESHOLD)

ED_THRESHOLD sets the threshold that the RSSI indicated shall be greater than in order for PMD_ED to be enabled.

15.4.5.14.3 When generated

This primitive shall be generated by the PLCP sublayer to change or set the current DSSS PHY energy detect threshold.

15.4.5.14.4 Effect of receipt

The receipt of PMD_ED immediately changes the energy detection threshold as set by the ED_THRESHOLD parameter.

15.4.5.15 PHY-CCA.indicate

15.4.5.15.1 Function

This primitive, which is generated by the PMD, indicates to the PLCP layer that the receiver has detected RF energy that adheres to the CCA algorithm.

15.4.5.15.2 Semantics of the service primitive

The PHY-CCA primitive provides CCA status at the PLCP layer to the MAC.

15.4.5.15.3 When generated

This primitive shall be generated by the PHY sublayer when the PHY is receiving RF energy from any source that exceeds the ED_THRESHOLD parameter (PMD_ED is active), and optionally is a valid correlated DSSS PHY signal whereby PMD_CS would also be active.

15.4.5.15.4 Effect of receipt

This indicator shall be provided to the PLCP for forwarding to the MAC entity for information purposes through the PHY-CCA indicator. This parameter indicates that the RF medium may be busy with an RF energy source that may or may not be DSSS PHY compliant. If a DSSS PHY source is being received, the PMD_CS function shall be enabled shortly after the PMD_ED function is enabled.

15.4.6 PMD operating specifications, general

The following subclauses provide general specifications for the DSSS PMD sublayer. These specifications apply to both the Receive and the Transmit functions and general operation of a DSSS PHY.

15.4.6.1 Operating frequency range

The DSSS PHY shall operate in the frequency range of 2.4 GHz to 2.4835 GHz as allocated by regulatory bodies in the USA and Europe or in the 2.471 GHz to 2.497 GHz frequency band as allocated by regulatory authority in Japan.

15.4.6.2 Number of operating channels

The channel center frequencies and CHNL_ID numbers shall be as shown in Table 63. The FCC (US), IC (Canada), and ETSI (Europe) specify operation from 2.4 GHz to 2.4835 GHz. For Japan, operation is specified as 2.471 GHz to 2.497 GHz. France allows operation from 2.4465 GHz to 2.4835 GHz, and Spain

allows operation from 2.445 GHz to 2.475 GHz. For each supported regulatory domain, all channels in Table 63 marked with “X” shall be supported.

Table 63—DSSS PHY frequency channel plan

CHNL_ID	Frequency	Regulatory domains					
		X'10' FCC	X'20' IC	X'30' ETSI	X'31' Spain	X'32' France	X'40' MKK
1	2412 MHz	X	X	X	—	—	—
2	2417 MHz	X	X	X	—	—	—
3	2422 MHz	X	X	X	—	—	—
4	2427 MHz	X	X	X	—	—	—
5	2432 MHz	X	X	X	—	—	—
6	2437 MHz	X	X	X	—	—	—
7	2442 MHz	X	X	X	—	—	—
8	2447 MHz	X	X	X	—	—	—
9	2452 MHz	X	X	X	—	—	—
10	2457 MHz	X	X	X	X	X	—
11	2462 MHz	X	X	X	X	X	—
12	2467 MHz	—	—	X	—	X	—
13	2472 MHz	—	—	X	—	X	—
14	2484 MHz	—	—	—	—	—	X

In a multiple cell network topology, overlapping and/or adjacent cells using different channels can operate simultaneously without interference if the distance between the center frequencies is at least 30 MHz. Channel 14 shall be designated specifically for operation in Japan.

15.4.6.3 Spreading sequence

The following 11-chip Barker sequence shall be used as the PN code sequence:

+1, -1, +1, +1, -1, +1, +1, +1, -1, -1, -1

The leftmost chip shall be output first in time. The first chip shall be aligned at the start of a transmitted symbol. The symbol duration shall be exactly 11 chips long.

15.4.6.4 Modulation and channel data rates

Two modulation formats and data rates are specified for the DSSS PHY: a *basic access rate* and an *enhanced access rate*. The basic access rate shall be based on 1 Mbit/s DBPSK modulation. The DBPSK encoder is

specified in Table 64. The enhanced access rate shall be based on 2 Mbit/s DQPSK. The DQPSK encoder is specified in Table 65. (In the tables, $+j\omega$ shall be defined as counterclockwise rotation.)

Table 64—1 Mbit/s DBPSK encoding table

Bit input	Phase change ($+j\omega$)
0	0
1	π

Table 65—2 Mbit/s DQPSK encoding table

Dibit pattern (d0,d1) d0 is first in time	Phase change ($+j\omega$)
00	0
01	$\pi/2$
11	π
10	$3\pi/2$ ($-\pi/2$)

15.4.6.5 Transmit and receive in-band and out-of-band spurious emissions

The DSSS PHY shall conform with in-band and out-of-band spurious emissions as set by regulatory bodies. For the USA, refer to FCC 15.247, 15.205, and 15.209. For Europe, refer to ETS 300–328.

15.4.6.6 Transmit-to-receive turnaround time

The TX-to-RX turnaround time shall be less than 10 μs , including the power-down ramp specified in 15.4.7.7.

The TX-to-RX turnaround time shall be measured at the air interface from the trailing edge of the last transmitted symbol to valid CCA detection of the incoming signal. The CCA should occur within 25 μs (10 μs for turnaround time plus 15 μs for energy detect) or by the next slot boundary occurring after the 25 μs has elapsed (refer to 15.4.8.4). A receiver input signal 3 dB above the ED threshold described in 15.4.8.4 shall be present at the receiver.

15.4.6.7 Receive-to-transmit turnaround time

The RX-to-TX turnaround time shall be measured at the MAC/PHY interface, using PHYTXSTART.request and shall be ≤ 5 μs . This includes the transmit power up ramp described in 15.4.7.7.

15.4.6.8 Slot time

The slot time for the DSSS PHY shall be the sum of the RX-to-TX turnaround time (5 μs) and the energy detect time (15 μs specified in 15.4.8.4). The propagation delay shall be regarded as being included in the energy detect time.

15.4.6.9 Transmit and receive antenna port impedance

The impedance of the transmit and receive antenna port(s) shall be 50 Ω if the port is exposed.

15.4.6.10 Transmit and receive operating temperature range

Three temperature ranges for full operation compliance to the DSSS PHY are specified in Clause 13. Type 1 shall be defined as 0 °C to 40 °C, and is designated for office environments. Type 2 shall be defined as –20 °C to +50 °C, and Type 3 shall be defined as –30 °C to +70 °C. These are designated for industrial environments.

15.4.7 PMD transmit specifications

The following subclauses describe the transmit functions and parameters associated with the PMD sublayer.

15.4.7.1 Transmit power levels

The maximum allowable output power as measured in accordance with practices specified by the regulatory bodies is shown in Table 66. In the USA, the radiated emissions should also conform with the ANSI uncontrolled radiation emission standards (IEEE Std C95.1-1991).

Table 66—Transmit power levels

Maximum output power	Geographic location	Compliance document
1000 mW	USA	FCC 15.247
100 mW (EIRP)	Europe	ETS 300–328
10 mW/MHz	Japan	MPT ordinance for Regulating Radio Equipment, Article 49-20

15.4.7.2 Minimum transmitted power level

The minimum transmitted power shall be no less than 1 mW.

15.4.7.3 Transmit power level control

Power control shall be provided for transmitted power greater than 100 mW. A maximum of four power levels may be provided. At a minimum, a radio capable of transmission greater than 100 mW shall be capable of switching power back to 100 mW or less.

15.4.7.4 Transmit spectrum mask

The transmitted spectral products shall be less than –30 dBr (dB relative to the SINx/x peak) for $f_c - 22$ MHz $< f < f_c - 11$ MHz, $f_c + 11$ MHz $< f < f_c + 22$ MHz, –50 dBr for $f < f_c - 22$ MHz, and $f > f_c + 22$ MHz, where f_c is the channel center frequency. The transmit spectral mask is shown in Figure 95. The measurements shall be made using 100 kHz resolution bandwidth and a 30 kHz video bandwidth.

15.4.7.5 Transmit center frequency tolerance

The transmitted center frequency tolerance shall be ± 25 ppm maximum.

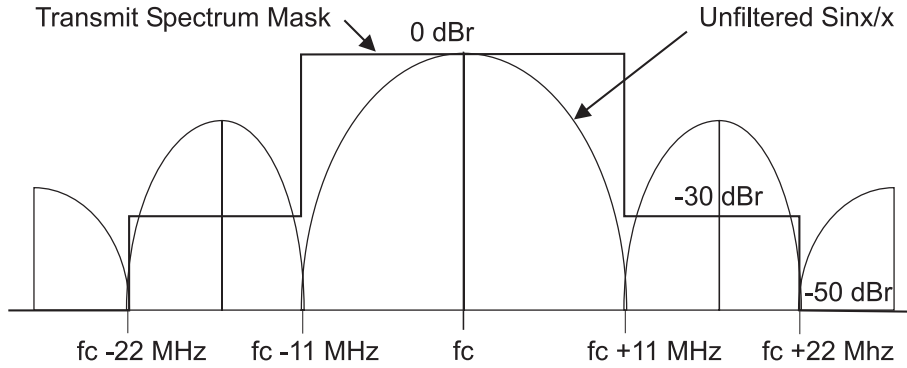


Figure 95—Transmit spectrum mask

15.4.7.6 Chip clock frequency tolerance

The PN code chip clock frequency tolerance shall be better than ± 25 ppm maximum.

15.4.7.7 Transmit power-on and power-down ramp

The transmit power-on ramp for 10% to 90% of maximum power shall be no greater than 2 μ s. The transmit power-on ramp is shown in Figure 96.

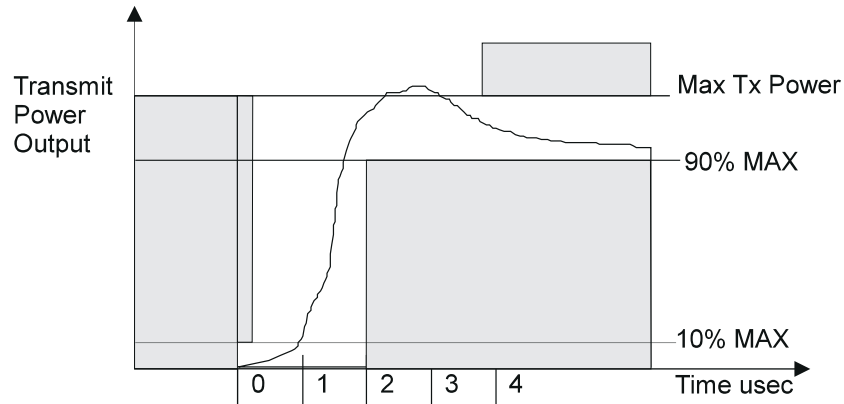


Figure 96—Transmit power-on ramp

The transmit power-down ramp for 90% to 10% maximum power shall be no greater than 2 μ s. The transmit power down ramp is shown in Figure 97.

The transmit power ramps shall be constructed such that the DSSS PHY emissions conform with spurious frequency product specification defined in 15.4.6.5.

15.4.7.8 RF carrier suppression

The RF carrier suppression, measured at the channel center frequency, shall be at least 15 dB below the peak $\text{SIN}(x)/x$ power spectrum. The RF carrier suppression shall be measured while transmitting a repetitive 01 data sequence with the scrambler disabled using DQPSK modulation. A 100 kHz resolution bandwidth shall be used to perform this measurement.

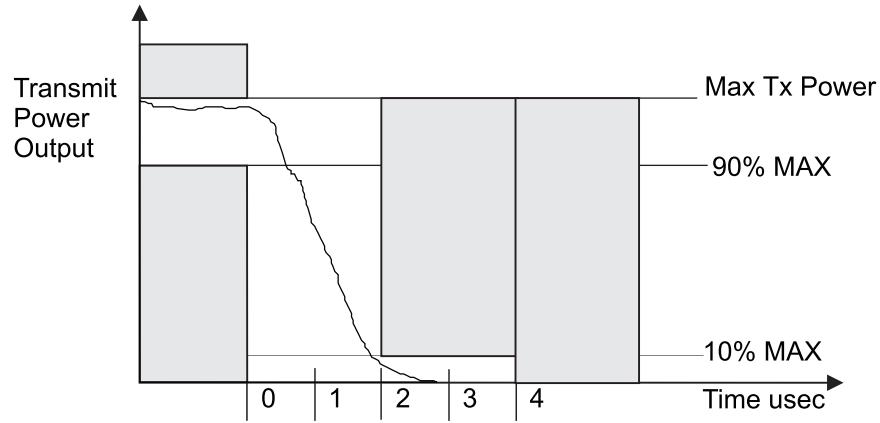


Figure 97—Transmit power-down ramp

15.4.7.9 Transmit modulation accuracy

The transmit modulation accuracy requirement for the DSSS PHY shall be based on the difference between the actual transmitted waveform and the ideal signal waveform. Modulation accuracy shall be determined by measuring the peak vector error magnitude measured during each chip period. Worst-Case vector error magnitude shall not exceeded 0.35 for the normalized sampled chip data. The ideal complex I and Q constellation points associated with DQPSK modulation (0.707,0.707), (0.707, -0.707), (-0.707, 0.707), (-0.707, -0.707) shall be used as the reference. These measurements shall be from baseband I and Q sampled data after recovery through a reference receiver system.

Figure 98 illustrates the ideal DQPSK constellation points and range of worst-case error specified for modulation accuracy.

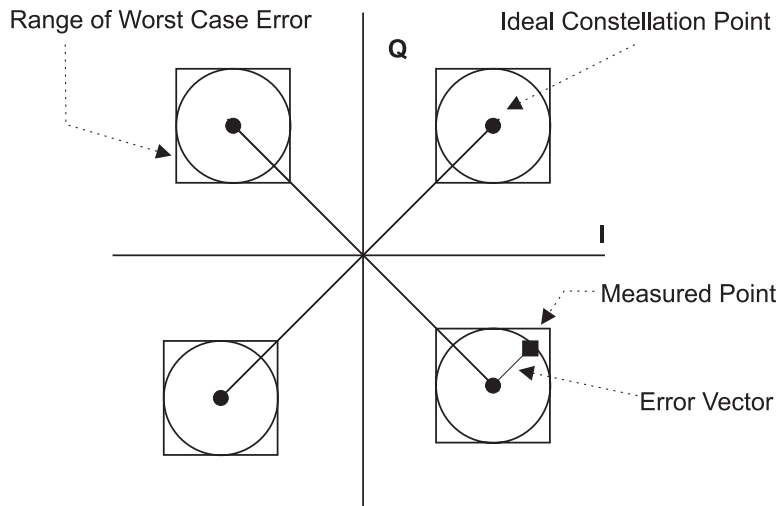


Figure 98—Modulation accuracy measurement example

Error vector measurement requires a reference receiver capable of carrier lock. All measurements shall be made under carrier lock conditions. The distortion induced in the constellation by the reference receiver shall be calibrated and measured. The test data error vectors described below shall be corrected to compensate for the reference receiver distortion.

The IEEE 802.11 vendor compatible radio shall provide an exposed TX chip clock, which shall be used to sample the I and Q outputs of the reference receiver.

The measurement shall be made under the conditions of continuous DQPSK transmission using scrambled all 1's.

The eye pattern of the I channel shall be used to determine the I and Q sampling point. The chip clock provided by the vendor radio shall be time delayed such that the samples fall at a 1/2 chip period offset from the mean of the zero crossing positions of the eye (see Figure 99). This is the ideal center of the eye and may not be the point of maximum eye opening.

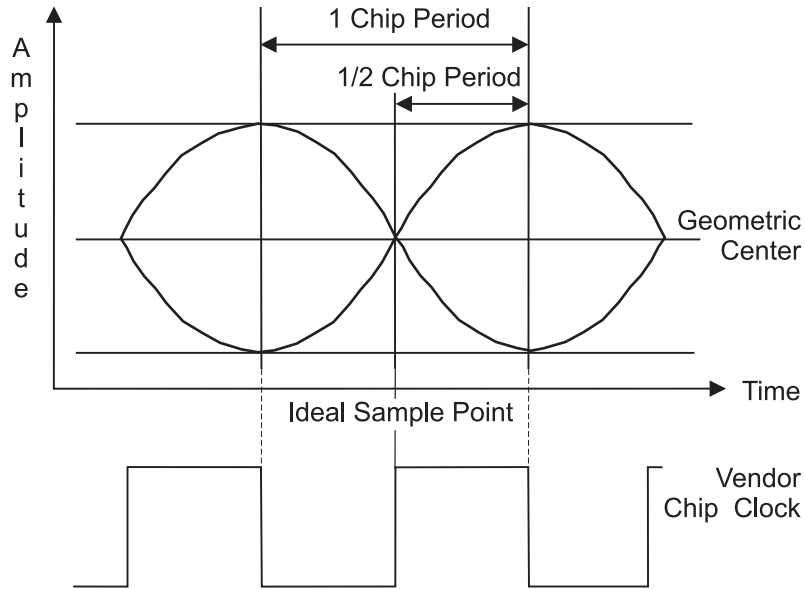


Figure 99—Chip clock alignment with baseband eye pattern

Using the aligned chip clock, 1000 samples of the *I* and *Q* baseband outputs from the reference receiver are captured. The vector error magnitudes shall be calculated as follows:

Calculate the dc offsets for *I* and *Q* samples.

$$I_{\text{mean}} = \sum_{n=0}^{1000} I(n) / 1000$$

$$Q_{\text{mean}} = \sum_{n=0}^{1000} Q(n) / 1000$$

Calculate the dc corrected *I* and *Q* samples for all $n = 1000$ sample pairs.

$$I_{\text{dc}}(n) = I(n) - I_{\text{mean}}$$

$$Q_{\text{dc}}(n) = Q(n) - Q_{\text{mean}}$$

Calculate the average magnitude of *I* and *Q* samples.

$$I_{\text{mag}} = \sum_{n=0}^{1000} |I_{\text{dc}}(n)| / 1000$$

$$Q_{\text{mag}} = \sum_{n=0}^{1000} |Q_{\text{dc}}(n)| / 1000$$

Calculate the normalized error vector magnitude for the $I_{\text{dc}}(n)/Q_{\text{dc}}(n)$ pairs.

$$V_{\text{err}}(n) = \left[\frac{1}{2} \times (\{|I_{\text{dc}}(n)|/I_{\text{mag}}\}^2 + \{|Q_{\text{dc}}(n)|/Q_{\text{mag}}\}^2) \right]^{\frac{1}{2}} - V_{\text{correction}}$$

with $V_{\text{correction}}$ = error induced by the reference receiver system.

A vendor DSSS PHY implementation shall be compliant if for all $n=1000$ samples the following condition is met:

$$V_{\text{err}}(n) < 0.35$$

15.4.8 PMD receiver specifications

The following subclauses describe the receive functions and parameters associated with the PMD sublayer.

15.4.8.1 Receiver minimum input level sensitivity

The frame error ratio (FER) shall be less than 8×10^{-2} at an MPDU length of 1024 bytes for an input level of -80 dBm measured at the antenna connector. This FER shall be specified for 2 Mbit/s DQPSK modulation. The test for the minimum input level sensitivity shall be conducted with the energy detection threshold set less than or equal to -80 dBm.

15.4.8.2 Receiver maximum input level

The receiver shall provide a maximum FER of 8×10^{-2} at an MPDU length of 1024 bytes for a maximum input level of -4 dBm measured at the antenna. This FER shall be specified for 2 Mbit/s DQPSK modulation.

15.4.8.3 Receiver adjacent channel rejection

Adjacent channel rejection is defined between any two channels with ≥ 30 MHz separation in each channel group defined in 15.4.6.2.

The adjacent channel rejection shall be equal to or better than 35 dB with an FER of 8×10^{-2} using 2 Mbit/s DQPSK modulation described in 15.4.6.4 and an MPDU length of 1024 bytes.

The adjacent channel rejection shall be measured using the following method:

Input a 2 Mbit/s DQPSK modulated signal at a level 6 dB greater than specified in 15.4.8.1. In an adjacent channel (≥ 30 MHz separation as defined by the channel numbering), input a signal modulated in a similar fashion that adheres to the transmit mask specified in 15.4.7.4 to a level 41 dB above the level specified in 15.4.8.1. The adjacent channel signal shall be derived from a separate signal source. It cannot be a frequency shifted version of the reference channel. Under these conditions, the FER shall be no worse than 8×10^{-2} .

15.4.8.4 CCA

The DSSS PHY shall provide the capability to perform CCA according to at least one of the following three methods:

- *CCA Mode 1*: Energy above threshold. CCA shall report a busy medium upon detecting any energy above the ED threshold.
- *CCA Mode 2*: Carrier sense only. CCA shall report a busy medium only upon the detection of a DSSS signal. This signal may be above or below the ED threshold.
- *CCA Mode 3*: Carrier sense with energy above threshold. CCA shall report a busy medium upon the detection of a DSSS signal with energy above the ED threshold.

The energy detection status shall be given by the PMD primitive, PMD_ED. The carrier sense status shall be given by PMD_CS. The status of PMD_ED and PMD_CS is used in the PLCP convergence procedure to indicate activity to the MAC through the PHY interface primitive PHY-CCA.indicate.

A busy channel shall be indicated by PHY-CCA.indicate of class BUSY.

Clear channel shall be indicated by PHY-CCA.indicate of class IDLE.

The PHY MIB attribute aCCAModeSuprt shall indicate the appropriate operation modes. The PHY shall be configured through the PHY MIB attribute aCurrentCCAMode.

The CCA shall be TRUE if there is no energy detect or carrier sense. The CCA parameters are subject to the following criteria:

- a) The energy detection threshold shall be less than or equal to -80 dBm for TX power > 100 mW, -76 dBm for 50 mW $<$ TX power ≤ 100 mW, and -70 dBm for TX power ≤ 50 mW.
- b) With a valid signal (according to the CCA mode of operation) present at the receiver antenna within 5 μ s of the start of a MAC slot boundary, the CCA indicator shall report channel busy before the end of the slot time. This implies that the CCA signal is available as an exposed test point. Refer to Figure 47 for a definition of slot time boundary definition.
- c) In the event that a correct PLCP Header is received, the DSSS PHY shall hold the CCA signal inactive (channel busy) for the full duration as indicated by the PLCP LENGTH field. Should a loss of carrier sense occur in the middle of reception, the CCA shall indicate a busy medium for the intended duration of the transmitted packet.

Conformance to DSSS PHY CCA shall be demonstrated by applying a DSSS compliant signal, above the appropriate ED threshold (a), such that all conditions described in b) and c) above are demonstrated.

16. Infrared (IR) PHY specification

16.1 Overview

The physical layer for the infrared system is specified in this clause. The IR PHY uses near-visible light in the 850 nm to 950 nm range for signaling. This is similar to the spectral usage of both common consumer devices such as infrared remote controls, as well as other data communications equipment, such as IrDA (Infrared Data Association) devices.

Unlike many other infrared devices, however, the IR PHY is not directed. That is, the receiver and transmitter do not have to be aimed at each other and do not need a clear line of sight. This permits the construction of a true LAN system, whereas with an aimed system, it would be difficult or impossible to install a LAN because of physical constraints.

A pair of conformant infrared devices would be able to communicate in a typical environment at a range up to about 10 m. The standard allows conformant devices to have more sensitive receivers, and this may increase range up to about 20 m.

The IR PHY relies on both reflected infrared energy as well as line-of-sight infrared energy for communications. Most designs anticipate that *all* of the energy at the receiver is reflected energy. This reliance on reflected infrared energy is called *diffuse infrared* transmission.

The standard specifies the transmitter and receiver in such a way that a conformant design will operate well in most environments where there is no line-of-sight path from the transmitter to the receiver. However, in an environment that has few or no reflecting surfaces, and where there is no line of sight, an IR PHY system may suffer reduced range.

The IR PHY will operate only in indoor environments. Infrared radiation does not pass through walls, and is significantly attenuated passing through most exterior windows. This characteristic can be used to “contain” an IR PHY in a single physical room, like a classroom or conference room. Different LANs using the IR PHY can operate in adjacent rooms separated only by a wall without interference, and without the possibility of eavesdropping.

At the time of this standard’s preparation, the only known regulatory standards that apply to the use of infrared radiation are safety regulations, such as IEC 60825-1 [B2] and ANSI Z136.1 [B1]. While a conformant IR PHY device can be designed to also comply with these safety standards, conformance with this standard does not ensure conformance with other standards.

Worldwide, there are currently no frequency allocation or bandwidth allocation regulatory restrictions on infrared emissions.

Emitter (typically LED) and detector (typically PIN diode) devices for infrared communications are relatively inexpensive at the infrared wavelengths specified in the IR PHY, and at the electrical operating frequencies required by this PHY.

While many other devices in common use also use infrared emissions in the same optical band, these devices usually transmit infrared intermittently and do not interfere with the proper operation of a compliant IR PHY. If such a device does interfere, by transmitting continuously and with a very strong signal, it can be physically isolated (placing it in a different room) from the IEEE 802.11 LAN.

16.1.1 Scope

The PHY services provided to the IEEE 802.11 wireless LAN MAC by the IR system are described in this clause. The IR PHY layer consists of two protocol functions as follows:

- a) A physical layer convergence function, which adapts the capabilities of the physical medium dependent (PMD) system to the PHY service. This function is supported by the physical layer convergence procedure (PLCP), which defines a method of mapping the IEEE 802.11 MAC sublayer protocol data units (MPDU) into a framing format suitable for sending and receiving user data and management information between two or more STAs using the associated PMD system.
- b) A PMD system, whose function defines the characteristics of, and method of transmitting and receiving data through, the wireless medium (WM) between two or more STAs.

16.1.2 IR PHY functions

The IR PHY contains three functional entities: the PMD function, the physical layer convergence function, and the layer management function. Each of these functions is described in detail below.

The IR PHY service is provided to the MAC entity at the STA through a service access point (SAP) as described in Clause 12. For a visual guide to the relationship of the IR PHY to the remainder of a system, refer to Figure 11.

16.1.2.1 PLCP sublayer

To allow the IEEE 802.11 MAC to operate with minimum dependence on the PMD sublayer, a physical layer convergence sublayer is defined. This function simplifies the PHY service interface to the IEEE 802.11 MAC services. The PHY-specific preamble is normally associated with this convergence layer.

16.1.2.2 PMD sublayer

The PMD sublayer provides a clear channel assessment (CCA) mechanism, transmission mechanism, and reception mechanism that are used by the MAC via the PLCP to send or receive data between two or more STAs.

16.1.2.3 PHY management entity (PLME)

The PLME performs management of the local PHY functions in conjunction with the MAC management entity. Subclause 16.4 lists the MIB variables that may be accessed by the PHY sublayer entities and intra-layer of higher layer management entities (LME). These variables are accessed via the PLME-GET, PLME-SET, and PLME-RESET primitives defined in Clause 10.

16.1.3 Service specification method and notation

The models represented by figures and state diagrams are intended as the illustrations of functions provided. It is important to distinguish between a model and a real implementation. The models are optimized for simplicity and clarity of presentation; the actual method of implementation is left to the discretion of the IEEE 802.11 IR PHY compliant developer. Conformance to the standard is not dependent on following the model, and an implementation that follows the model closely may not be conformant.

Abstract services are specified here by describing the service primitives and parameters that characterize each service. This definition is independent of any particular implementation. In particular, the PHY-SAP operations are defined and described as instantaneous; however, this may be difficult to achieve in an implementation.

16.2 IR PLCP sublayer

While the PLCP sublayer and the PMD sublayer are described separately, the separation and distinction between these sublayers is artificial, and is not meant to imply that the implementation must separate these functions. This distinction is made primarily to provide a point of reference from which to describe certain functional components and aspects of the PMD. The functions of the PLCP can be subsumed by a PMD sublayer; in this case, the PMD will incorporate the PHY-SAP as its interface, and will not offer a PMD-SAP.

16.2.1 Overview

A convergence procedure is provided by which MPDUs are converted to and from PLCPDUs. During transmission, the MPDU (PLCSDU) is prepended with a PLCP Preamble and PLCP Header to create the PLCPDU. At the receiver, the PLCP Preamble is processed and the internal data fields are processed to aid in demodulation and delivery of the MPDU (PSDU).

16.2.2 PLCP frame format

Figure 100 shows the format for the PLCPDU including the PLCP Preamble, the PLCP Header, and the PSDU. The PLCP Preamble contains the following fields: Synchronization (SYNC) and Start Frame Delimiter (SFD). The PLCP Header contains the following fields: Data Rate (DR), DC Level Adjustment (DCLA), Length (LENGTH), and Cyclic Redundancy Check (CRC). Each of these fields is described in detail in 16.2.4.

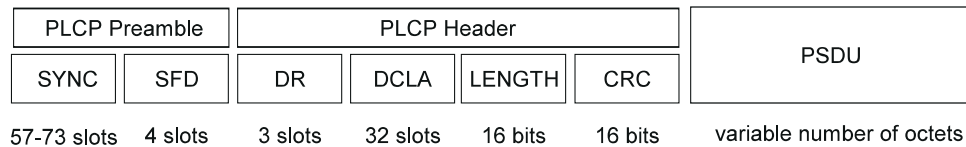


Figure 100—PLCPDU frame format

16.2.3 PLCP modulation and rate change

The PLCP Preamble shall be transmitted using the basic pulse defined in 16.3.3.2. The PLCSDU, LENGTH, and CRC fields shall be transmitted using pulse position modulation (PPM). PPM maps bits in the octet into symbols: 16-PPM maps four bits into a 16-position symbol, and 4-PPM maps two bits into a 4-position symbol. The basic L-PPM time unit is the slot. A slot corresponds to one of the L positions of a symbol and has a 250 ns duration. The PLCSDU, LENGTH, and CRC fields are transmitted at one of two bit rates: 1 Mbit/s or 2 Mbit/s. The Data Rate field indicates the data rate that will be used to transmit the PLCSDU, LENGTH, and CRC fields. The 1 Mbit/s data rate uses 16-PPM (basic access rate), and the 2 Mbit/s data rate uses 4-PPM (enhanced access rate). The transmitter and receiver will initiate the modulation or demodulation indicated by the DR field starting with the first 4 bits (in 16-PPM) or 2 bits (in 4-PPM) of the LENGTH field. The PSDU transmission rate is set by the DATARATE parameter in the PHY-TXSTART.request primitive. Any conformant IR PHY shall be capable of receiving at 1 Mbit/s and 2 Mbit/s. Transmission at 2 Mbit/s is optional.

A PHY-TXSTART.request that specifies a data rate which is not supported by a PHY instance will cause the PHY to indicate an error to its MAC instance. A PHY is not permitted under any circumstance to transmit at a different rate than the requested rate.

16.2.4 PLCP field definitions

16.2.4.1 PLCP Synchronization (SYNC) field

The SYNC field consists of a sequence of alternated presence and absence of a pulse in consecutive slots. The SYNC field has a minimum length of 57 L-PPM slots and a maximum length of 73 L-PPM slots and shall terminate with the absence of a pulse in the last slot. This field is provided so that the receiver can perform clock recovery (slot synchronization), automatic gain control (optional), signal-to-noise ratio estimation (optional), and diversity selection (optional).

The SYNC field is not modulated using L-PPM, but instead consists of transitions in L-PPM slots that would otherwise constitute an illegal symbol. See 16.3.2.1 for legal symbols.

16.2.4.2 PLCP Start Frame Delimiter (SFD) field

The SFD field length is four L-PPM slots and consists of the binary sequence 1001, where 1 indicates a pulse in the L-PPM slot and 0 indicates no pulse in the L-PPM slot. The leftmost bit shall be transmitted first. The SFD field is provided to indicate the start of the PLCP Preamble and to perform bit and symbol synchronization.

The SFD field is not modulated using L-PPM, but instead consists of transitions in L-PPM slots that would otherwise constitute an illegal symbol.

16.2.4.3 PLCP Data Rate (DR) field

The DR field indicates to the PHY the data rate that shall be used for the transmission or reception of the PLCSDU, LENGTH, and CRC fields. The transmitted value shall be provided by the PHY-TXSTART.request primitive as described in Clause 12. The DR field has a length of three L-PPM slots. The leftmost bit, as shown below, shall be transmitted first. The IR PHY currently supports two data rates defined by the slot pattern shown for the three L-PPM slots following the SFD, where 1 indicates a pulse in the L-PPM slot and 0 indicates no pulse in the L-PPM slot:

1 Mbit/s: 000

2 Mbit/s: 001

The DR field is not modulated using L-PPM, but instead consists of transitions in L-PPM slots that would otherwise constitute an illegal symbol.

16.2.4.4 PLCP DC Level Adjustment (DCLA) field

The DCLA field is required to allow the receiver to stabilize the dc level after the SYNC, SFD, and DR fields. The leftmost bit, as shown below, shall be transmitted first. The length of the DCLA field is 32 L-PPM slots and consists of the contents shown, where 1 indicates a pulse in the L-PPM slot and 0 indicates no pulse in the L-PPM slot:

1 Mbit/s: 00000000100000000000000010000000

2 Mbit/s: 00100010001000100010001000100010

The DCLA field is not modulated using L-PPM, but instead consists of transitions in L-PPM slots that would otherwise constitute an illegal symbol.

16.2.4.5 PLCP LENGTH field

The LENGTH field is an unsigned 16-bit integer that indicates the number of octets to be transmitted in the PSDU. The transmitted value shall be provided by the PHYTXSTART.request primitive as described in Clause 12. The lsb shall be transmitted first. This field is modulated and sent in L-PPM format. This field is protected by the CRC described in 16.2.4.6.

16.2.4.6 PLCP CRC field

The LENGTH field shall be protected by a 16-bit CRC-CCITT. The CRC-CCITT is the one's complement of the remainder generated by the modulo 2 division of the LENGTH field by the polynomial:

$$x^{16} + x^{12} + x^5 + 1$$

The protected bits will be processed in transmit order. The msb of the 16-bit CRC-CCITT shall be transmitted first. This field shall be modulated and sent in L-PPM format. All CRC-CCITT calculations shall be made prior to L-PPM encoding on transmission and after L-PPM decoding on reception.

16.2.4.7 PSDU field

This field is composed of a variable number of octets. The minimum is 0 (zero) and the maximum is 2500. The lsb of each octet shall be transmitted first. All the octets of this field shall be modulated and sent in L-PPM format.

16.2.5 PLCP procedures

16.2.5.1 PLCP transmit procedure

All commands issued by the MAC require that a confirmation primitive be issued by the PHY. The confirmation primitives provide flow control between the MAC and the PHY.

The steps below are the transmit procedure:

- a) Based on the status of CCA, the MAC shall determine whether the channel is clear.
- b) If the channel is clear, transmission of the PSDU shall be initiated by a PHY-TXSTART.request with parameters LENGTH and DATARATE.
- c) The PHY entity shall immediately initiate transmission of the PLCP Preamble and PLCP Header based on the LENGTH and DATARATE parameters passed in the PHY-TXSTART.request. Once the PLCP Preamble and PLCP Header transmission is completed, the PHY entity shall issue a PHY-TXSTART.confirm.
- d) Each octet of the PSDU is passed from the MAC to the PHY by a single PHY-DATA.request primitive. Each PHY-DATA.request shall be confirmed by the PHY with a PHY-DATA.confirm before the next request can be made.
- e) At the PHY layer each PSDU octet shall be divided into symbols of 2 or 4 bits each. The symbols shall be modulated using L-PPM and transmitted into the medium.
- f) Transmission is terminated by the MAC through the primitive PHY-TXEND.request. The PHY shall confirm the resulting end of transmission with a PHY-TXEND.confirm.

16.2.5.2 PLCP receive procedure

The steps below are the receive procedure:

- a) CCA is provided to the MAC via the PHY-CCA.indicate primitive. When the PHY senses activity on the medium, it shall indicate that the medium is busy with a PHY-CCA.indicate with a value of BUSY. This will normally occur during the SYNC field of the PLCP Preamble.
- b) The PHY entity shall begin searching for the SFD field. Once the SFD field is detected, the PHY entity shall attempt to receive the PLCP Header. After receiving the DR and DCLA fields, the PHY shall initiate processing of the received CRC and LENGTH fields. The data rate indicated in the DR field applies to all symbols in the latter part of the received PHYSDU, commencing with the first symbol of the LENGTH field. The CRC-CCITT shall be checked for correctness immediately after its reception.
- c) If the CRC-CCITT check fails, or the value received in the DR field is not one supported by the PHY, then a PHY-RXSTART.indicate shall not be issued to the MAC. When the medium is again free, the PHY shall issue a PHY-CCA.indicate with a value of IDLE.
- d) If the PLCP Preamble and PLCP Header reception is successful, the PHY shall send a PHY-RXSTART.indicate to the MAC; this includes the parameters DATARATE and LENGTH.
In the absence of errors, the receiving PHY shall report the same length to its local MAC, in the RXVECTOR parameter of the PHY-RXSTART.indicate primitive, that the peer MAC presented to its local PHY entity in the TXVECTOR parameter of its respective PHY-TXSTART.request.
- e) The received PLCSDU L-PPM symbols shall be assembled into octets and presented to the MAC using a series of PHY-DATA.indicate primitives, one per octet.
- f) Reception shall be terminated after the reception of the final symbol of the last PLCSDU octet indicated by the PLCP Header's LENGTH field. After the PHY-DATA.indicate for that octet is issued, the PHY shall issue a PHY-RXEND.indicate primitive to its MAC.
- g) After issuing the PHY-RXEND.indicate primitive, and when the medium is no longer busy, the PHY shall issue a PHY-CCA.indicate primitive with a value of IDLE.

16.2.5.3 CCA procedure

CCA is provided to the MAC via the PHY-CCA.indicate primitive.

The steps below are the CCA procedure:

- a) When the PHY senses activity on the medium, a PHY-CCA.indicate primitive with a value of BUSY shall be issued. This will normally occur during reception of the SYNC field of the PLCP Preamble.
- b) When the PHY senses that the medium is free, a PHY-CCA.indicate primitive with a value of IDLE shall be issued.
- c) At any time, the MAC may issue a PHY-CCARESET.request primitive, which will reset the PHY's internal CCA detection mechanism to the medium not-busy (IDLE) state. This primitive will be acknowledged with a PHY-CCARESET.confirm primitive.

16.2.5.4 PMD_SAP peer-to-peer service primitive parameters

Several service primitives include a parameter vector. This vector shall be actually a list of parameters that may vary depending on PHY type. Table 67 indicates the parameters required by the MAC or IR PHY in each of the parameter vectors used for peer-to-peer interactions.

Table 67—IR PMD_SAP peer-to-peer service primitives

Parameter	Associated primitive	Value
LENGTH	RXVECTOR, TXVECTOR	4 to $2^{16} - 1$
DATARATE	RXVECTOR, TXVECTOR	PHY dependent

16.3 IR PMD sublayer

The IR PMD sublayer does not define PMD SAPs. The mechanism for communications between the PLCP and PMD sublayers, as well as the distinction between these two sublayers, if any, is left to implementors. In particular, it is possible to design and implement in a conformant way a single sublayer that subsumes the functions of both the PLCP and PMD, presenting only the PHY-SAP.

16.3.1 Overview

The PMD functional, electrical, and optical characteristics required for interoperability of implementations conforming to this specification are described in this subclause. The relationship of this specification to the entire IR physical layer is shown in Figure 11.

16.3.2 PMD operating specifications, general

General specifications for the IR PMD sublayer are provided in this subclause. These specifications apply to both the receive and transmit functions and general operation of a compliant IR PHY.

16.3.2.1 Modulation and channel data rates

Two modulation formats and data rates are specified for the IR PHY: a *basic access rate* and an *enhanced access rate*. The basic access rate is based on 1 Mbit/s 16-PPM modulation. The 16-PPM encoding is specified in Table 68. Each group of 4 data bits is mapped to one of the 16-PPM symbols. The enhanced access rate is based on 2 Mbit/s 4-PPM. The 4-PPM encoding is specified in Table 69. Each group of 2 data bits is mapped to one of the 4-PPM symbols. Transmission order of the symbol slots is from left to right, as shown in the table, where a 1 indicates in-band energy in the slot, and a 0 indicates the absence of in-band energy in the slot.

The data in Table 68 and Table 69 have been arranged (gray coded) so that a single out-of-position-by-one error in the medium, caused, for example, by intersymbol interference, results in only a single bit error in the received data, rather than in a multiple bit error.

Table 68—Sixteen-PPM basic rate mapping

Data	16-PPM symbol
0000	0000000000000001
0001	0000000000000010
0011	0000000000000100
0010	00000000000001000
0110	0000000000010000
0111	0000000000100000
0101	0000000001000000
0100	0000000010000000
1100	0000000100000000
1101	0000001000000000
1111	0000010000000000
1110	0000100000000000
1010	0001000000000000
1011	0010000000000000
1001	0100000000000000
1000	1000000000000000

Table 69—Four-PPM enhanced rate mapping

Data	4-PPM symbol
00	0001
01	0010
11	0100
10	1000

16.3.2.2 Octet partition and PPM symbol generation procedure

Since PPM is a block modulation method, with the block size less than a full octet, octets have to be partitioned prior to modulation (mapping into PPM symbols).

Octet partition depends on the PPM order being used.

Assume an octet is formed by eight bits numbered 7 6 5 4 3 2 1 0, where bit 0 is the lsb. Partition the octet as follows:

For 16-PPM, create two PPM symbols:

- The symbol using bits 3 2 1 0 shall be transmitted onto the medium first
- The symbol using bits 7 6 5 4 shall be transmitted onto the medium last

For 4-PPM, create four PPM symbols:

- The symbol using bits 1 0 shall be transmitted onto the medium first
- The symbol using bits 3 2 shall be transmitted onto the medium second
- The symbol using bits 5 4 shall be transmitted onto the medium third
- The symbol using bits 7 6 shall be transmitted onto the medium last

16.3.2.3 Operating environment

The IR PHY will operate only in indoor environments. IR PHY interfaces cannot be exposed to direct sunlight. The IR PHY relies on reflected infrared energy and does not require a line of sight between emitter and receiver in order to work properly. The range and bit-error-rate of the system may vary with the geometry of the environment and with natural and artificial illumination conditions.

16.3.2.4 Operating temperature range

The temperature range for full operation compliance with the IR PHY is specified as 0 °C to 40 °C.

16.3.3 PMD transmit specifications

The following subclauses describe the transmit functions and parameters associated with the PMD sublayer.

16.3.3.1 Transmitted peak optical power

The peak optical power of an emitted pulse shall be as specified in Table 70.

Table 70—Peak optical power as a function of emitter radiation pattern mask

Emitter radiation pattern mask	Peak optical power
Mask 1	2 W \pm 20%
Mask 2	0.55 W \pm 20%

16.3.3.2 Basic pulse shape and parameters

The basic pulse width, measured between the 50% amplitude points, shall be 250 ± 10 ns. The pulse rise time, measured between the 10% and 90% amplitude points, shall be no more than 40 ns. The pulse fall time, measured between the 10% and 90% amplitude points, shall be no more than 40 ns. The edge jitter, defined as the absolute deviation of the edge from its correct position, shall be no more than 10 ns. The basic pulse shape is shown in Figure 101.

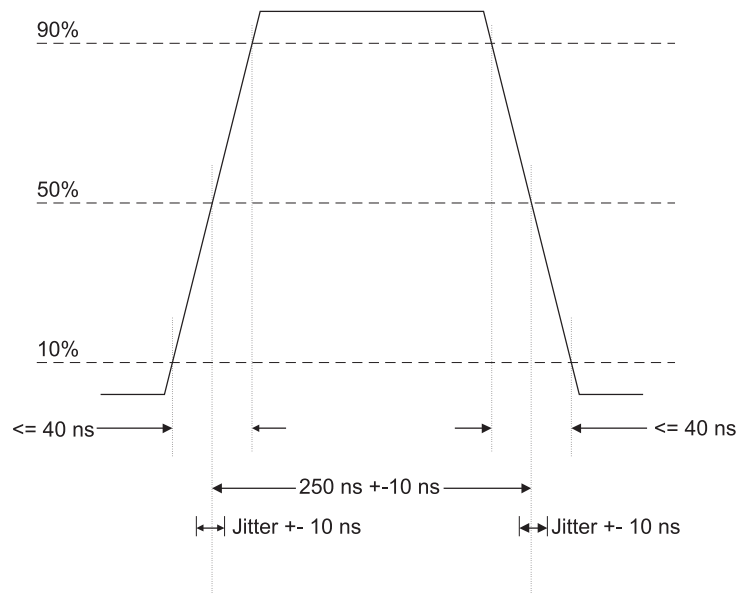


Figure 101—Basic pulse shape

16.3.3.3 Emitter radiation pattern mask

Currently the standard contains two emitter radiation pattern masks. Mask 1 is defined in Table 71 and illustrated in Figure 102. Mask 2 is defined in Table 72 and illustrated in Figure 104.

Table 71 – Definition of the emitter radiation pattern mask 1

Declination angle	Normalized irradiance
$\alpha \leq 60^\circ$	$> 3.5e-6$
$\alpha \leq 29^\circ$	$\leq 2.2e-5$
$29^\circ < \alpha \leq 43^\circ$	$\leq -1.06e-4 + (0.44e-5) \alpha$
$43^\circ < \alpha \leq 57^\circ$	$\leq 1.15e-4 - (7.1e-7) \alpha$
$57^\circ < \alpha \leq 74^\circ$	$\leq 2.98e-4 - (3.9e-6) \alpha$
$74^\circ < \alpha \leq 90^\circ$	$\leq 4.05e-5 - (4.5e-7) \alpha$

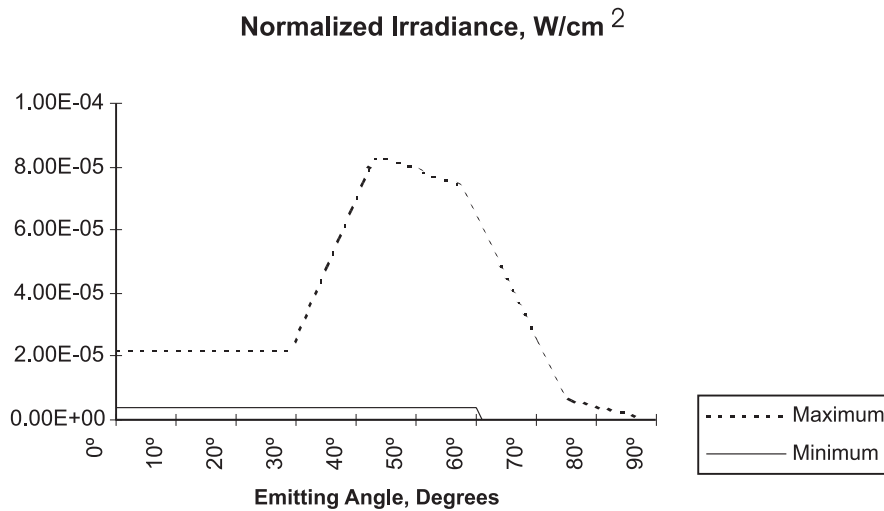


Figure 102 – Emitter radiation pattern mask 1

Following is a description of how to interpret the Mask 1 table and figure. Position the conformant Mask 1 device in its recommended attitude. Define the conformant Mask 1 device axis as the axis passing through the emitter center and having the direction of the vertical from the floor. The mask represents the irradiance normalized to the total peak emitted power, as a function of the angle between the conformant Mask 1 device axis and the axis from the emitter center to the test receiver center (declination angle). The distance between emitter and test receiver is 1 m. The test receiver normal is always aimed at the emitter center. The azimuth angle is a rotation angle on the conformant device axis.

A device is conformant if for any azimuth angle its radiation pattern as a function of declination angle falls within the pattern mask.

Figure 103 is a description of how to interpret the Mask 2 table with reference to Figure 104.

Table 72—Definition of emitter radiation pattern mask 2

Declination angle	Pitch angle	Normalized irradiance
$\alpha \leq 60$	$\alpha = 0$	$0.05 \pm 15\%$
$\alpha \leq 90$	$\alpha = 0$	$0.025 \pm 15\%$
$\alpha \geq 100$	$\alpha = 0$	≤ 0.015
$0 \leq \alpha \leq 60$	$0 \leq \alpha \leq 10$	$0.035 \leq I \leq 0.055$
$0 \leq \alpha \leq 60$	$10 \leq \alpha \leq 20$	$0.0225 \leq I \leq 0.05$
$0 \leq \alpha \leq 60$	$\alpha \geq 30$	≤ 0.015

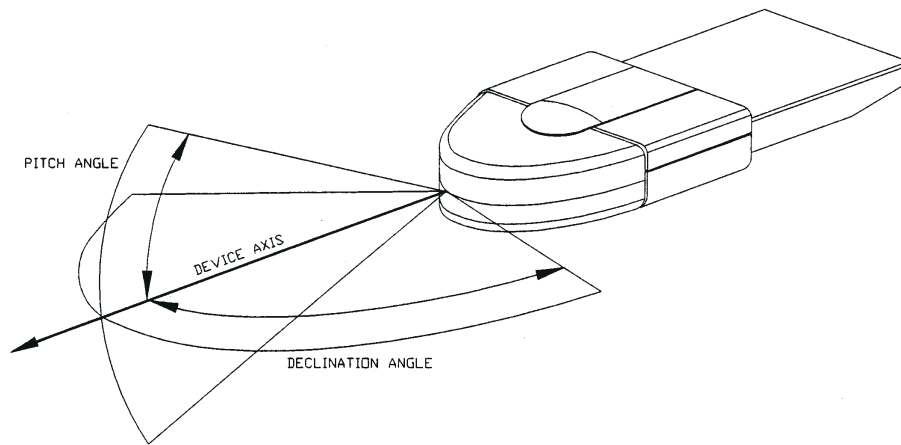


Figure 103—Mask 2 device orientation drawing

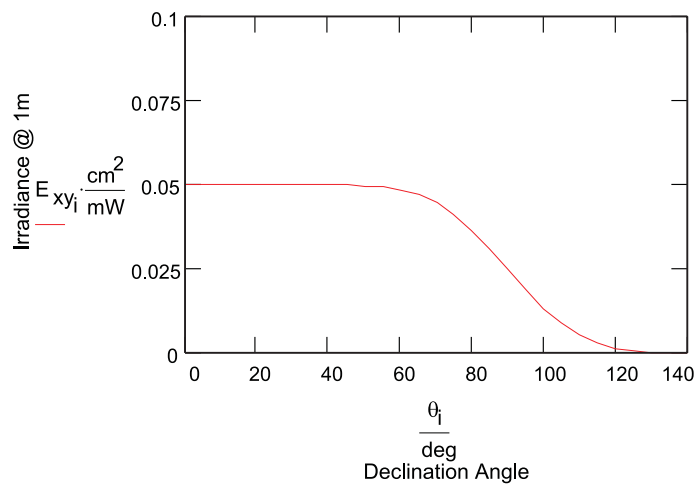


Figure 104—Emitter radiation pattern mask 2

Position the conformant Mask 2 device in its recommended attitude. Define the conformant Mask 2 device axis as passing through the emitter center and having the direction relative to the device as defined by the manufacturer. The declination angle plane is as defined by the manufacturer. The mask represents the irradiance normalized to the peak emitted power on the conformant Mask 2 device axis, as a function of the angle between the conformant device axis and the axis from the emitter center to the test receiver center (declination angle) in the declination plane. The distance between emitter and test receiver is 1 m. The test receiver normal is always aimed at the emitter center. The pitch angle is an angle relative to the conformant device axis which is perpendicular to the declination plane.

The device is conformant if, for a pitch angle of 0 degrees, at any declination angle from 0 to 100 degrees, and if, for any declination angle from 0 to 60 degrees, at any pitch angle from 0 to 20 degrees, its radiation pattern as a function of angle falls within the pattern mask.

Other radiation patterns are for future study.

16.3.3.4 Optical emitter peak wavelength

The optical emitter peak wavelength shall be between 850 and 950 nm.

16.3.3.5 Transmit spectrum mask

Define the transmit spectrum of a transmitter as the Fourier Transform, or equivalent, of a voltage (or current) signal whose amplitude, as a function of time, is proportional to the transmitted optical power.

The transmit spectrum of a conformant transmitter shall be 20 dB below its maximum for all frequencies above 15 MHz. The transmit spectrum mask is shown in Figure 105.

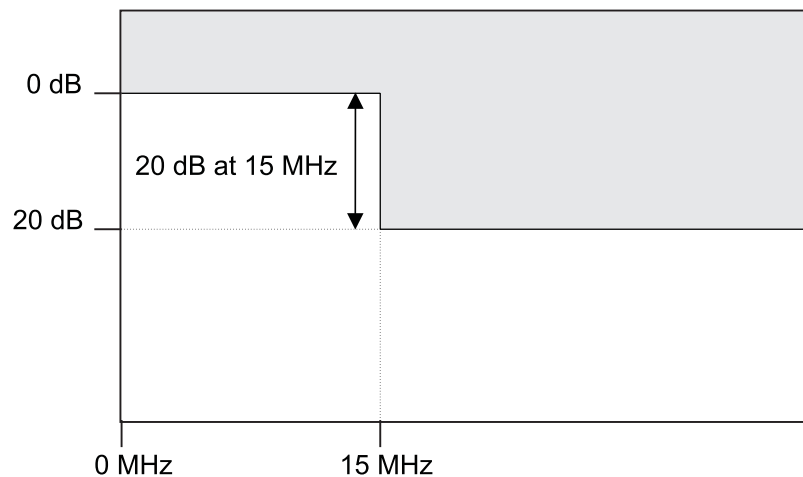


Figure 105—Transmit spectrum mask

16.3.4 PMD receiver specifications

The following subclauses describe the receive functions and parameters associated with the PMD sublayer.

16.3.4.1 Receiver sensitivity

The receiver sensitivity, defined as the minimum irradiance (in mW/cm^2) at the photodetector plane required for a frame error ratio (FER) of 4×10^{-5} with a PLCS DU of 512 octets and with an unmodulated background IR source between 800 nm and 1000 nm with a level of $0.1 \text{ mW}/\text{cm}^2$, shall be

- 1 Mbit/s: $2 \times 10^{-5} \text{ mW}/\text{cm}^2$
- 2 Mbit/s: $8 \times 10^{-5} \text{ mW}/\text{cm}^2$

16.3.4.2 Receiver dynamic range

The receiver dynamic range, defined as the ratio between the maximum and minimum irradiance at the plane normal to the receiver axis that assures an FER lower than or equal to 4×10^{-5} with a PLCS DU of 512 octets and with an unmodulated background IR source between 800 nm and 1000 nm with a level of $0.1 \text{ mW}/\text{cm}^2$, shall be $\geq 30 \text{ dB}$.

16.3.4.3 Receiver field-of-view (FOV)

The receiver axis is defined as the direction of incidence of the optical signal at which the received optical power is maximum.

The received optical power shall be greater than the values given in Table 73, at the angles indicated, where “angle of incidence” is the angle of incidence of the optical signal relative to the receiver axis, and “received power” is the received optical power as a percentage of that measured at the receiver axis.

Table 73—Definition of the receiver field of view

Angle of incidence	Received power
$\alpha \leq 20^\circ$	$\geq 65\%$
$\alpha \leq 40^\circ$	$\geq 55\%$
$\alpha \leq 60^\circ$	$\geq 35\%$
$\alpha \leq 80^\circ$	$\geq 10\%$

16.3.5 Energy Detect, Carrier Sense, and CCA definitions

16.3.5.1 Energy Detect (ED) signal

The ED signal shall be set true when IR energy variations in the band between 1 MHz and 10 MHz exceed $0.001 \text{ mW}/\text{cm}^2$.

The ED shall operate independently of the CS. ED shall not be asserted at the minimum signal level specified in 16.3.4.1, which is below the level specified in this subclause.

This signal is not directly available to the MAC.

16.3.5.2 Carrier Sense (CS) signal

The CS shall be asserted by the PHY when it detects and locks onto an incoming PLCP Preamble signal. Conforming PHYs shall assert this condition within the first $12 \mu\text{s}$ of signal reception, at the minimum signal

level equal to the receiver sensitivity specified in 16.3.4.1, with a background IR level as specified in 16.3.4.1.

The CS shall be deasserted by the PHY when the receiving conformant device loses carrier lock.

NOTE—The 12 μ s specification is somewhat less than the minimum length of the PLCP SYNC interval, which is 14.25 μ s.

The CS shall operate independently of ED and shall not require a prior ED before the acquisition and assertion of CS. This permits reception of signals at the minimum signal level specified in 16.3.4.1, even though these signals fall below the ED level.

This signal is not directly available to the MAC.

16.3.5.3 CCA

CCA shall be asserted “IDLE” by the PHY when the CS and the ED are both false, or when ED has been continuously asserted for a period of time defined by the product of `aCCAWatchdogTimerMax` and `aCCAWatchdogCountMax` without CS becoming active. When either CS or ED go true, CCA is indicated as “BUSY” to the MAC via the primitive `PHY-CCA.indicate`. CS and DE behavior are defined in 16.3.5.2.

Normally, CCA will be held “BUSY” throughout the period of the PLCP Header. After receiving the last PLCP bit and the first data octet, the PHY shall signal `PHY-RXSTART.indicate` with the parameters `LENGTH` and `RATE`. CCA shall be held “BUSY” until the number of octets specified in the decoded PLCP Header are received. At that time the PHY shall signal `PHY-RXEND.indicate`. The CCA may remain “BUSY” after the end of data if some form of energy is still being detected. The PHY will signal `PHY-CCA.indicate` with a value of `IDLE` only when the CCA goes “CLEAR.”

The transition of CCA from “BUSY” to “IDLE” is indicated to the MAC via the primitive `PHY-CCA.indicate`.

If CS and ED go false before the PHY signals `PHY-RXSTART.indicate`, CCA is set to “IDLE” and *immediately* signaled to the MAC via `PHY-CCA.indicate` with a value of `IDLE`. If CS and ED go false after the PHY has signaled `PHY-RXSTART.indicate`, implying that the PLCP Header has been properly decoded, then the PHY shall not signal a change in state of CCA until the proper interval has passed for the number of octets indicated by the received PLCP `LENGTH`. At that time, the PHY shall signal `PHY-RXEND.indicate` with an `RXERROR` parameter of `CarrierLost` followed by `PHY-CCA.indicate` with a value of `IDLE`.

The transition of CCA from “CLEAR” to “BUSY” resets the CCA watchdog timer and CCA watchdog counter. `aCCAWatchdogTimerMax` and `aCCAWatchdogCountMax` are parameters available via MIB entries and can be read and set via the LME.

Rise and fall times of CCA relative to the OR’ing of the CS and ED signals shall be less than 30 ns. CS and ED are both internal signals to the PHY and are not available directly to the MAC, nor are they defined at any exposed interface.

16.3.5.4 CHNL_ID

For the IR PHY, `CHNL_ID = X'01'` is defined as the baseband modulation method. All other values are not defined.

16.4 PHY attributes

PHY attributes have allowed values and default values that are PHY-dependent. Table 74 describes those values, and further specifies whether they are permitted to vary from implementation to implementation.

Table 74 does not provide the definition of the attributes, but only provides the IR PHY-specific values for the attributes whose definitions are in Clause 13 of this standard.

Table 74—IR PHY MIB attributes

PHY MIB object	Default value	Operational semantics	Operational behavior
aCCATime	5 μ s	Static	Identical for all conformant PHY
aRxTxTurnaroundTime	0 μ s	Static	Identical for all conformant PHY
aSlotTime	8 μ s	Static	Identical for all conformant PHY
aRxTxSwitchTime	0	Static	Identical for all conformant PHY
aTxRampOnTime	0	Static	Identical for all conformant PHY
aRxPLCPDelay	1 μ s	Static	Identical for all conformant PHY
aTxPLCPDelay	Implementation dependent	Static	Identical for all conformant PHY
aRxRFDelay	Implementation dependent	Static	Identical for all conformant PHY
aTxRFDelay	Implementation dependent	Static	Identical for all conformant PHY
aCCAWatchdogTimerMax	Implementation dependent	Dynamic	A conformant PHY may set this via the LME
aCCAWatchdogCountMax	Implementation dependent	Dynamic	A conformant PHY may set this via the LME
aCCAWatchdogTimerMin	22 μ s	Static	Identical for all conformant PHY
aCCAWatchdogCountMin	1	Static	Identical for all conformant PHY
aMACProcessingtDelay	2 μ s	Static	Identical for all conformant PHY
aTxRampOffTime	0 μ s	Static	Identical for all conformant PHY
aMPDUMaxLength	2500 octets	Static	Identical for all conformant PHY
aSIFSTime	7 μ s	Static	Identical for all conformant PHY
aSupportedRatesTx	Implementation dependent	Static	All conformant PHY must include the value X'02' (1 Mbit/s).
aSupportedRatesRx	Implementation dependent	Static	All conformant PHY must include the values X'02' (1 Mbit/s) and X'04' (2 Mbit/s).
aPHYType	03	Static	Identical for all conformant PHY
aCWmin	63	Static	Identical for all conformant PHY
aCWmax	1023	Static	Identical for all conformant PHY
aPLCPHeaderLength	41 μ s (1 Mbit/s) 25 μ s (2 Mbit/s)	Static	Identical for all conformant PHY
aPreambleLength	16 μ s (1 Mbit/s) 20 μ s (2 Mbit/s)	Static	Identical for all conformant PHY

Annex A

(normative)

Protocol Implementation Conformance Statement (PICS) proforma

A.1 Introduction

The supplier of a protocol implementation that is claimed to conform to IEEE Std 802.11-1997 shall complete the following PICS proforma.

A completed PICS proforma is the PICS for the implementation in question. The PICS is a statement of which capabilities and options of the protocol have been implemented. The PICS can have a number of uses, including use

- a) By the protocol implementor, as a checklist to reduce the risk of failure to conform to the standard through oversight;
- b) By the supplier and acquirer, or potential acquirer, of the implementation, as a detailed indication of the capabilities of the implementation, stated relative to the common basis for understanding provided by the standard PICS proforma;
- c) By the user, or potential user, of the implementation, as a basis for initially checking the possibility of interworking with another implementation (note that, while interworking can never be guaranteed, failure to interwork can often be predicted from incompatible PICS proformas);
- d) By a protocol tester, as the basis for selecting appropriate tests against which to assess the claim for conformance of the implementation.

A.2 Abbreviations and special symbols

A.2.1 Status symbols

M	mandatory
O	optional
O.<n>	optional, but support of at least one of the group of options labeled by the same numeral <n> is required
pred:	conditional symbol, including predicate identification

A.2.2 General abbreviations

N/A	not applicable
AD	address function capability
CF	implementation under test (IUT) configuration
FR	MAC frame capability
FS	frame sequence capability
PC	protocol capability
PICS	protocol implementation conformance statement

A.3 Instructions for completing the PICS proforma

A.3.1 General structure of the PICS proforma

The first part of the PICS proforma, Implementation Identification and Protocol Summary, is to be completed as indicated with the information necessary to identify fully both the supplier and the implementation.

The main part of the PICS proforma is a fixed questionnaire, divided into subclauses, each containing a number of individual items. Answers to the questionnaire items are to be provided in the rightmost column, either by simply marking an answer to indicate a restricted choice (usually Yes or No) or by entering a value or a set or a range of values. (Note that there are some items where two or more choices from a set of possible answers may apply. All relevant choices are to be marked, in these cases.)

Each item is identified by an item reference in the first column. The second column contains the question to be answered. The third column contains the reference or references to the material that specifies the item in the main body of IEEE Std 802.11-1997. The remaining columns record the status of each item, i.e., whether support is mandatory, optional, or conditional, and provide the space for the answers (see also A.3.4). Marking an item as supported is to be interpreted as a statement that all relevant requirements of the subclauses and normative annexes, cited in the References column for the item, are met by the implementation.

A supplier may also provide, or be required to provide, further information, categorized as either Additional Information or Exception Information. When present, each kind of further information is to be provided in a further subclause of items labeled A</> or X</>, respectively, for cross-referencing purposes, where </> is any unambiguous identification for the item (e.g., simply a numeral). There are no other restrictions on its format or presentation.

The PICS proforma for a station consists of A.4.1, through A.4.4 inclusive, and at least one of A.4.5, A.4.6, or A.4.7 corresponding to the PHY implemented.

A completed PICS proforma, including any Additional Information and Exception Information, is the PICS for the implementation in question.

NOTE—Where an implementation is capable of being configured in more than one way, a single PICS may be able to describe all such configurations. However, the supplier has the choice of providing more than one PICS, each covering some subset of the implementation's capabilities, if this makes for easier and clearer presentation of the information.

A.3.2 Additional Information

Items of Additional Information allow a supplier to provide further information intended to assist in the interpretation of the PICS. It is not intended or expected that a large quantity of information will be supplied, and a PICS can be considered complete without any such information. Examples of such Additional Information might be a outline of the ways in which an (single) implementation can be set up to operate in a variety of environments and configurations, or information about aspects of the implementation that are outside the scope of this standard but have a bearing upon the answers to some items.

References to items of Additional Information may be entered next to any answer in the questionnaire, and may be included in items of Exception Information.

A.3.3 Exception Information

It may happen occasionally that a supplier will wish to answer an item with mandatory status (after any conditions have been applied) in a way that conflicts with the indicated requirement. No preprinted answer will

be found in the Support column for this. Instead, the supplier shall write the missing answer into the Support column, together with an X</> reference to an item of Exception Information, and shall provide the appropriate rationale in the Exception Information item itself.

An implementation for which an Exception Information item is required in this way does not conform to IEEE Std 802.11-1997.

NOTE—A possible reason for the situation described above is that a defect in IEEE Std 802.11-1997 has been reported, a correction for which is expected to change the requirement not met by the implementation.

A.3.4 Conditional status

The PICS proforma contains a number of conditional items. These are items for which both the applicability of the item itself, and its status if it does apply, mandatory or optional, are dependent upon whether or not certain other items are supported.

Where a group of items is subject to the same condition for applicability, a separate preliminary question about the condition appears at the head of the group, with an instruction to skip to a later point in the questionnaire if the “Not Applicable” answer is selected. Otherwise, individual conditional items are indicated by a conditional symbol in the Status column.

A conditional symbol is of the form “<pred>:<S>”, where “<pred>” is a predicate as described below, and “<S>” is one of the status symbols M or O.

If the value of the predicate is true, the conditional item is applicable, and its status is given by S: the support column is to be completed in the usual way. Otherwise, the conditional item is not relevant and the Not Applicable (N/A) answer is to be marked.

A predicate is one of the following:

- a) An item-reference for an item in the PICS proforma: the value of the predicate is true if the item is marked as supported, and is false otherwise.
- b) A boolean expression constructed by combining item-references using the boolean operator OR: the value of the predicate is true if one or more of the items is marked as supported, and is false otherwise.

Each item referenced in a predicate, or in a preliminary question for grouped conditional items, is indicated by an asterisk in the Item column.

A.4 PICS proforma—IEEE Std 802.11-1997⁷

A.4.1 Implementation identification

Supplier	
Contact point for queries about the PICS	
Implementation Name(s) and Version(s)	
Other information necessary for full identification, e.g., name(s) and version(s) of the machines and/or operating systems(s), system names	

NOTES

1—Only the first three items are required for all implementations. Other information may be completed as appropriate in meeting the requirement for full identification.

2—The terms Name and Version should be interpreted appropriately to correspond with a supplier's terminology (e.g., Type, Series, Model).

A.4.2 Protocol summary, IEEE Std 802.11-1997

Identification of protocol standard	IEEE Std 802.11-1997
Identification of amendments and corrigenda to this PICS proforma that have been completed as part of this PICS	Amd. : Corr. : Amd. : Corr. :
Have any exception items been required? (See A.3.3; the answer Yes means that the implementation does not conform to IEEE Std 802.11-1997.)	Yes <input type="checkbox"/> No <input type="checkbox"/>

Date of statement (dd/mm/yy)	
------------------------------	--

⁷Copyright release for PICS proforma: Users of this standard may freely reproduce the PICS proforma in this annex so that it can be used for its intended purpose and may further publish the completed PICS.

A.4.3 IUT configuration

Item	IUT configuration	References	Status	Support
* CF1	What is the configuration of the IUT? Access Point (AP)	5.2	O.1	Yes <input type="checkbox"/> No <input type="checkbox"/>
* CF2	Independent station (<i>not</i> an AP)	5.2	O.1	Yes <input type="checkbox"/> No <input type="checkbox"/>
* CF3	Frequency-Hopping spread spectrum (FHSS) PHY for the 2.4 GHz band		O.2	Yes <input type="checkbox"/> No <input type="checkbox"/>
* CF4	Direct Sequence Spread Spectrum (DSSS) PHY for the 2.4 GHz band		O.2	Yes <input type="checkbox"/> No <input type="checkbox"/>
* CF5	Infrared PHY		O.2	Yes <input type="checkbox"/> No <input type="checkbox"/>

A.4.4 MAC protocol**A.4.4.1 MAC protocol capabilities**

Item	Protocol capability	References	Status	Support
PC1	Are the following MAC protocol capabilities supported? Authentication service	5.4.3.1, 5.4.3.2, 5.7.6, 5.7.7, 8.1, Annex C	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
PC1.1	Authentication state	5.5	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
PC1.2	Open System authentication	8.1.1	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
PC1.3	Shared Key authentication	8.1.2, 8.3	PC2:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
* PC2	WEP algorithm	5.4.3.3, 8.2, Annex C	O	Yes <input type="checkbox"/> No <input type="checkbox"/>
PC2.1	WEP Encryption procedure	8.2.3, 8.2.4, 8.2.5	PC2:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
PC2.2	WEP Decryption procedure	8.2.3, 8.2.4, 8.2.5	PC2:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
PC2.3	Security services management	8.3	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
PC3	Distributed Coordination function	9.1, 9.2, Annex C	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
PC3.1	Net Allocation Vector (NAV) function	9.2.1, 9.2.5, 9.3.2.2	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
PC3.2	Interframe space usage and timing	9.2.3, 9.2.5, 9.2.10	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
PC3.3	Random Backoff function	9.2.4	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
PC3.4	DCF Access procedure	9.2.5.1, 9.2.5.5	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
PC3.5	Random Backoff procedure	9.2.5.2	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
PC3.6	Recovery procedures and retransmit limits	9.2.5.3	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
PC3.7	RTS/CTS procedure	9.2.5.4, 9.2.5.6, 9.2.5.7	M	Yes <input type="checkbox"/> No <input type="checkbox"/>

A.4.4.1 MAC protocol capabilities (continued)

Item	Protocol capability	References	Status	Support
PC3.8	Directed MPDU transfer	9.2.6	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
PC3.9	Broadcast and multicast MPDU transfer	9.2.7	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
PC3.10	MAC level acknowledgment	9.2.2, 9.2.8	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
PC3.11	Duplicate detection and recovery	9.2.9	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
* PC4	Point coordinator (PC)	9.1, 9.3, Annex C	CF1:O	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
PC4.1	Maintenance of CFP structure and timing	9.3.1, 9.3.2	PC4:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
PC4.2	PCF MPDU transfer from PC	9.3.3	PC4:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
* PC4.3	PCF MPDU transfer to PC	9.3.3	PC4:O	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
PC4.4	Overlapping PC provisions	9.3.3.2	PC4:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
PC4.5	Polling list maintenance	9.3.4	PC4.3:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
* PC5	CF-Pollable	9.1, 9.3, Annex C	CF2:O	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
PC5.1	Interpretation of CFP structure and timing	9.3.1, 9.3.2	PC5:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
PC5.2	PCF MPDU transfer to/from and CF-Pollable STA	9.3.3	PC5:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
PC5.3	Polling list update	9.3.4	PC5:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
PC6	Fragmentation	9.2, 9.4, Annex C	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
PC7	Defragmentation	9.2, 9.5, Annex C	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
PC8	MAC data service	9.1.5, 9.8, Annex C	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
PC8.1	Reorderable-Multicast service class	9.8	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
PC8.2	StrictlyOrdered service class	9.8	O	Yes <input type="checkbox"/> No <input type="checkbox"/>
PC9	Multirate support	9.6, Annex C	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
* PC10	Multiple outstanding MSDU support	9.8, Annex C	O	Yes <input type="checkbox"/> No <input type="checkbox"/>
PC10.1	Multiple outstanding MSDU transmission restrictions	9.8	PC10:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
PC11	Timing synchronization	11.1, Annex C	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
PC11.1	Timing in an infrastructure network	11.1.1.1, 11.1.4	CF1:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
PC11.2	Timing in an Independent BSS (IBSS)	11.1.1.2, 11.1.4	CF2:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
PC11.3	Beacon Generation function	11.1.2	M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
PC11.5	TSF synchronization and accuracy	11.1.2	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
PC11.5	Infrastructure BSS initialization	11.1.3	CF1:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
PC11.6	Independent BSS initialization	11.1.3	CF2:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
PC11.7	Passive scanning	11.1.3	CF2:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
PC11.8	Active scanning	11.1.3	CF2:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>

A.4.4.1 MAC protocol capabilities (continued)

Item	Protocol capability	References	Status	Support
PC11.9	Probe response	11.1.3	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
PC11.10	Hop Synchronization function	11.1.5	CF3:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
PC12	Infrastructure power management	11.2.1, Annex C	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
PC12.1	Station power management modes	11.2.1.1, 11.2.1.8	CF2:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
PC12.2	TIM transmission	11.2.1.2, 11.2.1.3	CF1:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
PC12.3	AP function during CP	11.2.1.4	CF1:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
PC12.4	AP function during CFP	11.2.1.5	PC4:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
PC12.5	Receive function during CP	11.2.1.6	CF2:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
PC12.6	Receive function during CFP	11.2.1.7	PC5:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
PC12.7	Aging function	11.2.1.9	CF1:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
PC13	IBSS power management	11.2.2, Annex C	CF2:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
PC13.1	Initialization of power management	11.2.2.2	CF2:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
PC13.2	STA power state transitions	11.2.2.3	CF2:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
PC13.3	ATIM and frame transmission	11.2.2.4	CF2:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
PC14	Association and reassociation	5.4, 5.7, 11.3, Annex C	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
PC14.1	Association state	5.5	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
PC14.2	STA association procedure	11.3.1	CF2:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
PC14.3	AP association procedure	11.3.2	CF1:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
PC14.4	STA reassociation procedure	11.3.3	CF2:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
PC14.5	AP reassociation procedure	11.3.4	CF1:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
PC15	Management information base (MIB)	11.4, Annex C	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
PC15.1	SMT object class	11.4.2.1	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
* PC15.2	Privacy package	11.4.2.1	PC2:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
PC15.3	MAC object class	11.4.2.2	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
* PC15.4	MAC statistics package	11.4.2.2	O	Yes <input type="checkbox"/> No <input type="checkbox"/>
PC15.3	Resource type object class	11.4.2.3	M	Yes <input type="checkbox"/> No <input type="checkbox"/>

A.4.4.2 MAC frames

Item	MAC frame	References	Status	Support
	Is transmission of the following MAC frames supported?	7, Annex C		
FT1	Association request	7	CF2:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
FT2	Association response	7	CF1:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
FT3	Reassociation request	7	CF2:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
FT4	Reassociation response	7	CF1:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
FT5	Probe request	7	CF2:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
FT6	Probe response	7	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FT7	Beacon	7	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FT8	ATIM	7	CF2:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
FT9	Disassociation	7	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FT10	Authentication	7	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FT11	Deauthentication	7	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FT12	PS-Poll	7	CF2:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
FT13	RTS	7	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FT14	CTS	7	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FT15	ACK	7	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FT16	CF-End	7	PC4:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
FT17	CF End+CF-Ack	7	PC4:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
FT18	Data	7	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FT19	Data + CF-Ack	7	(PC4 OR PC5):M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
FT20	Data + CF-Poll	7	PC4.3:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
FT21	Data + CF-Ack+CF-Poll	7	PC4.3:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
FT22	Null	7	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FT23	CF-Ack (no data)	7	(PC4 OR PC5):M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
FT24	CF-Poll (no data)	7	PC4.3:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
FT25	CF-Ack+CF-Poll (no data)	7	PC4.3:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
	Is reception of the following MAC frames supported?	7, Annex C		
FR1	Association request	7	CF1:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
FR2	Association response	7	CF2:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
FR3	Reassociation request	7	CF1:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
FR4	Reassociation response	7	CF2:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
FR5	Probe request	7	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FR6	Probe response	7	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FR7	Beacon	7	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FR8	ATIM	7	CF2:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
FR9	Disassociation	7	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FR10	Authentication	7	M	Yes <input type="checkbox"/> No <input type="checkbox"/>

A.4.4.2 MAC frames (continued)

Item	MAC frame	References	Status	Support
FR11	Deauthentication	7	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FR12	PS-Poll	7	CF1:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
FR13	RTS	7	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FR14	CTS	7	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FR15	ACK	7	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FR16	CF-End	7	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FR17	CF End+CF-Ack	7	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FR18	Data	7	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FR19	Data + CF-Ack	7	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FR20	Data + CF-Poll	7	PC5:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
FR21	Data + CF-Ack+CF-Poll	7	PC5:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
FR22	Null	7	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FR23	CF-Ack (no data)	7	(PC4 OR PC5):M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
FR24	CF-Poll (no data)	7	PC5:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
FR25	CF-Ack+CF-Poll (no data)	7	PC5:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>

A.4.4.3 Frame exchange sequences

Item	Frame exchange sequence	References	Status	Support
	Are the following frame sequences supported?			
FS1	Basic frame sequences	9.7, Annex C	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FS2	CF-Frame sequences	9.7, Annex C	(PC4 OR PC5):M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>

A.4.4.4 MAC addressing functions

Item	MAC Address function	References	Status	Support
	Are the following MAC Addressing functions supported?			
AD1	STA universal individual IEEE802 address	5.3.3, 7.1.3.3	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
AD2	BSS identifier generation	7.1.3.3, 11.1.3, Annex C	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
AD3	Receive address matching	7.1.3.3, 7.2.2, Annex C	M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>

A.4.5 Frequency-Hopping PHY functions

Item	Protocol feature	References	Status	Support
FH1	Which requirements and options does the PHY support? PHY service primitive parameters			
FH1.1	TXVECTOR parameter: LENGTH	14.2.2.1	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH1.2	TXVECTOR parameter: PLCPBITRATE	14.2.2.2	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH1.2.1	PLCPBITRATE = X'00' (1.0 Mbit/s)	14.2.2.2	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
* FH1.2.2	PLCPBITRATE = X'02' (2.0 Mbit/s)	14.2.2.2	O	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH1.3	RXVECTOR parameter: LENGTH	14.2.3.1	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH1.4	RXVECTOR parameter: RSSI	14.2.3.2	O	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH2	PLCP frame format			
FH2.1	PLCP Preamble: Sync	14.3.2.1.1	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH2.2	PLCP Preamble: Start Frame Delimiter	14.3.2.1.2	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH2.3	PLCP Header: Length Word	14.3.2.2.1	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH2.4	PLCP Header: Signaling field	14.3.2.2.2	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH2.5	PLCP Header: Header Error Check	14.3.2.2.3	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH2.6	PLCP Data Whitener: Scrambling and bias suppression encoding	14.3.2.3, 14.3.3.1.1	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH3	PLCP Transmit procedure			
FH3.1	Transmit: transmit on MAC request	14.3.3.1.1	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH3.2	Transmit: format and whiten frame	14.3.3.1.1	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH3.3	Transmit: Timing	14.3.3.1.1	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH4	PLCP CS/CCA procedure			
FH4.1	CS/CCA: perform on a minimum of one antenna	14.3.3.2.1	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH4.2	CS/CCA: Detect preamble starting up to 20 μ s after start of slot time	14.3.3.2.1	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH4.3	CS/CCA: Detect preamble starting at least 16 μ s prior to end of slot time	14.3.3.2.1	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH4.4	CS/CCA: Detect random data	14.3.3.2.1	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH4.5	CS/CCA: Perform on antenna with essentially same gain and pattern as transmit antenna	14.3.3.2.1	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH4.6	CS/CCA: Detect valid SFD and PLCP header	14.3.3.2.1	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH4.7	CS/CCA: Maintain BUSY indication until end of length contained in valid PLCP header	14.3.3.2.1	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH5	PLCP Receive procedure			
FH5.1	Receive: Receive and dewhiten frame	14.3.3.3.1	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH6	PHY LME			
FH6.1	PLME: Support FH sync	14.4.2.2	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH6.2	PLME: Support PLME primitives	14.4.3.2	O	Yes <input type="checkbox"/> No <input type="checkbox"/>

A.4.5 Frequency-Hopping PHY functions (continued)

Item	Protocol feature	References	Status	Support
FH7	Geographic area specific requirements			
* FH7.1	Geographic areas			
FH7.1.1	North America	14.6.2	O.1	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH7.1.2	Most of Europe	14.6.2	O.1	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH7.1.3	Japan	14.6.2	O.1	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH7.1.4	Spain	14.6.2	O.1	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH7.1.5	France	14.6.2	O.1	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH7.2	Operating frequency range	14.6.3	FH7.1:M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH7.3	Number of operating channels	14.6.4	FH7.1:M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH7.4	Operating channel frequencies	14.6.5	FH7.1:M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH7.5	Occupied channel bandwidth	14.6.6	FH7.1:M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH7.6	Minimum hop rate	14.6.7	FH7.1:M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH7.7	Hop sequences	14.6.8	FH7.1:M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH7.8	Unwanted emissions	14.6.9	FH7.1:M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH8	1 Mbit/s PMD			
FH8.1	Modulation 2GFSK, BT=0.5, 1=positive freq. dev, 0=negative freq. dev.	14.6.10	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH8.2	Peak frequency deviation	14.6.10	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH8.3	Zero-Crossing error	14.6.10	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH8.4	Nominal channel data rate	14.6.11	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH8.5	Channel switching/settling time	14.6.12	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH8.6	Receive to transmit switch time	14.6.13	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH8.7	Nominal transmit power	14.6.14.1	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH8.8	Transmit power levels	14.6.14.2	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH8.9	Transmit power level control to <100 mW	14.6.14.3	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH8.10	Transmit spectrum shape	14.6.14.4	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH8.11	Transmit center frequency tolerance	14.6.14.5	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH8.12	Transmitter ramp periods	14.6.14.6	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH8.13	Receiver input dynamic range	14.6.15.1	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH8.14	Receiver center frequency acceptance range	14.6.15.2	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH8.15	Clear channel assessment power threshold for Pdet 90% (preamble)/70% (random data) for 100 mW units	14.6.15.3	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH8.16	Clear channel assessment power threshold for units >100 mW; sensitivity threshold is 1/2 dB lower for every dB above 20 dBm	14.6.15.3	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH8.17	Minimum receiver sensitivity at FER=3% with 400 octet frames	14.6.15.4	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH8.18	Intermodulation protection	14.6.15.5	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH8.19	Desensitization	14.6.15.6	M	Yes <input type="checkbox"/> No <input type="checkbox"/>

A.4.5 Frequency-Hopping PHY functions (continued)

Item	Protocol feature	References	Status	Support
FH8.20	Operating temperature range	14.6.16	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH8.20.1	Temperature type 1	14.6.16	O	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH8.20.2	Temperature type 2	14.6.16	O	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH8.20.3	Temperature type 3	14.6.16	O	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH9	2 Mbit/s PMD			
FH9.1	All 1M PMD requirements	14.7.1	FH1.2.2:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
FH9.2	Modulation 4GFSK, BT=0.5	14.7.2	FH1.2.2:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
FH9.3	Frame structure for 2M PHY	14.7.2.1	FH1.2.2:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
FH9.4	Nominal channel data rate	14.7.3	FH1.2.2:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
FH9.5	Input dynamic range	14.7.4	FH1.2.2:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
FH9.6	Minimum receiver sensitivity at FER=3% with 400 octet frames	14.7.5	FH1.2.2:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
FH9.7	Intermodulation protection	14.7.6	FH1.2.2:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
FH9.8	Desensitization	14.7.7	FH1.2.2:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
FH10	MIB	13.1, 14.8, Annex C	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
FH10.1	PHY object class	13.1,14.8	M	Yes <input type="checkbox"/> No <input type="checkbox"/>

A.4.6 Direct sequence PHY functions

Item	PHY feature	References	Status	Support
DS1	PLCP sublayer procedures	15.2		
DS1	Preamble prepend on TX	15.2.1	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
DS1.1	PLCP frame format	15.2.2, 15.2.3	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
DS1.2	PLCP integrity check generation	15.2.3, 15.2.3.6	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
DS1.3	TX rate change capability	15.2.3.3, 15.2.5	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
DS1.4	Supported data rates	15.1, 15.2.3.3	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
DS1.5	Data whitener scrambler	15.2.4	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
DS1.6	Scrambler initialization	15.2.4	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
DS2	Preamble process on RX	15.2.1		
DS2.1	PLCP frame format	15.2.2, 15.2.3	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
DS2.2	PLCP integrity check verify	15.2.3, 15.2.3.6	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
DS2.3	RX Rate change capability	15.2.3.3, 15.2.5	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
DS2.4	Data whitener descrambler	15.2.4	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
DS3	PN code sequence	15.4.6.3	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
DS4	Chipping continue on power down	15.2.6	O	Yes <input type="checkbox"/> No <input type="checkbox"/>
*DS5	Operating channel capability	15.2.6, 15.4.6.2		
* DS5.1	North America (FCC)	15.2.6, 15.4.6.2	DS5:O.1	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
DS5.1.1	channel 1	15.2.6, 15.4.6.2	DS5.1:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>

A.4.6 Direct sequence PHY functions (continued)

Item	PHY feature	References	Status	Support
DS5.1.2	channel 2	15.2.6, 15.4.6.2	DS5.1:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
DS5.1.3	channel 3	15.2.6, 15.4.6.2	DS5.1:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
DS5.1.4	channel 4	15.2.6, 15.4.6.2	DS5.1:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
DS5.1.5	channel 5	15.2.6, 15.4.6.2	DS5.1:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
DS5.1.6	channel 6	15.2.6, 15.4.6.2	DS5.1:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
DS5.1.7	channel 7	15.2.6, 15.4.6.2	DS5.1:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
DS5.1.8	channel 8	15.2.6, 15.4.6.2	DS5.1:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
DS5.1.9	channel 9	15.2.6, 15.4.6.2	DS5.1:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
DS5.1.10	channel 10	15.2.6, 15.4.6.2	DS5.1:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
DS5.1.11	channel 11	15.2.6, 15.4.6.2	DS5.1:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
* DS5.2	Canada (IC)	15.2.6, 15.4.6.2	DS5:O.1	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
DS5.2.1	channel 1	15.2.6, 15.4.6.2	DS5.2:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
DS5.2.2	channel 2	15.2.6, 15.4.6.2	DS5.2:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
DS5.2.3	channel 3	15.2.6, 15.4.6.2	DS5.2:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
DS5.2.4	channel 4	15.2.6, 15.4.6.2	DS5.2:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
DS5.2.5	channel 5	15.2.6, 15.4.6.2	DS5.2:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
DS5.2.6	channel 6	15.2.6, 15.4.6.2	DS5.2:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
DS5.2.7	channel 7	15.2.6, 15.4.6.2	DS5.2:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
DS5.2.8	channel 8	15.2.6, 15.4.6.2	DS5.2:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
DS5.2.9	channel 9	15.2.6, 15.4.6.2	DS5.2:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
DS5.2.10	channel 10	15.2.6, 15.4.6.2	DS5.2:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
DS5.2.11	channel 11	15.2.6, 15.4.6.2	DS5.2:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
* DS5.3	Europe (ETSI)	15.2.6, 15.4.6.2	DS5:O.1	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
DS5.3.1	channel 1	15.2.6, 15.4.6.2	DS5.3:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
DS5.3.2	channel 2	15.2.6, 15.4.6.2	DS5.3:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
DS5.3.3	channel 3	15.2.6, 15.4.6.2	DS5.3:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
DS5.3.4	channel 4	15.2.6, 15.4.6.2	DS5.3:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
DS5.3.5	channel 5	15.2.6, 15.4.6.2	DS5.3:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
DS5.3.6	channel 6	15.2.6, 15.4.6.2	DS5.3:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
DS5.3.7	channel 7	15.2.6, 15.4.6.2	DS5.3:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
DS5.3.8	channel 8	15.2.6, 15.4.6.2	DS5.3:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
DS5.3.9	channel 9	15.2.6, 15.4.6.2	DS5.3:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
DS5.3.10	channel 10	15.2.6, 15.4.6.2	DS5.3:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
DS5.3.11	channel 11	15.2.6, 15.4.6.2	DS5.3:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
DS5.3.12	channel 12	15.2.6, 15.4.6.2	DS5.3:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
DS5.3.13	channel 13	15.2.6, 15.4.6.2	DS5.3:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
* DS5.4	France	15.2.6, 15.4.6.2	DS5:O.1	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
DS5.4.1	channel 10	15.2.6, 15.4.6.2	DS5.4:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
DS5.4.2	channel 11	15.2.6, 15.4.6.2	DS5.4:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
DS5.4.3	channel 12	15.2.6, 15.4.6.2	DS5.4:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>

A.4.6 Direct sequence PHY functions (continued)

Item	PHY feature	References	Status	Support
DS5.4.4	channel 13	15.2.6, 15.4.6.2	DS5.4:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
* DS5.5	Spain	15.2.6, 15.4.6.2	DS5:O.1	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
DS5.5.1	channel 10	15.2.6, 15.4.6.2	DS5.5:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
DS5.5.2	channel 11	15.2.6, 15.4.6.2	DS5.5:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
* DS5.6	Japan (RCR)	15.2.6, 15.4.6.2	DS5:O.1	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
DS6	Bits to symbol mapping	15.4.6.4		
DS6.1	1 Mbit/s	15.4.6.4	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
DS6.2	2 Mbit/s	15.4.6.4	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
*DS7	CCA functionality	15.4.8.4		
DS7.1	Energy Only (RSSI above threshold)	15.4.8.4	DS7:O.2	Yes <input type="checkbox"/> No <input type="checkbox"/>
DS7.2	IEEE 802.11 DSSS correlation	15.4.8.4	DS7:O.2	Yes <input type="checkbox"/> No <input type="checkbox"/>
DS7.3	Both methods	15.4.8.4	DS7:O.2	Yes <input type="checkbox"/> No <input type="checkbox"/>
DS7.4	Hold CCA busy for packet duration of a correctly received PLCP but carrier lost during reception of MPDU	15.2.7	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
DS7.5	Hold CCA busy for packet duration of a correctly received but out of spec PLCP	15.2.7	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
DS8	Transmit antenna selection	15.4.5.5, 15.4.5.6	O	Yes <input type="checkbox"/> No <input type="checkbox"/>
DS9	Receive antenna diversity	15.4.5.5, 15.4.5.6, 15.4.5.7	O	Yes <input type="checkbox"/> No <input type="checkbox"/>
*DS10	Antenna port(s) availability	15.4.6.9	O	Yes <input type="checkbox"/> No <input type="checkbox"/>
DS10.1	50 Ω impedance	15.4.6.9	DS10:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
*DS11	Transmit power level support	15.4.5.8, 15.4.7.3	O	Yes <input type="checkbox"/> No <input type="checkbox"/>
DS11.1	If greater than 100 mW capability	15.4.7.3	DS11:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
*DS12	Radio type (temperature range)	15.4.6.10		
DS12.1	Type 1	15.4.6.10	DS12:O.3	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
DS12.2	Type 2	15.4.6.10	DS12:O.3	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
DS13	Spurious emissions conformance	15.4.6.5	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
DS14	TX-RX turnaround time	15.4.6.6	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
DS15	RX-TX turnaround time	15.4.6.7	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
DS16	Slot time	15.4.6.8	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
DS17	ED reporting time	15.4.6.8, 15.4.8.4	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
DS18	Minimum transmit power level	15.4.7.2	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
DS19	Transmit spectral mask conformance	15.4.7.4	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
DS20	Transmitted center frequency tolerance	15.4.7.5	M	Yes <input type="checkbox"/> No <input type="checkbox"/>

A.4.6 Direct sequence PHY functions (continued)

Item	PHY feature	References	Status	Support
DS21	Chip clock frequency tolerance	15.4.7.6	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
DS22	Transmit power on ramp	15.4.7.7	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
DS23	Transmit power down ramp	15.4.7.7	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
DS24	RF carrier suppression	15.4.7.8	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
DS25	Transmit modulation accuracy	15.4.7.9	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
DS26	Receiver minimum input level sensitivity	15.4.8.1	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
DS27	Receiver maximum input level	15.4.8.2	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
DS28	Receiver adjacent channel rejection	15.4.8.3	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
DS29	MIB	13.1, 15.3.4, Annex C	M	Yes <input type="checkbox"/> No <input type="checkbox"/>
DS29.1	PHY object class	13.1, 15.3.4	M	Yes <input type="checkbox"/> No <input type="checkbox"/>

A.4.7 Infrared baseband PHY functions

Item	Feature	References	Status	Support
IR1	Is the transmitted SYNC field length in the range of required number of PPM slots, with the absence of a pulse in the last slot of the field?	16.2.4.1	M	Yes <input type="checkbox"/>
IR2	Is the transmitted SYNC field entirely populated by alternating presence and absence of pulses in consecutive PPM slots, with the absence of a pulse in the last slot of the field?	16.2.4.1	M	Yes <input type="checkbox"/>
IR3	Is the transmitted SFD field the binary sequence 1001, where 1 indicates a pulse in the PPM slot and 0 indicates no pulse in the PPM slot?	16.2.4.2	M	Yes <input type="checkbox"/>
IR4	Is the transmitted DR field pulse sequence equal to the correct value for the data rate provided by the TXVECTOR parameter PLCP BITRATE, where 1 indicates a pulse in the PPM slot and 0 indicates no pulse in the PPM slot?	16.2.4.3	M	Yes <input type="checkbox"/>
IR5	Is the transmitted DCLA field 32 PPM slots long with the specified sequence for 1 Mbit/s, where 1 indicates a pulse in the PPM slot and 0 indicates no pulse in the PPM slot? 1 Mbit/s: 00000000100000000000000010000000	16.2.4.4	M	Yes <input type="checkbox"/>
* IR5a	Does the unit support 2 Mbit/s transmission?	16.2.4.4	O	Yes <input type="checkbox"/> No <input type="checkbox"/>
IR5b	If the unit supports 2 Mbit/s transmission, is the transmitted DCLA field 32 PPM slots long with the specified sequence for 2 Mbit/s, where 1 indicates a pulse in the PPM slot and 0 indicates no pulse in the PPM slot? 2 Mbit/s: 00100010001000100010001000100010	16.2.4.4	IR5a:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>

A.4.7 Infrared baseband PHY functions (continued)

Item	Feature	References	Status	Support
IR6	Is the transmitted LENGTH field the correct PPM representation of the unsigned 16-bit binary integer, lsb transmitted first, equal to the correct value provided by the TXVECTOR parameter LENGTH?	16.2.4.5	M	Yes <input type="checkbox"/>
IR7	Is the transmitted CRC field the correct PPM representation of the CRC value calculated as per reference subclause, transmitted lsb first?	16.2.4.6	M	Yes <input type="checkbox"/>
IR8	Is the transmitted PSDU field the correct PPM representation of the PSDU, transmitted lsb first?	16.2.4.7	M	Yes <input type="checkbox"/>
IR9	When the CCA is false does transmission begin based on PHYTXSTART.request?	16.2.5.1	M	Yes <input type="checkbox"/>
IR10	Does the PHY issue a PHYTXSTART.confirm after the transmission of the PLCP header?	16.2.5.1	M	Yes <input type="checkbox"/>
IR11	Does the PHY accept each octet of the PSDU in a PHYDATA.request and answer with a PHYDATA.confirm?	16.2.5.1	M	Yes <input type="checkbox"/>
IR12	Does the PHY cease transmission in response to a PHYTXEND.request and answer with a PHYTXEND.confirm?	16.2.5.1	M	Yes <input type="checkbox"/>
IR13	Does the PHY of a receiving STA send a PHYCCA.indicate during reception of the SYNC field?	16.2.5.2	M	Yes <input type="checkbox"/>
IR14	Does the PHY of a receiving STA properly receive a transmission that changes data rate according to the DR field?	16.2.5.2	M	Yes <input type="checkbox"/>
IR15	Does the PHY of a receiving STA properly reject an incorrect CRC?	16.2.5.2	M	Yes <input type="checkbox"/>
IR16	Does the PHY of a receiving STA properly reject a DR field other than those specified in reference subclause?	16.2.5.2, 16.2.4.3	M	Yes <input type="checkbox"/>
IR17	Does the PHY of a receiving STA send PHYRXSTART.indicate with correct RATE and LENGTH parameters after proper reception of PLCP preamble and PLCP header?	16.2.5.2	M	Yes <input type="checkbox"/>
IR18	Does the PHY of a receiving STA forward receive octets in PHYDATA.indicate primitives?	16.2.5.2	M	Yes <input type="checkbox"/>
IR19	Does the PHY of a receiving STA send a PHYRXEND.indicate after the final octet indicated by the LENGTH field?	16.2.5.2	M	Yes <input type="checkbox"/>
IR20	Does the PHY of a receiving STA send a PHYCCA.indicate with a state value of IDLE after the PHYRXEND.indicate?	16.2.5.2	M	Yes <input type="checkbox"/>
IR21	Does the PHY reset its CCA detection mechanism upon receiving a PHYCCARST.request, and respond with a PHYCCARST.indicate?	16.2.5.3	M	Yes <input type="checkbox"/>

A.4.7 Infrared baseband PHY functions (continued)

Item	Feature	References	Status	Support
IR22	When transmitting at 1 Mbit/s does the PHY transmit PPM symbols according the 16-PPM Basic Rate Mapping table, transmitting from left to right?	16.3.2.1, 16.3.2.2	M	Yes <input type="checkbox"/>
IR23	When transmitting at 2 Mbit/s does the PHY transmit PPM symbols according to the 4-PPM Enhanced Rate Mapping table, transmitting from left to right?	16.3.2.1, 16.3.2.2	IR5a:M	Yes <input type="checkbox"/>
IR24	Does the PHY operate over a temperature range of 0 to 40 °C?	16.3.2.4	M	Yes <input type="checkbox"/>
* IR25	If the unit is conformant to emitter radiation mask 1, is the peak optical power of an emitted pulse within the specification range averaged over the pulse width?	16.3.3.1	O.1	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
* IR26	If the unit is conformant to emitter radiation mask 2, is the peak optical power of an emitted pulse within the specification range averaged over the pulse width?	16.3.3.1	O.1	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
IR27	Does the transmitted pulse shape conform to the description of the reference subclause?	16.3.3.2	M	Yes <input type="checkbox"/>
IR28	Does the emitter radiation pattern as a function of angle conform to the requirements of the reference subclause as applicable based on conformance to emitter radiation mask 1?	16.3.3.3	IR25:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
IR28a	Does the emitter radiation pattern as a function of angle conform to the requirements of the reference subclause as applicable based on conformance to emitter radiation mask 2?	16.3.3.3	IR26:M	Yes <input type="checkbox"/> No <input type="checkbox"/> N/A <input type="checkbox"/>
IR29	Is the peak emitter optical output as a function of wavelength in the range specified?	16.3.3.4	M	Yes <input type="checkbox"/>
IR30	Does the spectrum of the transmit signal amplitude as a voltage or current meet the requirements of the reference subclause?	16.3.3.5	M	Yes <input type="checkbox"/>
IR31	Does the receiver sensitivity meet the requirements of the reference subclause for receive signals of both 1 and 2 Mbit/s?	16.3.4.1	M	Yes <input type="checkbox"/>
IR32	Does the receiver exhibit a dynamic range as specified in reference subclause?	16.3.4.2	M	Yes <input type="checkbox"/>
IR33	Does the receiver field of view conform to the requirements of the reference subclause?	16.3.4.3	M	Yes <input type="checkbox"/>
IR34	When it is known that the conditions are such that the Carrier Detect Signal and the Energy Detect Signal are false is the CCA asserted IDLE?	16.3.5.1	M	Yes <input type="checkbox"/>

A.4.7 Infrared baseband PHY functions (continued)

Item	Feature	References	Status	Support
IR35	When the conditions are such that Energy Detect is true for greater then the time defined in reference subclause, does CCA become IDLE?	16.3.5.1	M	Yes <input type="checkbox"/>
IR36	When conditions are such that either Carrier Detect or Energy Detect go true, does CCA go BUSY?	16.3.5.1	M	Yes <input type="checkbox"/>
IR37	Is the MIB completely supported?	16.4	M	Yes <input type="checkbox"/>

Annex B

(informative)

Hopping sequences

The following tables pertain to the hopping sequences for North America and ETSI.

Table B.1—Hopping sequence set 1

index	0	3	6	9	12	15	18	21	24	27	30	33	36
1	2	5	8	11	14	17	20	23	26	29	32	35	38
2	25	28	31	34	37	40	43	46	49	52	55	58	61
3	64	67	70	73	76	79	3	6	9	12	15	18	21
4	10	13	16	19	22	25	28	31	34	37	40	43	46
5	45	48	51	54	57	60	63	66	69	72	75	78	2
6	18	21	24	27	30	33	36	39	42	45	48	51	54
7	73	76	79	3	6	9	12	15	18	21	24	27	30
8	49	52	55	58	61	64	67	70	73	76	79	3	6
9	21	24	27	30	33	36	39	42	45	48	51	54	57
10	63	66	69	72	75	78	2	5	8	11	14	17	20
11	78	2	5	8	11	14	17	20	23	26	29	32	35
12	31	34	37	40	43	46	49	52	55	58	61	64	67
13	61	64	67	70	73	76	79	3	6	9	12	15	18
14	24	27	30	33	36	39	42	45	48	51	54	57	60
15	54	57	60	63	66	69	72	75	78	2	5	8	11
16	65	68	71	74	77	80	4	7	10	13	16	19	22
17	28	31	34	37	40	43	46	49	52	55	58	61	64
18	79	3	6	9	12	15	18	21	24	27	30	33	36
19	33	36	39	42	45	48	51	54	57	60	63	66	69
20	4	7	10	13	16	19	22	25	28	31	34	37	40
21	20	23	26	29	32	35	38	41	44	47	50	53	56
22	13	16	19	22	25	28	31	34	37	40	43	46	49
23	38	41	44	47	50	53	56	59	62	65	68	71	74
24	74	77	80	4	7	10	13	16	19	22	25	28	31
25	56	59	62	65	68	71	74	77	80	4	7	10	13
26	71	74	77	80	4	7	10	13	16	19	22	25	28
27	23	26	29	32	35	38	41	44	47	50	53	56	59
28	5	8	11	14	17	20	23	26	29	32	35	38	41
29	39	42	45	48	51	54	57	60	63	66	69	72	75
30	12	15	18	21	24	27	30	33	36	39	42	45	48
31	36	39	42	45	48	51	54	57	60	63	66	69	72
32	68	71	74	77	80	4	7	10	13	16	19	22	25
33	9	12	15	18	21	24	27	30	33	36	39	42	45
34	70	73	76	79	3	6	9	12	15	18	21	24	27
35	77	80	4	7	10	13	16	19	22	25	28	31	34
36	6	9	12	15	18	21	24	27	30	33	36	39	42
37	62	65	68	71	74	77	80	4	7	10	13	16	19
38	29	32	35	38	41	44	47	50	53	56	59	62	65
39	14	17	20	23	26	29	32	35	38	41	44	47	50

Table B.1—Hopping sequence set 1 (continued)

index	0	3	6	9	12	15	18	21	24	27	30	33	36
40	27	30	33	36	39	42	45	48	51	54	57	60	63
41	16	19	22	25	28	31	34	37	40	43	46	49	52
42	59	62	65	68	71	74	77	80	4	7	10	13	16
43	43	46	49	52	55	58	61	64	67	70	73	76	79
44	76	79	3	6	9	12	15	18	21	24	27	30	33
45	34	37	40	43	46	49	52	55	58	61	64	67	70
46	72	75	78	2	5	8	11	14	17	20	23	26	29
47	11	14	17	20	23	26	29	32	35	38	41	44	47
48	60	63	66	69	72	75	78	2	5	8	11	14	17
49	80	4	7	10	13	16	19	22	25	28	31	34	37
50	47	50	53	56	59	62	65	68	71	74	77	80	4
51	22	25	28	31	34	37	40	43	46	49	52	55	58
52	75	78	2	5	8	11	14	17	20	23	26	29	32
53	66	69	72	75	78	2	5	8	11	14	17	20	23
54	41	44	47	50	53	56	59	62	65	68	71	74	77
55	15	18	21	24	27	30	33	36	39	42	45	48	51
56	35	38	41	44	47	50	53	56	59	62	65	68	71
57	67	70	73	76	79	3	6	9	12	15	18	21	24
58	52	55	58	61	64	67	70	73	76	79	3	6	9
59	58	61	64	67	70	73	76	79	3	6	9	12	15
60	44	47	50	53	56	59	62	65	68	71	74	77	80
61	50	53	56	59	62	65	68	71	74	77	80	4	7
62	17	20	23	26	29	32	35	38	41	44	47	50	53
63	7	10	13	16	19	22	25	28	31	34	37	40	43
64	19	22	25	28	31	34	37	40	43	46	49	52	55
65	8	11	14	17	20	23	26	29	32	35	38	41	44
66	69	72	75	78	2	5	8	11	14	17	20	23	26
67	51	54	57	60	63	66	69	72	75	78	2	5	8
68	42	45	48	51	54	57	60	63	66	69	72	75	78
69	3	6	9	12	15	18	21	24	27	30	33	36	39
70	30	33	36	39	42	45	48	51	54	57	60	63	66
71	57	60	63	66	69	72	75	78	2	5	8	11	14
72	37	40	43	46	49	52	55	58	61	64	67	70	73
73	55	58	61	64	67	70	73	76	79	3	6	9	12
74	26	29	32	35	38	41	44	47	50	53	56	59	62
75	46	49	52	55	58	61	64	67	70	73	76	79	3
76	53	56	59	62	65	68	71	74	77	80	4	7	10
77	40	43	46	49	52	55	58	61	64	67	70	73	76
78	32	35	38	41	44	47	50	53	56	59	62	65	68
79	48	51	54	57	60	63	66	69	72	75	78	2	5

Table B.1—Hopping sequence set 1 (continued)

index	39	42	45	48	51	54	57	60	63	66	69	72	75
1	41	44	47	50	53	56	59	62	65	68	71	74	77
2	64	67	70	73	76	79	3	6	9	12	15	18	21
3	24	27	30	33	36	39	42	45	48	51	54	57	60
4	49	52	55	58	61	64	67	70	73	76	79	3	6
5	5	8	11	14	17	20	23	26	29	32	35	38	41
6	57	60	63	66	69	72	75	78	2	5	8	11	14
7	33	36	39	42	45	48	51	54	57	60	63	66	69
8	9	12	15	18	21	24	27	30	33	36	39	42	45
9	60	63	66	69	72	75	78	2	5	8	11	14	17
10	23	26	29	32	35	38	41	44	47	50	53	56	59
11	38	41	44	47	50	53	56	59	62	65	68	71	74
12	70	73	76	79	3	6	9	12	15	18	21	24	27
13	21	24	27	30	33	36	39	42	45	48	51	54	57
14	63	66	69	72	75	78	2	5	8	11	14	17	20
15	14	17	20	23	26	29	32	35	38	41	44	47	50
16	25	28	31	34	37	40	43	46	49	52	55	58	61
17	67	70	73	76	79	3	6	9	12	15	18	21	24
18	39	42	45	48	51	54	57	60	63	66	69	72	75
19	72	75	78	2	5	8	11	14	17	20	23	26	29
20	43	46	49	52	55	58	61	64	67	70	73	76	79
21	59	62	65	68	71	74	77	80	4	7	10	13	16
22	52	55	58	61	64	67	70	73	76	79	3	6	9
23	77	80	4	7	10	13	16	19	22	25	28	31	34
24	34	37	40	43	46	49	52	55	58	61	64	67	70
25	16	19	22	25	28	31	34	37	40	43	46	49	52
26	31	34	37	40	43	46	49	52	55	58	61	64	67
27	62	65	68	71	74	77	80	4	7	10	13	16	19
28	44	47	50	53	56	59	62	65	68	71	74	77	80
29	78	2	5	8	11	14	17	20	23	26	29	32	35
30	51	54	57	60	63	66	69	72	75	78	2	5	8
31	75	78	2	5	8	11	14	17	20	23	26	29	32
32	28	31	34	37	40	43	46	49	52	55	58	61	64
33	48	51	54	57	60	63	66	69	72	75	78	2	5
34	30	33	36	39	42	45	48	51	54	57	60	63	66
35	37	40	43	46	49	52	55	58	61	64	67	70	73
36	45	48	51	54	57	60	63	66	69	72	75	78	2
37	22	25	28	31	34	37	40	43	46	49	52	55	58
38	68	71	74	77	80	4	7	10	13	16	19	22	25
39	53	56	59	62	65	68	71	74	77	80	4	7	10

Table B.1—Hopping sequence set 1 (continued)

index	39	42	45	48	51	54	57	60	63	66	69	72	75
40	66	69	72	75	78	2	5	8	11	14	17	20	23
41	55	58	61	64	67	70	73	76	79	3	6	9	12
42	19	22	25	28	31	34	37	40	43	46	49	52	55
43	3	6	9	12	15	18	21	24	27	30	33	36	39
44	36	39	42	45	48	51	54	57	60	63	66	69	72
45	73	76	79	3	6	9	12	15	18	21	24	27	30
46	32	35	38	41	44	47	50	53	56	59	62	65	68
47	50	53	56	59	62	65	68	71	74	77	80	4	7
48	20	23	26	29	32	35	38	41	44	47	50	53	56
49	40	43	46	49	52	55	58	61	64	67	70	73	76
50	7	10	13	16	19	22	25	28	31	34	37	40	43
51	61	64	67	70	73	76	79	3	6	9	12	15	18
52	35	38	41	44	47	50	53	56	59	62	65	68	71
53	26	29	32	35	38	41	44	47	50	53	56	59	62
54	80	4	7	10	13	16	19	22	25	28	31	34	37
55	54	57	60	63	66	69	72	75	78	2	5	8	11
56	74	77	80	4	7	10	13	16	19	22	25	28	31
57	27	30	33	36	39	42	45	48	51	54	57	60	63
58	12	15	18	21	24	27	30	33	36	39	42	45	48
59	18	21	24	27	30	33	36	39	42	45	48	51	54
60	4	7	10	13	16	19	22	25	28	31	34	37	40
61	10	13	16	19	22	25	28	31	34	37	40	43	46
62	56	59	62	65	68	71	74	77	80	4	7	10	13
63	46	49	52	55	58	61	64	67	70	73	76	79	3
64	58	61	64	67	70	73	76	79	3	6	9	12	15
65	47	50	53	56	59	62	65	68	71	74	77	80	4
66	29	32	35	38	41	44	47	50	53	56	59	62	65
67	11	14	17	20	23	26	29	32	35	38	41	44	47
68	2	5	8	11	14	17	20	23	26	29	32	35	38
69	42	45	48	51	54	57	60	63	66	69	72	75	78
70	69	72	75	78	2	5	8	11	14	17	20	23	26
71	17	20	23	26	29	32	35	38	41	44	47	50	53
72	76	79	3	6	9	12	15	18	21	24	27	30	33
73	15	18	21	24	27	30	33	36	39	42	45	48	51
74	65	68	71	74	77	80	4	7	10	13	16	19	22
75	6	9	12	15	18	21	24	27	30	33	36	39	42
76	13	16	19	22	25	28	31	34	37	40	43	46	49
77	79	3	6	9	12	15	18	21	24	27	30	33	36
78	71	74	77	80	4	7	10	13	16	19	22	25	28
79	8	11	14	17	20	23	26	29	32	35	38	41	44

Table B.2—Hopping sequence set 2

index	1	4	7	10	13	16	19	22	25	28	31	34	37
1	3	6	9	12	15	18	21	24	27	30	33	36	39
2	26	29	32	35	38	41	44	47	50	53	56	59	62
3	65	68	71	74	77	80	4	7	10	13	16	19	22
4	11	14	17	20	23	26	29	32	35	38	41	44	47
5	46	49	52	55	58	61	64	67	70	73	76	79	3
6	19	22	25	28	31	34	37	40	43	46	49	52	55
7	74	77	80	4	7	10	13	16	19	22	25	28	31
8	50	53	56	59	62	65	68	71	74	77	80	4	7
9	22	25	28	31	34	37	40	43	46	49	52	55	58
10	64	67	70	73	76	79	3	6	9	12	15	18	21
11	79	3	6	9	12	15	18	21	24	27	30	33	36
12	32	35	38	41	44	47	50	53	56	59	62	65	68
13	62	65	68	71	74	77	80	4	7	10	13	16	19
14	25	28	31	34	37	40	43	46	49	52	55	58	61
15	55	58	61	64	67	70	73	76	79	3	6	9	12
16	66	69	72	75	78	2	5	8	11	14	17	20	23
17	29	32	35	38	41	44	47	50	53	56	59	62	65
18	80	4	7	10	13	16	19	22	25	28	31	34	37
19	34	37	40	43	46	49	52	55	58	61	64	67	70
20	5	8	11	14	17	20	23	26	29	32	35	38	41
21	21	24	27	30	33	36	39	42	45	48	51	54	57
22	14	17	20	23	26	29	32	35	38	41	44	47	50
23	39	42	45	48	51	54	57	60	63	66	69	72	75
24	75	78	2	5	8	11	14	17	20	23	26	29	32
25	57	60	63	66	69	72	75	78	2	5	8	11	14
26	72	75	78	2	5	8	11	14	17	20	23	26	29
27	24	27	30	33	36	39	42	45	48	51	54	57	60
28	6	9	12	15	18	21	24	27	30	33	36	39	42
29	40	43	46	49	52	55	58	61	64	67	70	73	76
30	13	16	19	22	25	28	31	34	37	40	43	46	49
31	37	40	43	46	49	52	55	58	61	64	67	70	73
32	69	72	75	78	2	5	8	11	14	17	20	23	26
33	10	13	16	19	22	25	28	31	34	37	40	43	46
34	71	74	77	80	4	7	10	13	16	19	22	25	28
35	78	2	5	8	11	14	17	20	23	26	29	32	35
36	7	10	13	16	19	22	25	28	31	34	37	40	43
37	63	66	69	72	75	78	2	5	8	11	14	17	20
38	30	33	36	39	42	45	48	51	54	57	60	63	66
39	15	18	21	24	27	30	33	36	39	42	45	48	51

Table B.2—Hopping sequence set 2 (continued)

index	1	4	7	10	13	16	19	22	25	28	31	34	37
40	28	31	34	37	40	43	46	49	52	55	58	61	64
41	17	20	23	26	29	32	35	38	41	44	47	50	53
42	60	63	66	69	72	75	78	2	5	8	11	14	17
43	44	47	50	53	56	59	62	65	68	71	74	77	80
44	77	80	4	7	10	13	16	19	22	25	28	31	34
45	35	38	41	44	47	50	53	56	59	62	65	68	71
46	73	76	79	3	6	9	12	15	18	21	24	27	30
47	12	15	18	21	24	27	30	33	36	39	42	45	48
48	61	64	67	70	73	76	79	3	6	9	12	15	18
49	2	5	8	11	14	17	20	23	26	29	32	35	38
50	48	51	54	57	60	63	66	69	72	75	78	2	5
51	23	26	29	32	35	38	41	44	47	50	53	56	59
52	76	79	3	6	9	12	15	18	21	24	27	30	33
53	67	70	73	76	79	3	6	9	12	15	18	21	24
54	42	45	48	51	54	57	60	63	66	69	72	75	78
55	16	19	22	25	28	31	34	37	40	43	46	49	52
56	36	39	42	45	48	51	54	57	60	63	66	69	72
57	68	71	74	77	80	4	7	10	13	16	19	22	25
58	53	56	59	62	65	68	71	74	77	80	4	7	10
59	59	62	65	68	71	74	77	80	4	7	10	13	16
60	45	48	51	54	57	60	63	66	69	72	75	78	2
61	51	54	57	60	63	66	69	72	75	78	2	5	8
62	18	21	24	27	30	33	36	39	42	45	48	51	54
63	8	11	14	17	20	23	26	29	32	35	38	41	44
64	20	23	26	29	32	35	38	41	44	47	50	53	56
65	9	12	15	18	21	24	27	30	33	36	39	42	45
66	70	73	76	79	3	6	9	12	15	18	21	24	27
67	52	55	58	61	64	67	70	73	76	79	3	6	9
68	43	46	49	52	55	58	61	64	67	70	73	76	79
69	4	7	10	13	16	19	22	25	28	31	34	37	40
70	31	34	37	40	43	46	49	52	55	58	61	64	67
71	58	61	64	67	70	73	76	79	3	6	9	12	15
72	38	41	44	47	50	53	56	59	62	65	68	71	74
73	56	59	62	65	68	71	74	77	80	4	7	10	13
74	27	30	33	36	39	42	45	48	51	54	57	60	63
75	47	50	53	56	59	62	65	68	71	74	77	80	4
76	54	57	60	63	66	69	72	75	78	2	5	8	11
77	41	44	47	50	53	56	59	62	65	68	71	74	77
78	33	36	39	42	45	48	51	54	57	60	63	66	69
79	49	52	55	58	61	64	67	70	73	76	79	3	6

Table B.2—Hopping sequence set 2 (continued)

index	40	43	46	49	52	55	58	61	64	67	70	73	76
1	42	45	48	51	54	57	60	63	66	69	72	75	78
2	65	68	71	74	77	80	4	7	10	13	16	19	22
3	25	28	31	34	37	40	43	46	49	52	55	58	61
4	50	53	56	59	62	65	68	71	74	77	80	4	7
5	6	9	12	15	18	21	24	27	30	33	36	39	42
6	58	61	64	67	70	73	76	79	3	6	9	12	15
7	34	37	40	43	46	49	52	55	58	61	64	67	70
8	10	13	16	19	22	25	28	31	34	37	40	43	46
9	61	64	67	70	73	76	79	3	6	9	12	15	18
10	24	27	30	33	36	39	42	45	48	51	54	57	60
11	39	42	45	48	51	54	57	60	63	66	69	72	75
12	71	74	77	80	4	7	10	13	16	19	22	25	28
13	22	25	28	31	34	37	40	43	46	49	52	55	58
14	64	67	70	73	76	79	3	6	9	12	15	18	21
15	15	18	21	24	27	30	33	36	39	42	45	48	51
16	26	29	32	35	38	41	44	47	50	53	56	59	62
17	68	71	74	77	80	4	7	10	13	16	19	22	25
18	40	43	46	49	52	55	58	61	64	67	70	73	76
19	73	76	79	3	6	9	12	15	18	21	24	27	30
20	44	47	50	53	56	59	62	65	68	71	74	77	80
21	60	63	66	69	72	75	78	2	5	8	11	14	17
22	53	56	59	62	65	68	71	74	77	80	4	7	10
23	78	2	5	8	11	14	17	20	23	26	29	32	35
24	35	38	41	44	47	50	53	56	59	62	65	68	71
25	17	20	23	26	29	32	35	38	41	44	47	50	53
26	32	35	38	41	44	47	50	53	56	59	62	65	68
27	63	66	69	72	75	78	2	5	8	11	14	17	20
28	45	48	51	54	57	60	63	66	69	72	75	78	2
29	79	3	6	9	12	15	18	21	24	27	30	33	36
30	52	55	58	61	64	67	70	73	76	79	3	6	9
31	76	79	3	6	9	12	15	18	21	24	27	30	33
32	29	32	35	38	41	44	47	50	53	56	59	62	65
33	49	52	55	58	61	64	67	70	73	76	79	3	6
34	31	34	37	40	43	46	49	52	55	58	61	64	67
35	38	41	44	47	50	53	56	59	62	65	68	71	74
36	46	49	52	55	58	61	64	67	70	73	76	79	3
37	23	26	29	32	35	38	41	44	47	50	53	56	59
38	69	72	75	78	2	5	8	11	14	17	20	23	26
39	54	57	60	63	66	69	72	75	78	2	5	8	11

Table B.2—Hopping sequence set 2 (continued)

index	40	43	46	49	52	55	58	61	64	67	70	73	76
40	67	70	73	76	79	3	6	9	12	15	18	21	24
41	56	59	62	65	68	71	74	77	80	4	7	10	13
42	20	23	26	29	32	35	38	41	44	47	50	53	56
43	4	7	10	13	16	19	22	25	28	31	34	37	40
44	37	40	43	46	49	52	55	58	61	64	67	70	73
45	74	77	80	4	7	10	13	16	19	22	25	28	31
46	33	36	39	42	45	48	51	54	57	60	63	66	69
47	51	54	57	60	63	66	69	72	75	78	2	5	8
48	21	24	27	30	33	36	39	42	45	48	51	54	57
49	41	44	47	50	53	56	59	62	65	68	71	74	77
50	8	11	14	17	20	23	26	29	32	35	38	41	44
51	62	65	68	71	74	77	80	4	7	10	13	16	19
52	36	39	42	45	48	51	54	57	60	63	66	69	72
53	27	30	33	36	39	42	45	48	51	54	57	60	63
54	2	5	8	11	14	17	20	23	26	29	32	35	38
55	55	58	61	64	67	70	73	76	79	3	6	9	12
56	75	78	2	5	8	11	14	17	20	23	26	29	32
57	28	31	34	37	40	43	46	49	52	55	58	61	64
58	13	16	19	22	25	28	31	34	37	40	43	46	49
59	19	22	25	28	31	34	37	40	43	46	49	52	55
60	5	8	11	14	17	20	23	26	29	32	35	38	41
61	11	14	17	20	23	26	29	32	35	38	41	44	47
62	57	60	63	66	69	72	75	78	2	5	8	11	14
63	47	50	53	56	59	62	65	68	71	74	77	80	4
64	59	62	65	68	71	74	77	80	4	7	10	13	16
65	48	51	54	57	60	63	66	69	72	75	78	2	5
66	30	33	36	39	42	45	48	51	54	57	60	63	66
67	12	15	18	21	24	27	30	33	36	39	42	45	48
68	3	6	9	12	15	18	21	24	27	30	33	36	39
69	43	46	49	52	55	58	61	64	67	70	73	76	79
70	70	73	76	79	3	6	9	12	15	18	21	24	27
71	18	21	24	27	30	33	36	39	42	45	48	51	54
72	77	80	4	7	10	13	16	19	22	25	28	31	34
73	16	19	22	25	28	31	34	37	40	43	46	49	52
74	66	69	72	75	78	2	5	8	11	14	17	20	23
75	7	10	13	16	19	22	25	28	31	34	37	40	43
76	14	17	20	23	26	29	32	35	38	41	44	47	50
77	80	4	7	10	13	16	19	22	25	28	31	34	37
78	72	75	78	2	5	8	11	14	17	20	23	26	29
79	9	12	15	18	21	24	27	30	33	36	39	42	45

Table B.3—Hopping sequence set 3

index	2	5	8	11	14	17	20	23	26	29	32	35	38
1	4	7	10	13	16	19	22	25	28	31	34	37	40
2	27	30	33	36	39	42	45	48	51	54	57	60	63
3	66	69	72	75	78	2	5	8	11	14	17	20	23
4	12	15	18	21	24	27	30	33	36	39	42	45	48
5	47	50	53	56	59	62	65	68	71	74	77	80	4
6	20	23	26	29	32	35	38	41	44	47	50	53	56
7	75	78	2	5	8	11	14	17	20	23	26	29	32
8	51	54	57	60	63	66	69	72	75	78	2	5	8
9	23	26	29	32	35	38	41	44	47	50	53	56	59
10	65	68	71	74	77	80	4	7	10	13	16	19	22
11	80	4	7	10	13	16	19	22	25	28	31	34	37
12	33	36	39	42	45	48	51	54	57	60	63	66	69
13	63	66	69	72	75	78	2	5	8	11	14	17	20
14	26	29	32	35	38	41	44	47	50	53	56	59	62
15	56	59	62	65	68	71	74	77	80	4	7	10	13
16	67	70	73	76	79	3	6	9	12	15	18	21	24
17	30	33	36	39	42	45	48	51	54	57	60	63	66
18	2	5	8	11	14	17	20	23	26	29	32	35	38
19	35	38	41	44	47	50	53	56	59	62	65	68	71
20	6	9	12	15	18	21	24	27	30	33	36	39	42
21	22	25	28	31	34	37	40	43	46	49	52	55	58
22	15	18	21	24	27	30	33	36	39	42	45	48	51
23	40	43	46	49	52	55	58	61	64	67	70	73	76
24	76	79	3	6	9	12	15	18	21	24	27	30	33
25	58	61	64	67	70	73	76	79	3	6	9	12	15
26	73	76	79	3	6	9	12	15	18	21	24	27	30
27	25	28	31	34	37	40	43	46	49	52	55	58	61
28	7	10	13	16	19	22	25	28	31	34	37	40	43
29	41	44	47	50	53	56	59	62	65	68	71	74	77
30	14	17	20	23	26	29	32	35	38	41	44	47	50
31	38	41	44	47	50	53	56	59	62	65	68	71	74
32	70	73	76	79	3	6	9	12	15	18	21	24	27
33	11	14	17	20	23	26	29	32	35	38	41	44	47
34	72	75	78	2	5	8	11	14	17	20	23	26	29
35	79	3	6	9	12	15	18	21	24	27	30	33	36
36	8	11	14	17	20	23	26	29	32	35	38	41	44
37	64	67	70	73	76	79	3	6	9	12	15	18	21
38	31	34	37	40	43	46	49	52	55	58	61	64	67
39	16	19	22	25	28	31	34	37	40	43	46	49	52

Table B.3—Hopping sequence set 3 (continued)

index	2	5	8	11	14	17	20	23	26	29	32	35	38
40	29	32	35	38	41	44	47	50	53	56	59	62	65
41	18	21	24	27	30	33	36	39	42	45	48	51	54
42	61	64	67	70	73	76	79	3	6	9	12	15	18
43	45	48	51	54	57	60	63	66	69	72	75	78	2
44	78	2	5	8	11	14	17	20	23	26	29	32	35
45	36	39	42	45	48	51	54	57	60	63	66	69	72
46	74	77	80	4	7	10	13	16	19	22	25	28	31
47	13	16	19	22	25	28	31	34	37	40	43	46	49
48	62	65	68	71	74	77	80	4	7	10	13	16	19
49	3	6	9	12	15	18	21	24	27	30	33	36	39
50	49	52	55	58	61	64	67	70	73	76	79	3	6
51	24	27	30	33	36	39	42	45	48	51	54	57	60
52	77	80	4	7	10	13	16	19	22	25	28	31	34
53	68	71	74	77	80	4	7	10	13	16	19	22	25
54	43	46	49	52	55	58	61	64	67	70	73	76	79
55	17	20	23	26	29	32	35	38	41	44	47	50	53
56	37	40	43	46	49	52	55	58	61	64	67	70	73
57	69	72	75	78	2	5	8	11	14	17	20	23	26
58	54	57	60	63	66	69	72	75	78	2	5	8	11
59	60	63	66	69	72	75	78	2	5	8	11	14	17
60	46	49	52	55	58	61	64	67	70	73	76	79	3
61	52	55	58	61	64	67	70	73	76	79	3	6	9
62	19	22	25	28	31	34	37	40	43	46	49	52	55
63	9	12	15	18	21	24	27	30	33	36	39	42	45
64	21	24	27	30	33	36	39	42	45	48	51	54	57
65	10	13	16	19	22	25	28	31	34	37	40	43	46
66	71	74	77	80	4	7	10	13	16	19	22	25	28
67	53	56	59	62	65	68	71	74	77	80	4	7	10
68	44	47	50	53	56	59	62	65	68	71	74	77	80
69	5	8	11	14	17	20	23	26	29	32	35	38	41
70	32	35	38	41	44	47	50	53	56	59	62	65	68
71	59	62	65	68	71	74	77	80	4	7	10	13	16
72	39	42	45	48	51	54	57	60	63	66	69	72	75
73	57	60	63	66	69	72	75	78	2	5	8	11	14
74	28	31	34	37	40	43	46	49	52	55	58	61	64
75	48	51	54	57	60	63	66	69	72	75	78	2	5
76	55	58	61	64	67	70	73	76	79	3	6	9	12
77	42	45	48	51	54	57	60	63	66	69	72	75	78
78	34	37	40	43	46	49	52	55	58	61	64	67	70
79	50	53	56	59	62	65	68	71	74	77	80	4	7

Table B.3—Hopping sequence set 3 (continued)

index	41	44	47	50	53	56	59	62	65	68	71	74	77
1	43	46	49	52	55	58	61	64	67	70	73	76	79
2	66	69	72	75	78	2	5	8	11	14	17	20	23
3	26	29	32	35	38	41	44	47	50	53	56	59	62
4	51	54	57	60	63	66	69	72	75	78	2	5	8
5	7	10	13	16	19	22	25	28	31	34	37	40	43
6	59	62	65	68	71	74	77	80	4	7	10	13	16
7	35	38	41	44	47	50	53	56	59	62	65	68	71
8	11	14	17	20	23	26	29	32	35	38	41	44	47
9	62	65	68	71	74	77	80	4	7	10	13	16	19
10	25	28	31	34	37	40	43	46	49	52	55	58	61
11	40	43	46	49	52	55	58	61	64	67	70	73	76
12	72	75	78	2	5	8	11	14	17	20	23	26	29
13	23	26	29	32	35	38	41	44	47	50	53	56	59
14	65	68	71	74	77	80	4	7	10	13	16	19	22
15	16	19	22	25	28	31	34	37	40	43	46	49	52
16	27	30	33	36	39	42	45	48	51	54	57	60	63
17	69	72	75	78	2	5	8	11	14	17	20	23	26
18	41	44	47	50	53	56	59	62	65	68	71	74	77
19	74	77	80	4	7	10	13	16	19	22	25	28	31
20	45	48	51	54	57	60	63	66	69	72	75	78	2
21	61	64	67	70	73	76	79	3	6	9	12	15	18
22	54	57	60	63	66	69	72	75	78	2	5	8	11
23	79	3	6	9	12	15	18	21	24	27	30	33	36
24	36	39	42	45	48	51	54	57	60	63	66	69	72
25	18	21	24	27	30	33	36	39	42	45	48	51	54
26	33	36	39	42	45	48	51	54	57	60	63	66	69
27	64	67	70	73	76	79	3	6	9	12	15	18	21
28	46	49	52	55	58	61	64	67	70	73	76	79	3
29	80	4	7	10	13	16	19	22	25	28	31	34	37
30	53	56	59	62	65	68	71	74	77	80	4	7	10
31	77	80	4	7	10	13	16	19	22	25	28	31	34
32	30	33	36	39	42	45	48	51	54	57	60	63	66
33	50	53	56	59	62	65	68	71	74	77	80	4	7
34	32	35	38	41	44	47	50	53	56	59	62	65	68
35	39	42	45	48	51	54	57	60	63	66	69	72	75
36	47	50	53	56	59	62	65	68	71	74	77	80	4
37	24	27	30	33	36	39	42	45	48	51	54	57	60
38	70	73	76	79	3	6	9	12	15	18	21	24	27
39	55	58	61	64	67	70	73	76	79	3	6	9	12

Table B.3—Hopping sequence set 3 (continued)

index	41	44	47	50	53	56	59	62	65	68	71	74	77
40	68	71	74	77	80	4	7	10	13	16	19	22	25
41	57	60	63	66	69	72	75	78	2	5	8	11	14
42	21	24	27	30	33	36	39	42	45	48	51	54	57
43	5	8	11	14	17	20	23	26	29	32	35	38	41
44	38	41	44	47	50	53	56	59	62	65	68	71	74
45	75	78	2	5	8	11	14	17	20	23	26	29	32
46	34	37	40	43	46	49	52	55	58	61	64	67	70
47	52	55	58	61	64	67	70	73	76	79	3	6	9
48	22	25	28	31	34	37	40	43	46	49	52	55	58
49	42	45	48	51	54	57	60	63	66	69	72	75	78
50	9	12	15	18	21	24	27	30	33	36	39	42	45
51	63	66	69	72	75	78	2	5	8	11	14	17	20
52	37	40	43	46	49	52	55	58	61	64	67	70	73
53	28	31	34	37	40	43	46	49	52	55	58	61	64
54	3	6	9	12	15	18	21	24	27	30	33	36	39
55	56	59	62	65	68	71	74	77	80	4	7	10	13
56	76	79	3	6	9	12	15	18	21	24	27	30	33
57	29	32	35	38	41	44	47	50	53	56	59	62	65
58	14	17	20	23	26	29	32	35	38	41	44	47	50
59	20	23	26	29	32	35	38	41	44	47	50	53	56
60	6	9	12	15	18	21	24	27	30	33	36	39	42
61	12	15	18	21	24	27	30	33	36	39	42	45	48
62	58	61	64	67	70	73	76	79	3	6	9	12	15
63	48	51	54	57	60	63	66	69	72	75	78	2	5
64	60	63	66	69	72	75	78	2	5	8	11	14	17
65	49	52	55	58	61	64	67	70	73	76	79	3	6
66	31	34	37	40	43	46	49	52	55	58	61	64	67
67	13	16	19	22	25	28	31	34	37	40	43	46	49
68	4	7	10	13	16	19	22	25	28	31	34	37	40
69	44	47	50	53	56	59	62	65	68	71	74	77	80
70	71	74	77	80	4	7	10	13	16	19	22	25	28
71	19	22	25	28	31	34	37	40	43	46	49	52	55
72	78	2	5	8	11	14	17	20	23	26	29	32	35
73	17	20	23	26	29	32	35	38	41	44	47	50	53
74	67	70	73	76	79	3	6	9	12	15	18	21	24
75	8	11	14	17	20	23	26	29	32	35	38	41	44
76	15	18	21	24	27	30	33	36	39	42	45	48	51
77	2	5	8	11	14	17	20	23	26	29	32	35	38
78	73	76	79	3	6	9	12	15	18	21	24	27	30
79	10	13	16	19	22	25	28	31	34	37	40	43	46

Annex C

(normative)

Formal description of MAC operation

This annex contains formal descriptions of the behavior of MAC station (STA) and access point (AP) entities. These descriptions also describe the frame formats and the generation and interpretation of information encoded in MAC frames, in the parameters of service primitives supported by the MAC, and in MIB attributes used or generated by the MAC. The MAC is described using the 1992 version of the ITU Specification and Description Language (SDL-92). SDL-92 is defined in ITU-T Recommendation Z.100 (03/93). An update to Z.100 was approved in 1996 (SDL-96), but none of the SDL facilities used in this annex were modified. An introduction to the MAC formal description is provided in Clause C.1. Definitions of the data types and operators used by the MAC state machines are provided in Clause C.2. An SDL system describing MAC operation at an IEEE 802.11 station is contained in Clause C.3. Finally, a subset of an SDL system describing the aspects of MAC operation at an IEEE 802.11 AP that differ from operation at a non-AP station is provided in Clause C.4.

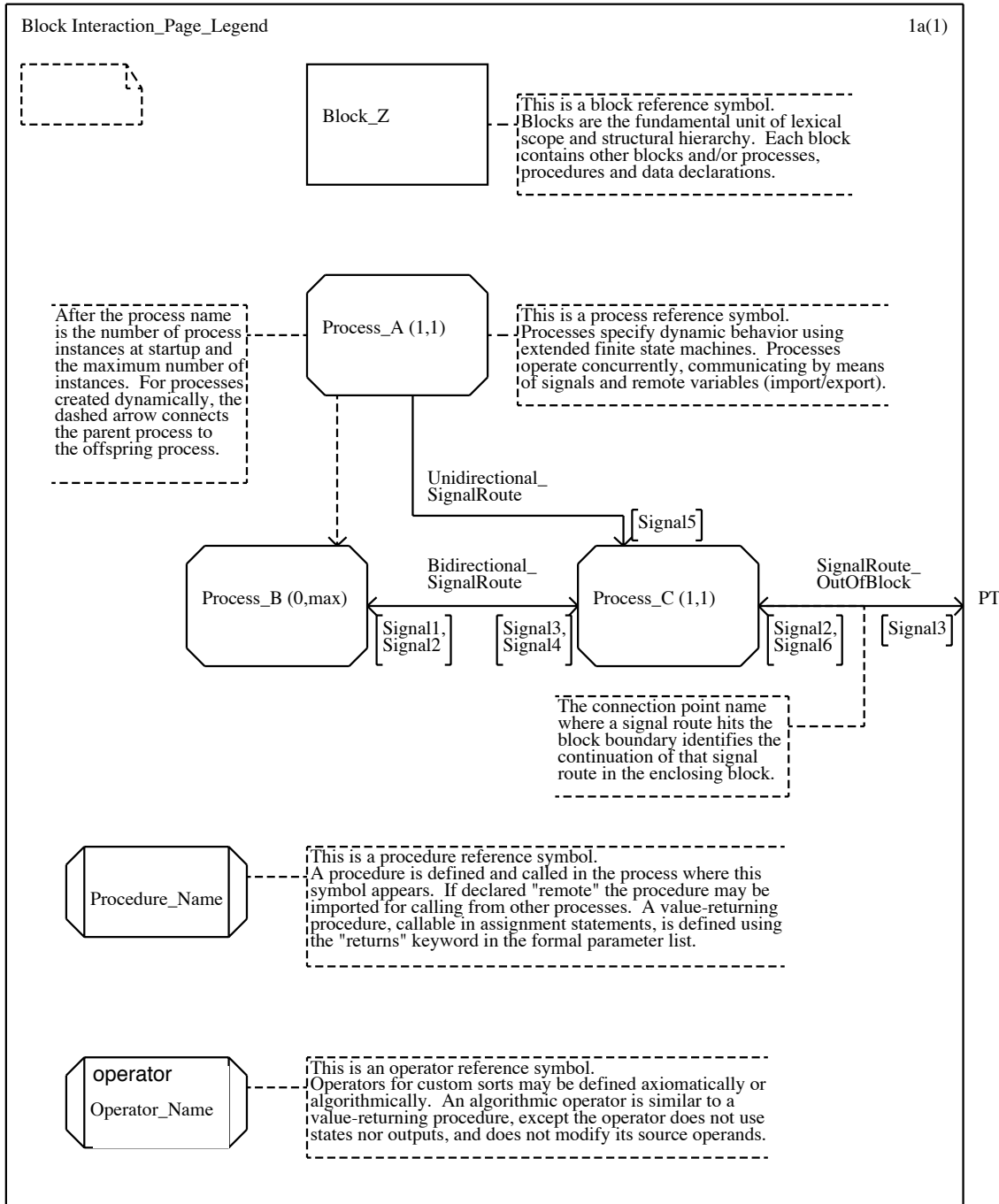
In Annex D, the MAC and PHY management information bases are described in Abstract Syntax Notation One (ASN.1), defined in ISO/IEC 8824 and ISO/IEC 8825. ITU-T Recommendation Z.105 (03/95) defines the use of SDL in conjunction with ASN.1, allowing system behavior to be defined using SDL and data types to be defined using ASN.1. Incomplete tool support precluded the use of Z.105 in this annex. However, within the limits of Z.100, the data types in Clause C.2 are defined in a similar manner to Z.105. Annex E contains a listing of available documentation.

NOTES

1—Software for generating, analyzing, verifying, and simulating SDL system descriptions is available from several sources. The SDL code in this annex was generated using *SDT/PC version 3.02*; from Telelogic AB, Malmö, Sweden (+46-40-174700; internet: telelogic.se); USA office in Princeton, NJ (+1-609-520-1935; internet: telelogic.com). Telelogic offers SDT for several workstation platforms in addition to SDT/PC.

2—The SDL definitions in this annex should be usable with any SDL tool that supports the 1993 version or 1996 update of Recommendation Z.100. The use of Telelogic's product to prepare this annex does not constitute an endorsement of SDT by the IEEE LAN/MAN Standards Committee or by the IEEE.

2—The diagrams on the next two pages show most of the symbols of SDL graphical syntax (SDL-GR) used in the MAC formal description. The symbols in these diagrams have labels and comments that explain their meanings. These diagrams are intended to serve as a legend for the SDL-GR symbols that comprise most of the process interaction and state transition diagrams. These diagrams are neither a complete SDL system, nor a complete presentation of SDL-GR symbology. Also, this state machine fragment exists to illustrate the SDL graphical syntax, and does not describe any useful behavior.



C.1 Introduction to the MAC formal description

This formal description defines the behavior of IEEE 802.11 MAC entities. The MAC protocol functional decomposition used herein facilitates explicit description of the reference points and durations of the various timed intervals; the bases for generation and/or validation of header fields, service parameters, and MIB attributes; and the interpretation of each value in cases where enumerated data types are used in service parameters.

C.1.1 Fundamental assumptions

The MAC protocol is described as an SDL system, which is a set of extended finite state machines. Each state machine is a set of independent processes, all of which operate concurrently. All variable data-holding entities and procedures exist solely within the context of a single process. In SDL all interprocess communication is done with signals (there are no global variables). Signals may be sent and received explicitly, using SDL's output and input symbols, or implicitly, using SDL's export/import mechanism (only if the variables or procedures are declared "remote"). By default, signals incur delays when traversing channels between blocks; however, only nondelaying channels and signal routes are used in the MAC state machines, and all remote variables and procedures are declared with the "nodelay" property.

State transitions, procedure calls, and tasks (assignment statements and other algorithmic processing steps) are assumed to require zero time. This permits the time intervals that are part of the normative MAC behavior to be defined explicitly, using SDL timers. One unit of system time (a 1.0 change in the value of "now") is assumed to represent one microsecond of real time. Usec (microsecond) and TU (time unit) data types are defined, with operators to convert Usec and TU values to SDL time or duration when necessary.

The SDL system boundary encloses the MAC entities. The LLC, SME, PHY, and distribution system are part of the environment. SDL generally assumes that entities in the environment operate as specified; however, the MAC state machines that communicate with the various SAPs attempt to validate inputs from the environment, and to handle cases where a pair of communicating entities, one within the system and the other outside the system boundary, have different local views of the medium, station, or service state. All stations in an IEEE 802.11 service set are assumed to exhibit the behaviors described herein. Nevertheless, because of the open nature of the wireless medium, the MAC state machines check for error cases that can arise only when an entity on the wireless medium is transmitting IEEE 802.11 PDUs, but is not obeying the communication protocols specified by this standard.

C.1.2 Notation conventions

When practical, names used in the clauses of this standard are spelled identically in this annex. The principal exceptions are those names that conflict with one of SDL's reserved words (such as power management mode "active" is renamed "sta_active" in SDL). To help fit the SDL text into the graphic symbols, acronyms with multiple, sequential capital letters are written with only the first letter capitalized (e.g., "MSDU" is written "Msdu" and "MLMEJoin.request" is written "MlmeJoin.request").

SDL reserved words and the names of variables and synonyms (named constants) begin with lowercase letters. The names of sorts (data types), signals, signal routes, channels, blocks, and processes begin with uppercase letters. The names of certain groups of variables and/or synonyms begin with a particular lowercase letter, followed by the remainder of the name, beginning with an uppercase letter. These groups are

"aNameOfAttribute"	MIB attributes
"cNameOfCapability"	Capability bits, also used for internal values exported as MIB counters
"dNameOfDuration"	Duration (relative time) values, declared as Usec, TU, or Duration
"eNameOfElement"	Element ID values

“mNameOfVariable”	Remote variables used for intra-MAC communication, but not part of the MIB. Most of these variables are exported from the MLME block.
“sNameOfStaticValue”	Synonyms for static data values used within the MAC
“tNameOfTime”	Time (absolute time) values, declared as Usec, TU, or Time. The names of timers begin with “T.”

C.1.3 Modeling techniques

State machines are grouped according to defined function sets that are visible, directly or indirectly, at an exposed interface. The emphasis in the organization of the state machines is explicitly to show initiation of and response to events at the exposed interfaces, and time-related actions, including those dependent on the absence of external events (e.g., response timeouts) and intervals measured in derived units (e.g., backoff “time” in units of slots during which the wireless medium is idle). The operations associated with the various state transitions emphasize communication functions. Most of the details regarding insertion, extraction, and encoding of information in fields of the protocol data units is encapsulated with the definitions of those fields. This approach, which relies heavily on SDL’s abstract data type and inheritance mechanisms, permits the behavior of the data-holding entities to be precisely defined, without obscuring process flow by adding in-line complexity to the individual state transitions.

The modeling of protocol and service data units requires sorts such as octet strings, and operators such as bitwise boolean functions, which are not predefined in SDL. These sorts and operators are defined in Package macsorts, which appears in Clause C.2.

Protocol and service data unit sorts are based on the **Bit** sort. Bit is a subtype of SDL’s predefined Boolean sort. As a result, Bit literals “0” and “1” are alternative names for “false” and “true,” and have no numeric significance. To use “0” or “1” as integer values requires a conversion operation. Items of the **Bitstring** sort are 0-origin, variable-length strings of Bits. With Bitstring operands, operators “and,” “or,” “xor,” and “not” operate bitwise, with the length of the result equal to the length of the longest (or only) source string. The **Octet** sort is a subtype of Bitstring that adds conversion operators to and from Integer. Each item of the Octet sort has length=8 {by usage convention in Z.100, enforced in Z.105}. Items of the **Octetstring** sort are 0-origin, variable-length strings of Octets. The **Frame** sort is a subtype of Octetstring that adds operators to extract and to modify all MAC header fields and most other MAC frame fields and elements. Most MAC fields and elements that contain named values with specific value assignments or enumerations are defined as subtypes of Frame, Octetstring, or Bitstring with the names added as literals or synonyms, so the state machines can refer to the names without introducing ambiguity about the value encodings.

Where communication at a SAP or between processes is strictly FIFO, the (implicit) input queue of the SDL processes is used. When more sophisticated queue management is needed, a queue whose entries are instances of one, specified sort is created using the **Queue** generator. Entries on Queue sorts may be added and removed at either the tail or the head, and the number of queue entries may be determined. The contents of a Queue may also be searched to locate entries with particular parameter values.

Clause C.2 contains an SDL-92 Package (a named collection of SDL definitions that can be included by reference into SDL System specification), which is a formal description of the formats and data encodings used in IEEE 802.11 service data units, protocol data units, and the parameters of the service primitives used at each of the service access points supported by the IEEE 802.11 MAC. This package also contains definitions for some data structures and operators used internally by one or more of the MAC state machines.

The behaviors of many intra-MAC operators are part of the normative description of the MAC protocol because results of the specified operations are visible, directly or indirectly, at exposed interfaces. For example, custom operators are used to define the generation of the CRC-32 value used in the FCS field (operator crc32, page 330), the calculation of frame transmission time used as part of the value in the Duration/ID field in certain types of frames (operator calcDur, page 343), the comparison of the values of particular fields of a

received MAC header with cached data values as part of the procedure for detecting duplicate frames (operator searchTupleCache, page 320), and numerous other aspects of frame formats and information encoding. On the other hand, data structures used solely for intra-MAC storage or for transferring of information between different state machines of a single station or access point, are only normative to the extent that they define items of internal state and the temporal sequence necessary for proper operation of the MAC protocol. The specific structures and encodings used for internal data storage and communication functions in this formal description do *not* constrain MAC implementations, provided those implementations exhibit the specified behaviors at the defined service access points and, in conjunction with an appropriate PHY, on the wireless medium.

C.2 Data type and operator definitions for the MAC state machines

This clause is in SDL/PR (phrase notation), with the exception of procedural operators, which are defined in SDL/GR (graphic notation). Package macsorts contains the definitions of the sorts (data types with associated operators and literals) and synonyms (named constants) used by the MAC state machines. Package macmib defines data types for attributes in the MAC MIB, and portions of the PHY MIB, accessed by the MAC state machines. Package macmib exists solely to satisfy SDL's strong type checking in the absence of an SDL tool that fully supports Z.105 (the combined use of SDL with ASN.1).

```

/*****/
Package macsorts;

/*****
*      Enumerated types used within the MAC state machines
*****/
newtype ChangeType          /* type of change due at the next boundary */
  literals      dwell,      /* dwell (only with FH PHY) */
               mocp;       /* medium occupancy (only with PCF) */
endnewtype ChangeType;

newtype Imed                /* priority for queuing MMPDUs, relative to MSDUs */
  literals      head,      /* place MMPDU at head of transmit queue */
               norm;      /* place MMPDU at tail of transmit queue */
endnewtype Imed;

newtype NavSrc              /* source of duration in SetNav & ClearNav signals */
  literals      rts,        /* RTS frame */
               cfpBss, cfpBss, /* start/end of CFP in own BSS */
               cfpOther, cfpOther, /* start/end of CFP in other BSS */
               cswitch,    /* channel switch */
               misc,       /* durId from other frame types */
               nosrc;      /* non-reception events */
endnewtype NavSrc;

newtype PsMode              /* power-save mode of a station (PsResponse signal) */
  literals      sta_active, power_save, unknown; endnewtype PsMode;

newtype PsState            /* power-save state of this station */
  literals      awake, doze; endnewtype PsState;

newtype StateErr           /* requests disassoc or death (MmIndicate signal) */
  literals      noerr, class2, class3; endnewtype StateErr;

newtype StationState       /* asoc/auth state of sta (SsResponse signal) */
  literals      not_auth, auth_open, auth_key, asoc, dis_asoc;
endnewtype StationState;

newtype TxResult           /* transmission attempt status (PduConfirm signal) */

```



```

    literals successful, partial, retryLimit, txLifetime
    atimAck, atimNak; endnewtype TxResult;

/*****
 * Enumerated types used in PHY service primitives
 *****/
newtype CcaStatus /* <state> parameter of PhyCca.indication */
    literals busy, idle; endnewtype CcaStatus;

newtype PhyRxStat /* <rxerror> parameter of PhyRxEnd.indication */
    literals no_error, fmt_violation, carrier_lost, unsupt_rate;
endnewtype PhyRxStat;

/*****
 * PLACEHOLDERS FOR MLME/PLME GET/SET PARAMETER VALUES
 *****/
/* MibAtrib (placeholder in MlmeGet/Set definitions) */
syntype MibAtrib = Charstring endsyntype MibAtrib;

/* MibValue (placeholder in MlmeGet/Set definitions) */
syntype MibValue = Integer endsyntype MibValue;

/*****
 * Enumerated types used in MAC and MLME service primitives
 *****/
newtype AuthType /* <authentication type> parm in Mlme primitives */
    inherits Octetstring operators all;
    adding literals open_system, shared_key;
    axioms open_system == mkOS(0, 2); shared_key == mkOS(1, 2);
endnewtype AuthType;

newtype AuthTypeSet powerset( AuthType); endnewtype AuthTypeSet;

newtype BssType /* <BSS type> parameter & BSS description element */
    literals infrastructure, independent, any_bss; endnewtype BssType;

newtype BssTypeSet powerset( BssType); endnewtype BssTypeSet;

newtype CfPriority /* <priority> parameter of various requests */
    literals contention, contentionFree; endnewtype CfPriority;

newtype MibStatus /* <status> parm of Mlme/Plme Get/Set.confirm */
    literals success, invalid, write_only, read_only;
endnewtype MibStatus;

newtype MlmeStatus /* <status> parm of Mlme operation confirm */
    literals success, invalid, timeout, refused,
        tomany_req, already_bss; endnewtype MlmeStatus;

newtype PwrSave /* <power save mode> parameter of MlmePowerMgt */
    literals sta_active, power_save; endnewtype PwrSave;

newtype Routing /* <routing info> parameter for MAC data service */
    literals null_rt; endnewtype Routing;

newtype RxStatus /* <reception status> parm of MaUnitdata indication */
    literals rx_success, rx_failure; endnewtype RxStatus;

newtype ScanType /* <scan type> parameter of MlmeScan.request */
    literals active_scan, passive_scan; endnewtype ScanType;

newtype ServiceClass /* <service class> parameter for MaUnitdata */
    literals reorderable, strictlyOrdered; endnewtype ServiceClass;

```

```

newtype TxStatus /* <transmission status> parm of MaUnitdataStatus */
  literals successful, retryLimit, txLifetime, noBss,
             excessiveDataLength, nonNullSourceRouting,
             unsupportedPriority, unavailablePriority,
             unsupportedServiceClass, unavailableServiceClass,
             unavailableKeyMapping; endnewtype TxStatus;

/*****
 *      Intra-MAC remote variables (names of form mXYZ)
 *****/
remote mActingAsAp Boolean nodelay; /* =true if STA started BSS */
remote mAid AssocId nodelay; /* AID assigned to STA by AP */
remote mAssoc Boolean nodelay; /* =true if STA associated w/BSS */
remote mAtimW Boolean nodelay; /* =true if ATIM window in prog */
remote mBkIP Boolean nodelay; /* =true if backoff in prog */
remote mBrates Ratestring nodelay; /* basic rate set for this sta */
remote mBssId MacAddr nodelay; /* identifier of current (I)BSS */
remote mCap Octetstring nodelay; /* capability info from MlmeJoin */
remote mCfp Boolean nodelay; /* =true if CF period in progress */
remote mDisable Boolean nodelay; /* =true if not in any BSS; then */
/* TX only sends probe_req; RX */
/* only accepts beacon, probe_rsp */
remote mDtimCount Integer nodelay; /* =0 at Tbt of Beacon with DTIM */
remote mDtimPeriod Integer nodelay; /* beacon periods per DTIM period */
remote mFxiP Boolean nodelay; /* =true during frame exchange seq */
remote mIbss Boolean nodelay; /* =true if STA is member of IBSS */
remote mListenInt Integer nodelay; /* beacons between wake up @TBTT */
remote mNavEnd Time nodelay; /* NAV end Time, <=now when idle */
remote mNextBdry Time nodelay; /* next boundary Time; =0 if none */
remote mNextTbtt Time nodelay; /* Time next beacon due to occur */
remote mOrates Ratestring nodelay; /* operational rate set in use */
remote mPcAvail Boolean nodelay; /* =true if point coord in BSS */
remote mPcDlvr Boolean nodelay; /* =true if CF delivery only */
remote mPcPoll Boolean nodelay; /* =true if CF delivery & polling */
remote mPdly Usec nodelay; /* probe delay from start or join */
remote mPsm PwrSave nodelay; /* power save mode of STA */
remote mPss PsState nodelay; /* power save state of STA */
remote mRxA Boolean nodelay; /* =true if RX indicated by PHY */
remote mSsId Octetstring nodelay; /* name of the current (I)BSS */
remote procedure TSF nodelay; /* read & update 64-bit TSF timer */
  fpar Integer, Boolean; returns Integer;

/*****
 *      Named static data values (names of form sXYZ)
 *****/
synonym sMaxMsduLng Integer = 2304; /* max octets in an MSDU */
synonym sMacHdrLng Integer = 24; /* octets in data header, no WEP */
synonym sWepHdrLng Integer = 28; /* octets in data header with WEP */
synonym sWepAddLng Integer = 8; /* octets added for WEP */
synonym sWdsAddLng Integer = 6; /* octets added for WDS (addr4) */
synonym sCrcLng Integer = 4; /* octets for crc32 (FCS, ICV) */
synonym sMaxMpduLng Integer = /* max octets in an MPDU */
  (sMaxMsduLng + sMacHdrLng + sWdsAddLng + sWepAddLng + sCrcLng);
syntype FrameIndexRange = Integer /* index range for octets in MPDU */
  constants 0 : sMaxMpduLng endsyntype FrameIndexRange;
synonym sTsOctet Integer = 24; /* first octet of Timestamp field */
synonym sMinFragLng Integer = 256; /* min value for aMpduMaxLength */
synonym sMaxFragNum Integer = /* maximum fragment number */
  (sMaxMsduLng / (sMinFragLng - sMacHdrLng - sCrcLng));
synonym sAckCtsLng Integer = 112; /* bits in ACK and CTS frames */

```

```

/*****
 *      Station configuration flags (static, supplementary to MIB)
 *****/
synonym sVersion Integer = 0;          /* supported Protocol Version */
synonym sCanBeAp Boolean = false;     /* =true if STA can operate as AP */
synonym sCanBePc Boolean = false;     /* =true if AP can be Point Coord */
synonym sCfPollable Boolean =true;    /* =true if responds to CF-polls */

/*****
 *      Discrete microsecond and Time Unit sorts
 *****/
/* SDL does not define the relationship between its concept
/* of Time and physical time in the system being described.
/* An abstraction is needed to establish this relationship,
/* because Time in SDL uses the semantics of Real, whereas
/* time in the MAC protocol is discrete, with the semantics
/* of Natural and a step size (resolution) of 1 microsecond. */

/* Most MAC times are defined using the subtypes of Integer
/* Usec and TU. These have operators for explicit conversion
/* to SDL Time (tUsec, tTU), SDL Duration (dUsec, dTU), and
/* from SDL Time (uTime, tuTime) as needed to comply with SDL's
/* strong type checking. Where the MAC state machines need to
/* access the contents of the TSF timer, SDL's "now" (current
/* time) is used. This yields readable time-dependent code,
/* but the value of "now" cannot be modified by an SDL program,
/* so adopting the TSF time from timestamps in received Beacons
/* or Probe Responses is shown as a call to remote procedure TSF. */

/* Microsecond sort -- also has operators tmin and tmax */
newtype Usec inherits Integer operators all;
adding operators
dUsec : Usec -> Duration;
tUsec : Usec -> Time;
uTime : Time -> Usec;
tmax : Usec, Usec -> Usec;
tmin : Usec, Usec -> Usec;
axioms
for all u, w in Usec(
  u >= w ==> tmax(u, w) == u;
  u < w ==> tmax(u, w) == w;
  u >= w ==> tmin(u, w) == w;
  u < w ==> tmin(u, w) == u;
  for all t in Time( for all r in Real(
    r = float(u) ==> tUsec(u) == Time!(Duration!(r));
    t = Time!(Duration!(r)) and u = fix(r) ==> u == uTime(t));)
  for all d in Duration( for all r in Real(
    r = float(u) ==> dUsec(u) == Duration!(r); ));
constants >= 0 /* constrain value range to be non-negative */
endnewtype Usec;

/* Time Unit sort -- (1 * TU) = (1024 * Usec) */
newtype TU inherits Integer operators all;
adding operators
dTU : TU -> Duration;
tTU : TU -> Time;
tuTime : Time -> TU;
u2TU : Usec -> TU;
tu2U : TU -> Usec;
axioms
for all k in TU( for all t in Time( for all r in Real(
  r = float(k) ==> tTU(k) == Time!(Duration!(1024 * r));
  t = Time!(Duration!(r)) and k = (fix(r) / 1024) ==>
  k == tuTime(t));)

```

```

    for all d in Duration(      for all r in Real(
        r = float(k) ==> dTU(k) == Duration!(1024 * r));)
    for all u in Usec(
        u2TU(u) == u / 1024;      k2U(k) == k * 1024; );
    constants >= 0 /* constrain value range to be non-negative */
endnewtype TU;

/*****
* Generator for 0-origin String sorts (adapted from Z.105, Annex A)
*****/
/* String0(sort, nullSymbol) can define strings of any sort.
/* These strings are indexed starting from 0 rather than 1.
/* Sorts defined by String0 have the normal String operators, plus
/* Tail (all but first item), Head (all but last item), and
/* aggregators S2, S3, S4, S6, S8 (make fixed length strings). */
generator String0(type Item, literal Emptystring)
    literals Emptystring;
    operators
        MkString : Item -> String0;      /* make a string from an item */
        Length : String0 -> Integer;     /* length of string */
        First : String0 -> Item;         /* first item in string */
        Tail : String0 -> String0;      /* all but first item in string */
        Last : String0 -> Item;         /* last item in string */
        head : String0 -> String0;      /* all but last item in string */
        "/" : String0, String0 -> String0; /* concatenation */
        Extract! : String0, Integer -> Item; /* get item from string */
        Modify! : String0, Integer, Item -> String0; /* modify string */
        SubStr : String0, Integer, Integer -> String0;
            /* SubStr(s,i,j) is string0 of length j starting at string0(i) */
        S2 : Item, Item -> String0;      S3 : Item, Item, Item -> String0;
        S4 : Item, Item, Item, Item -> String0;
        S6 : Item, Item, Item, Item, Item, Item -> String0;
        S8 : Item, Item, Item, Item, Item, Item, Item, Item -> String0;
    axioms
        for all item0,item1,item2,item3,item4,item5,item6,item7 in Item(
            for all s, s1, S2, S3 in String0(      for all i, j in Integer(
                /* constructors are Emptystring, MkString, and "/"; */
                /* equalities between constructor terms */
                s // Emptystring == s;
                Emptystring // s == s;
                (s1 // S2) // S3 == s1 // (S2 // S3);
            /* definition of Length by applying it to all constructors */
                type String Length(Emptystring) == 0;
                type String Length(MkString(item0)) == 1;
                type String Length(s1 // S2) == Length(s1) + Length(S2);
            /* definition of Extract! by applying it to all constructors, */
                Extract!(MkString(item0), 0) == item0;
                i < Length(s1) ==> Extract!(s1 // S2, i) == Extract!(s1, i);
                i >= Length(s1)
                    ==> Extract!(s1 // S2, i) == Extract!(S2, i - Length(s1));
                i < 0 or i >= Length(s) ==> Extract!(s, i) == error!;
            /* definition of First and Last by other operations */
                First(s) == Extract!(s, 0);
                Last(s) == Extract!(s, Length(s) - 1);
            /* definition of substr(s,i,j) by induction on j, */
                i >= 0 and i <= Length(s) ==> SubStr(s, i, 0) == Emptystring;
                i >= 0 and j > 0 and i + j <= Length(s)
                    ==> SubStr(s, i, j) ==
                        SubStr(s, i, j - 1) // MkString(Extract!(s, i + j - 1));
                i < 0 or j < 0 or i + j > Length(s) ==> SubStr(s, i, j) ==
                    error!;
            /* definition of Modify!, Head, Tail, Sx by other operations */
                Modify!(s, i, item0) ==
                    SubStr(s, 0, i) // MkString(item0) //

```

```

        SubStr(s, i + 1, Length(s) - i - 1);
    head(s) == SubStr(s, 0, Length(s) - 1);
    Tail(s) == SubStr(s, 1, Length(s) - 1);
    S2(item0, item1) == MkString(item0) // MkString(item1);
    S3(item0, item1, item2) ==
        MkString(item0) // MkString(item1) // MkString(item2);
    S4(item0, item1, item2, item3) ==
        MkString(item0) // MkString(item1) // MkString(item2) //
        MkString(item3);
    S6(item0, item1, item2, item3, item4, item5) ==
        MkString(item0) // MkString(item1) // MkString(item2) //
        MkString(item3) // MkString(item4) // MkString(item5);
    S8(item0, item1, item2, item3, item4, item5, item6, item7) ==
        MkString(item0) // MkString(item1) // MkString(item2) //
        MkString(item3) // MkString(item4) // MkString(item5) //
        MkString(item6) // MkString(item7); ));
endgenerator String0;

/*****
 *   ASN.1-style BIT sort (from Z.105, Annex A)
 *****/
/* Bit is a subtype of Boolean -- bit values 0 and 1 are
/* not numerals and cannot be used with Integer operators */
newtype Bit inherits Boolean
    literals 0 = false, 1 = true; operators all; endnewtype Bit;

/*****
 *   ASN.1-style BIT STRING sort (adapted from Z.105, Annex A)
 *****/
/* Bitstrings are 0-origin strings of Bit. Z.105 uses ASN.1-style
/* literals in binary ('1011'B) or hexadecimal ('D3'H), but this
/* syntax is not accepted for Z.100 string literals. Therefore,
/* this version provides only hexadecimal literals 0x00-0xFF.
/* Bitstring operators '='>', 'not', 'and', 'or', and 'xor' act
/* bitwise, with the length of the result string equal to the
/* length of the longest (or only) source string. */
newtype Bitstring String0(Bit, '')
    adding literals macro Hex_Literals;
    operators
        "not" : Bitstring -> Bitstring;
        "and" : Bitstring, Bitstring -> Bitstring;
        "or" : Bitstring, Bitstring -> Bitstring;
        "xor" : Bitstring, Bitstring -> Bitstring;
        "=>" : Bitstring, Bitstring -> Bitstring; noequality;
    axioms macro Hex_Axioms;
    for all s, s1, S2, S3 in Bitstring(
        s = s == true;
        s1 = S2 == S2 = s1;
        s1 /= S2 == not (s1 = S2);
        s1 = S2 == true ==> s1 == S2;
        ((s1 = S2) and (S2 = S3)) ==> s1 = S3 == true;
        ((s1 = S2) and (S2 /= S3)) ==> s1 = S3 == false;
    for all b, b1, b2 in Bit(
        not ('') == "";
        not (MkString(b) // s) == MkString(not (b)) // not (s);
'' and '' == '';
    Length(s) > 0 ==> '' and s == MkString(0) and s;
    Length(s) > 0 ==> s and '' == s and MkString(0);
    (MkString(b1) // s1) and (MkString(b2) // S2) ==
        MkString(b1 and b2) // (s1 and S2);
    s1 or S2 == not (not s1 and not S2);
    s1 xor S2 == (s1 or S2) and not (s1 and S2);
    s1 => S2 == not (not s1 and S2));
    map for all b1, b2 in Bitstring literals(

```

```

    for all bs1, bs2 in Charstring literals(
/* connection to the String generator */
    for all b in Bit literals(
        spelling(b1) = '''' // bs1 // bs2 // '''' ,
        spelling(b2) = '''' // bs2 // '''' , spelling(b) = bs1
        ==> b1 == MkString(b) // b2; ));
endnewtype Bitstring;

/*****
*   OCTET sort           (influenced by Z.105, Annex A)
*****/
/* Octet is a subtype of Bitstring where length always =8.
/* Z.105 adds a "size "keyword to SDL and defines Octet with
/* "... constants size (8) ..." to impose this length constraint.
/* Here Octet relies on proper use maintain lengths as multiples
/* of 8. Proper length strings are created by the hexadecimal
/* Bitstring literals (e.g. 0xD5) and operator mkOctet:
/*   o:= mkOctet(i)      converts a non-negative Integer (mod 256)
/*                       to an Octet (exactly 8 bits)
/*   i:= octetVal(o)     converts an Octet to an Integer (0:255)
/*   o:= flip(o)        reverses bit order of the Octet
/*                       (0<-->7, 1<-->6, 2<-->5, 3<-->4) */
newtype Octet inherits Bitstring operators all;
adding operators
mkOctet : Integer -> Octet;
octetVal : Octet -> Integer;
flip    : Octet -> Octet;
axioms
for all i in Integer(    for all z in Octet(
    i = 0 ==> mkOctet(i) == S8(0, 0, 0, 0, 0, 0, 0, 0);
    i = 1 ==> mkOctet(i) == S8(1, 0, 0, 0, 0, 0, 0, 0);
    i > 1 and i <= 255 ==> mkOctet(i) ==
        SubStr((First(mkOctet(i mod 2)) // mkOctet(i / 2)), 0, 8);
    i > 255 ==> mkOctet(i) == mkOctet(i mod 256);
    i < 0 ==> mkOctet(i) == error!;
    z = MkString(0) ==> octetVal(z) == 0;
    z = MkString(1) ==> octetVal(z) == 1;
    Length(z) > 1 and Length(z) <= 8 ==>
        octetVal(z) == octetVal(First(z)) +
            (2 * (octetVal(SubStr(z, 1, Length(z) - 1))));
    Length(z) > 8 ==> octetVal(z) == error!;
    flip(z) == S8(z(7),z(6),z(5),z(4),z(3),z(2),z(1),z(0)); ));
endnewtype Octet;

/*****
*   OCTET STRING sort   (somewhat influenced by Z.105, Annex A)
*****/
/* Octetstrings are 0-ORIGIN strings of Octet, NOT 1-ORIGIN
/* strings like Octet_String in Z.105 (hence the name change).
/* Octetstring has conversion operators to and from Bitstring,
/* and integer to Octetstring. Octetstring literals are "null"
/* and 1-4, 6, 8 item 0x00 strings O1, O2, O3, O4, O6, O8. */
newtype Octetstring String0(Octet, null)
adding literals O1, O2, O3, O4, O6, O8;
operators
B_S : Octetstring -> Bitstring; /* name changed from Z.105 */
O_S : Bitstring -> Octetstring; /* name changed from Z.105 */
mkOS : Integer,Integer -> Octetstring; /* mkOS(i1,i2) returns */
/* mkstring(mkOctet(i1)) padded (0x00) to length i2 */
mk2octets : Integer -> Octetstring; /* 16-bit integer to 2-octets */
axioms
for all b, b1, b2 in Bitstring(
    for all s in Octetstring(    for all o in Octet(
        B_S(null) == '';

```

```

    B_S(MkString(o) // s) == o // B_S(s);
    O_S('') == null;
    Length(b1) > 0, Length(b1) < 8 ==>
        O_S(b1) == MkString(b1 or 0x00); /* expand b1 to 8 bits */
    b == b1 // b2, Length(b1) = 8 ==>
        O_S(b) == MkString(b1) // O_S(b2);
    for all i, k in Integer(
        k = 1 ==> mkOS(i, k) == MkString(mkOctet(i));
        k > 1 ==> mkOS(i, k) == mkOS(i, k - 1) // MkString(0x00);
        k <= 0 ==> error!;
        mk2octets(i) == MkString(mkOctet(i mod 256)) //
            MkString(mkOctet(i / 256)); );
    O1 == MkString(0x00);    O2 == O1 // O1;
    O3 == O2 // O1;        O4 == O2 // O2;
    O6 == O4 // O2;        O8 == O4 // O4; ));
    map for all O1, O2 in Octetstring literals(
        for all b1, b2 in Bitstring literals(
            spelling(O1) = spelling(b1), spelling(O2) = spelling(b2)
            ==> O1 = O2 == b1 = b2; ));
endnewtype Octetstring;

/*****
*   MAC Address sorts
*****/

/* MacAddr is a subtype of Octetstring with added operators:
/*   isGroup(m) =true if given a group address
/*   isBcst(m)  =true if given the broadcast address
/*   isLocal(m) =true if given a locally-administered address
/*   adrOs(m)   converts MacAddr to Octetstring
/* MAC addresses must be defined to be exactly 6 octets long,
/* typically using the S6 operator or nullAddr synonym. */
newtype MacAddr inherits Octetstring operators all;
adding operators
    isGroup : MacAddr -> Boolean;
    isBcst  : MacAddr -> Boolean;
    isLocal : MacAddr -> Boolean;
    adrOs   : MacAddr -> Octetstring;
axioms
    for all m in MacAddr(
        (Length(m) = 6) and ((Extract!(m,0) and 0x01) = 0x01)
            ==> isGroup(m) == true;
        (Length(m) = 6) and ((Extract!(m,0) and 0x01) = 0x00)
            ==> isGroup(m) == false;
        (Length(m) = 6) and (m = S6(0xFF,0xFF,0xFF,0xFF,0xFF,0xFF))
            ==> isBcst == true;
        (Length(m) = 6) and (m /= S6(0xFF,0xFF,0xFF,0xFF,0xFF,0xFF))
            ==> isBcst == false;
        (Length(m) = 6) and ((Extract!(m,0) and 0x02) = 0x02)
            ==> isLocal == true;
        (Length(m) = 6) and ((Extract!(m,0) and 0x02) = 0x00)
            ==> isLocal == false;
        Length(m) /= 6 ==> error! /* common error! term */;
        for all o in Octetstring(m = MacAddr!(o) == adrOs(m) = o; ));
endnewtype MacAddr;

newtype MacAddrSet powerset( MacAddr) endnewtype MacAddrSet;

synonym bcstAddr MacAddr = /* Broadcast Address */
    <<type MacAddr>> S6(0xFF,0xFF,0xFF,0xFF,0xFF,0xFF);

synonym nullAddr MacAddr = /* Null Address */
    << type MacAddr>> S6(0x00,0x00,0x00,0x00,0x00,0x00);

```

```

/*****
 *      BSS description sorts
 *****/
/* BssDscr is used with MlmeScan.confirm and MlmeJoin.request */
newtype BssDscr struct
  bdBssId      MacAddr;
  bdSsid      Octetstring; /* 1 <= length <= 32 */
  bdType      BssType;
  bdBcnPer    TU; /* beacon period in Time Units */
  bdDtimPer   Integer; /* DTIM period in beacon periods */
  bdTstamp    Octetstring; /* 8 Octets from ProbeRsp/Beacon */
  bdStartTs   Octetstring; /* 8 Octets TSF when rx Tstamp */
  bdPhyParms  PhyParms; /* empty if not needed by PHY */
  bdCfParms   CfParms; /* empty if not CfPollable/no PCF */
  bdIbssParms IbssParms; /* empty if infrastructure BSS */
  bdCap       Capability; /* capability information */
  bdBrates    Ratestring; /* BSS basic rate set */
endnewtype BssDscr;

newtype BssDscrSet powerset( BssDscr) endnewtype BssDscrSet;

/*****
 *      Duplicate filtering support sorts
 *****/
syntype FragNum = Integer /* Range of possible fragment numbers */
  constants 0:sMaxFragNum endsyntype FragNum;
syntype SeqNum = Integer /* Range of possible sequence numbers */
  constants 0:4095 endsyntype SeqNum;
newtype Tuple struct /* for duplicate filtering & defragmentation */
  full Boolean; /* =true if Tuple contains valid info */
  ta MacAddr; /* transmitting station address (Addr2) */
  sn SeqNum; /* Msdu/Mmpdu sequence number */
  fn FragNum; /* most recent Mpdu fragment number */
  tRx Time; /* reception time (endRx of fragment) */
  default (. false, nullAddr, 0, 0, 0.);
endnewtype Tuple;

/*****
 *      TupleCache support sorts
 *****/
/* Number of TupleCache entries and associated index range */
synonym tupleCacheSize Integer = 32; /* this value is an example,
  TupleCache size is implementation dependent */
syntype CacheIndex = Integer constants 1:tupleCacheSize
  endsyntype CacheIndex;

/* TupleCache array
/* cache:= ClearTupleCache(cache) to initialize cache
/* cache:= UpdateTupleCache(cache, addr, seq, frag, endRx)
/* if <addr,seq> is already cached, updates frag
/* if <addr,seq> not cached, fills an empty entry
/* or replaces an entry using an unspecified algorithm
/* SearchTupleCache(cache, addr, seq, frag)
/* returns true if specified <addr,seq,frag> in cache */
newtype TupleCache Array( CacheIndex, Tuple);
adding operators
  ClearTupleCache : TupleCache -> TupleCache;
  SearchTupleCache : TupleCache, MacAddr, SeqNum, FragNum -> Boolean;
  UpdateTupleCache : TupleCache, MacAddr, SeqNum, FragNum, Time ->
    TupleCache;
operator ClearTupleCache;
  fpar cache TupleCache; returns TupleCache; referenced;
operator SearchTupleCache;
  fpar cache TupleCache, taddr MacAddr, tseq SeqNum, tfrag FragNum;

```



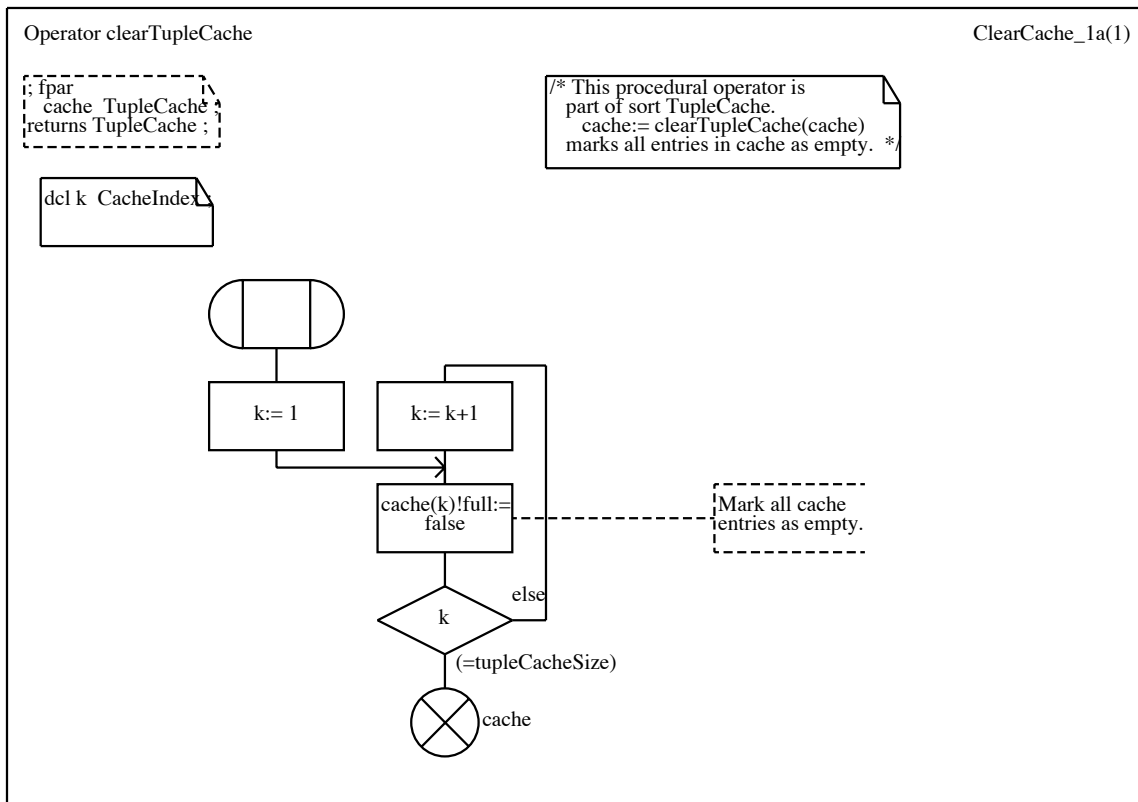
```

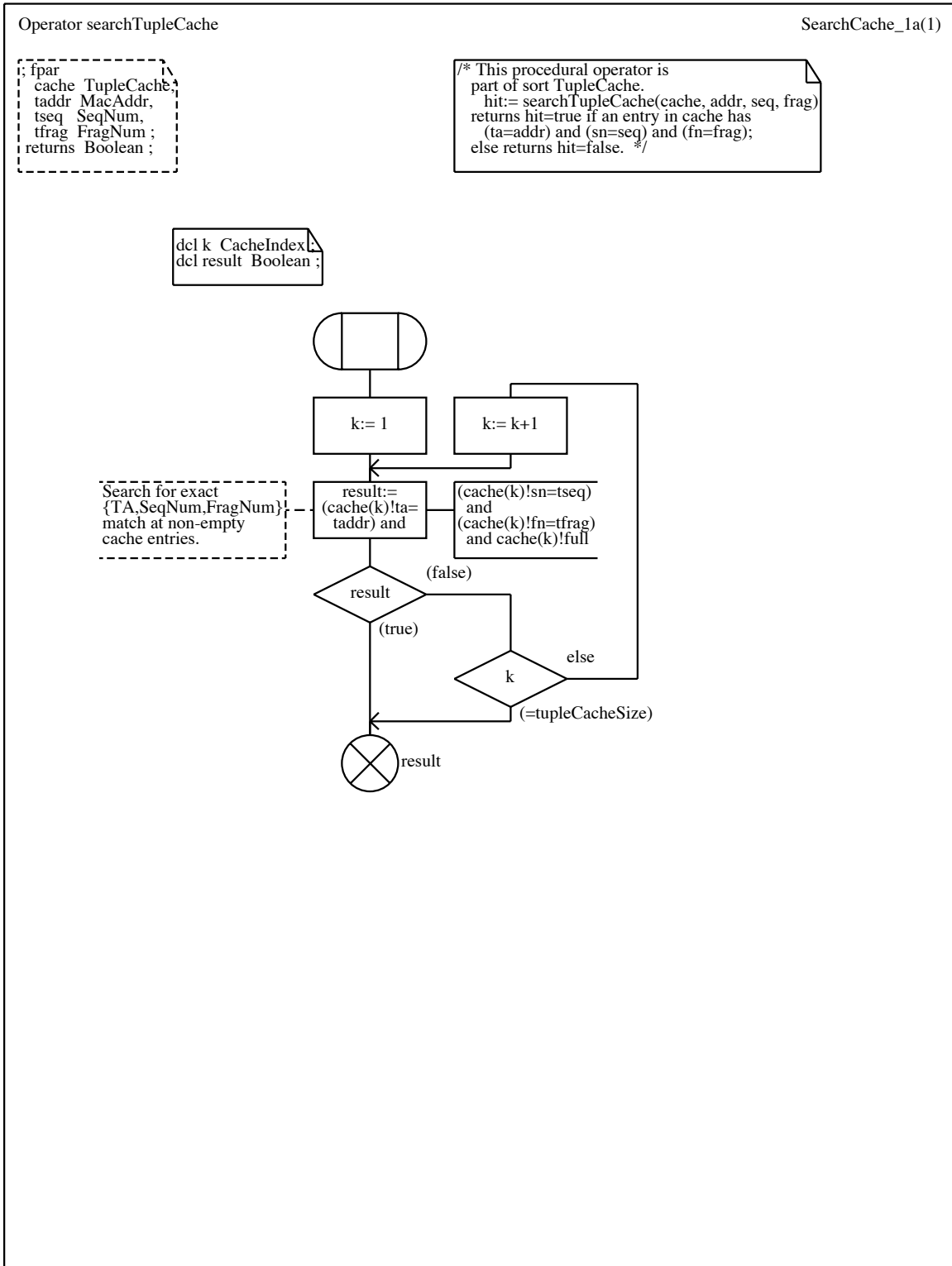
returns Boolean; referenced;
operator UpdateTupleCache;
  fpar cache TupleCache, taddr MacAddr, tseq SeqNum, tfrag FragNum,
  tnow Time; returns TupleCache; referenced;
endnewtype TupleCache;

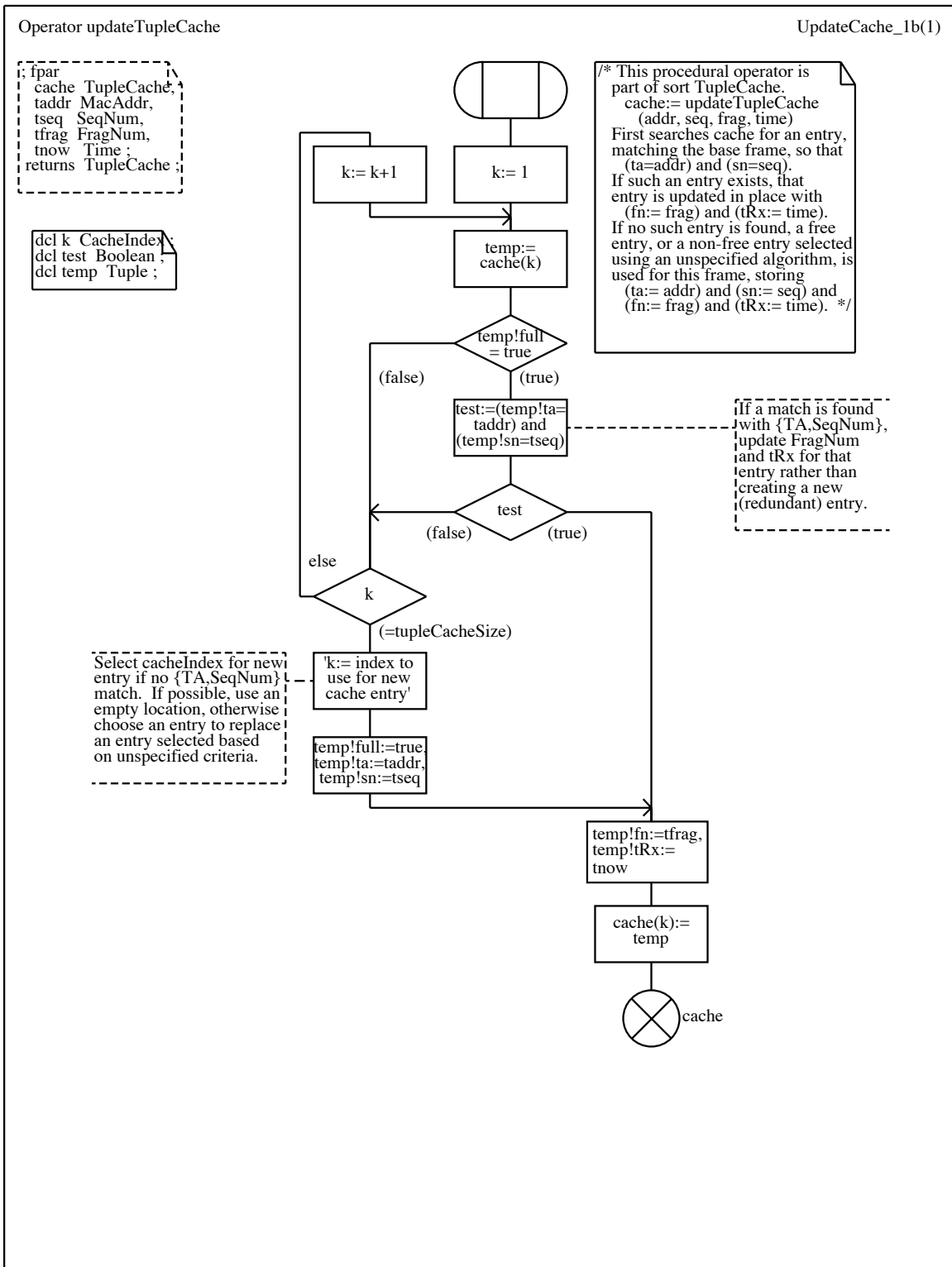
/*****
 * 32-bit Counter sort and Integer string sort
 *****/
/* This sort used for MIB counters, needed because SDL Integers
/* have no specified maximum value. inc(counter) increments the
/* counter value by 1, with wraparound from (2^32)-1 to 0. */
newtype Counter32 inherits Integer operators all;
  adding operators
  inc : Counter32 -> Counter32;
  axioms
  for all c in Counter32(
    c < 4294967295 ==> inc(c) == c + 1;
    c >= 4294967295 ==> inc(c) == 0; );
endnewtype Counter32;

/* String (1-origin) of Integer */
newtype Intstring String( Integer, noInt); endnewtype Intstring;

```







```

/*****
* Generator for Queue sorts
*****/
/* The Queue generator is derived from the String0 generator
/* to create Queues of any sort. Queues operators are:
/* Qfirst(queue,item) adds item as the first queue element
/* Qlast(queue,item) adds item as the last queue element
/* and the String0 operators Length, //, First, Last, Head, Tail
/* Because operators can only return a single value, removing an
/* element from a queue is a 2-step process:
/* dequeue first: item:=First(queue); queue:=Tail(queue);
/* dequeue last: item:=Last(queue); queue:=Head(queue); */
generator Queue(type Item, literal Emptyqueue)
  literals Emptyqueue;
  operators
    MkQ : Item -> Queue; /* make a queue from an item */
    Lent : Queue -> Integer; /* number of items on queue */
    First : Queue -> Item; /* first item on queue */
    Qfirst : Queue,Item -> Queue; /* add item as first on queue */
    Tail : Queue -> Queue; /* all but first item on queue */
    Last : Queue -> Item; /* last item on queue */
    Qlast : Queue,Item -> Queue; /* add item as last on queue */
    head : Queue -> Queue; /* all but last item on queue */
    "/" : Queue,Queue -> Queue; /* concatenation */
    Extract! : Queue,Integer -> Item; /* copy item from queue */
    Modify! : Queue,Integer,Item -> Queue; /* modify item in queue */
    SubQ : Queue,Integer,Integer -> Queue;
    /* SubQ(q,i,j) queue of length j starting from queue(i) */
  axioms
    for all item0 in Item( for all q, q1, q2, q3 in Queue(
      for all i, j in Integer(
        /* constructors are Emptyqueue, MkQueue, and "/"; */
        /* equalities between constructor terms */
        q // Emptyqueue == q;
        Emptyqueue // q == q;
        (q1 // q2) // q3 == q1 // (q2 // q3);
        /* definition of Length by applying it to all constructors */
        type Queue Length(Emptyqueue) == 0;
        type Queue Length(MkQueue(item0)) == 1;
        type Queue Length(q1 // q2) == Length(q1) + Length(q2);
        /* definition of Extract! by applying it to all constructors, */
        Extract!(MkQueue(item0), 0) == item0;
        i < Length(q1) ==> Extract!(q1 // q2, i) == Extract!(q1, i);
        i >= Length(q1)
          ==> Extract!(q1 // q2, i) == Extract!(q2, i - Length(q1));
        i < 0 or i >= Length(q) ==> Extract!(q, i) == error!;
        /* definition of First and Last by other operations */
        First(q) == Extract!(q, 0);
        Last(q) == Extract!(q, Length(q) - 1);
        /* definition of SubQ(q,i,j) by induction on j, */
        i >= 0 and i <= Length(q) ==> SubQ(q, i, 0) == Emptyqueue;
        i >= 0 and j > 0 and i + j <= Length(q) ==> SubQ(q, i, j) ==
          SubQ(q, i, j - 1) // MkQueue(Extract!(q, i + j - 1));
        i < 0 or j < 0 or i + j > Length(q) ==> SubQ(q,i,j) == error!;
        /* define Modify!, Head, Tail, Qfirst, Qlast by other ops */
        Modify!(q, i, item0) == SubQ(q, 0, I) //
          MkQueue(item0) // SubQ(q, i + 1, Length(q) - i - 1);
        head(q) == SubQ(q, 0, Length(q) - 1);
        Tail(q) == SubQ(q, 1, Length(q) - 1);
        Qfirst(q, item0) == MkQueue(item0) // q;
        Qlast(q, item0) == q // MkQueue(item0); )))
endgenerator Queue;

```

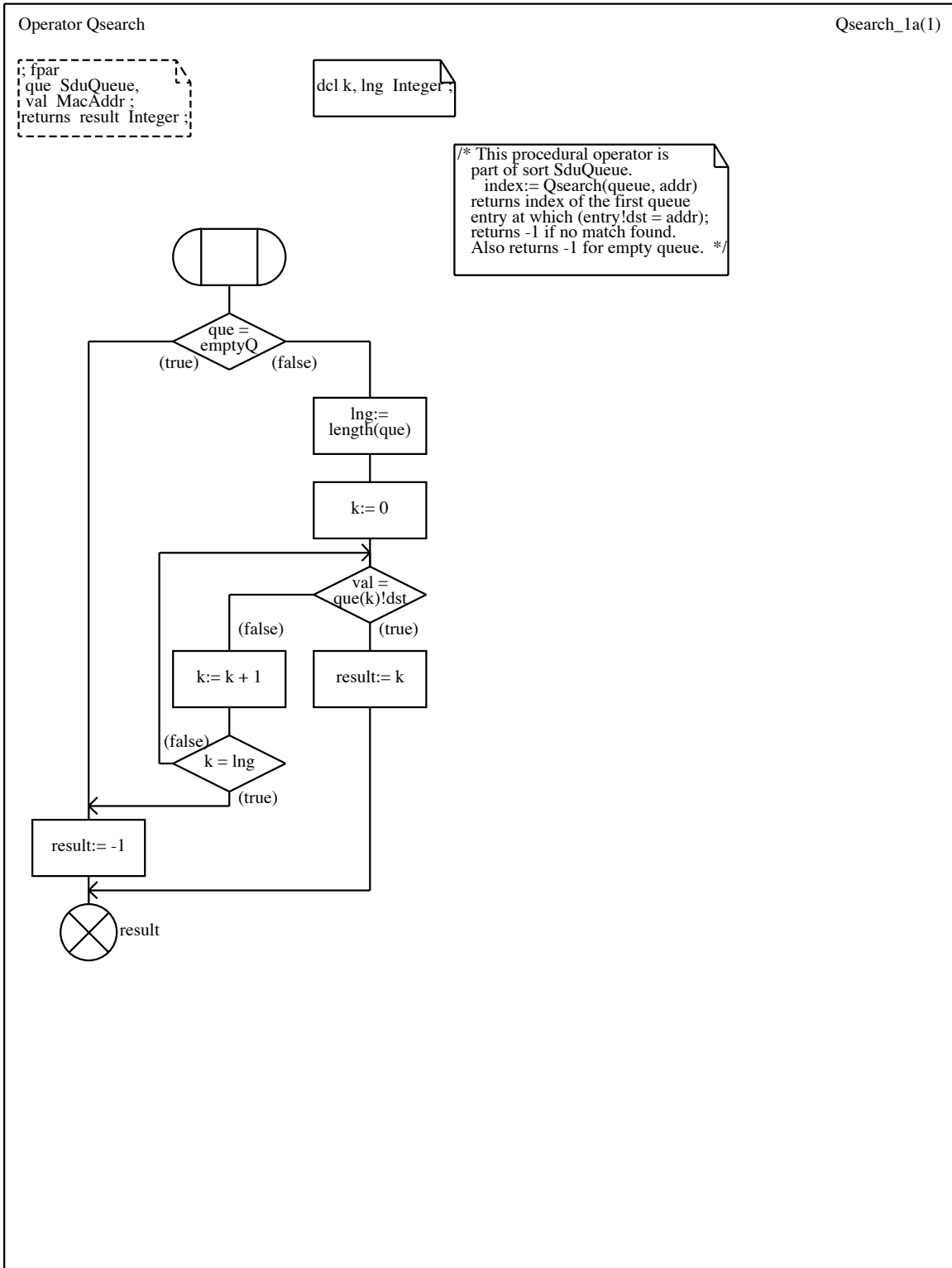
```

/*****
 *
 *   Fragmentation support sorts
 *****/
/* Array to hold up to FragNum fragments of an Msdu/Mmpdu */
newtype FragArray Array(FragNum, Frame); endnewtype FragArray;

/* FragSdu structure is for OUTGOING MSDUs/MMPDUs (called SDUs)
/* Each SDU, even if not fragmented, is held in an instance of
/* this structure awaiting its (re)transmission attempt(s).
/* Transmit queue(s) are ordered lists of FragSdu instances. */
newtype FragSdu struct
  fTot      FragNum;          /* number of fragments in pdus FragArray */
  fCur      FragNum;          /* next fragment number to send */
  fAnc      FragNum;          /* next fragment to announce in ATIM or TIM
                               when fAnc > fCur, pdus(fCur)+ may be sent */
  eol       Time;            /* set to (now + dUsec(aMaxTxMsduLifetime))
                               when the entry is created */
  sqf       SeqNum;          /* SDU sequence number, set at 1st Tx attempt */
  src       Integer;         /* short retry counter for this SDU */
  lrc       Integer;         /* long retry counter for this SDU */
  dst       MacAddr;         /* destination address */
  grpa      Boolean;         /* =true if RA (not DA) is a group address */
  psm       Boolean;         /* =true if RA (not DA) may be in pwr_save */
  resume    Boolean;         /* =true if fragment burst being resumed */
  cnfTo     PId;             /* address to which confirmation is sent */
  txrate    Rate;           /* data rate used for initial fragment */
  cf        CfPriority;       /* requested priority (from LLC) */
  pdus      FragArray;       /* array of Frame to hold fragments */
endnewtype FragSdu;

/* Queue of FragSdu
/* for power save buffers, etc., searchable with Qsearch operator:
/*   index:= Qsearch(queue, addr)   where queue is an SduQueue,
/*   index identifies the first queue entry at which
/*   entry!dst = addr; or as -1 if no match (or queue empty). */
newtype SduQueue Queue(FragSdu, emptyQ);
adding operators
  qSearch : SduQueue, MacAddr -> Integer;
operator qSearch;
  fpar que SduQueue, val MacAddr; returns Integer; referenced;
endnewtype SduQueue;

```



```

/*****
*   Defragmentation support sorts
*****/

/* The PartialSdu structure is for INCOMPLETE MSDUs/MMPDUs
/* (generically SDUs) for which at least 1 fragment has been
/* received. Unfragmented SDUs are reported upward immediately,
/* and are never stored in instances of this structure. */

newtype PartialSdu struct
    inUse      Boolean;      /* =true if this instance holds any fragments */
    rta        MacAddr;      /* transmitting station (Addr2) */
    rsn        SeqNum;       /* SDU sequence number */
    rCur      FragNum;      /* fragment number of most recent Mpdu */
    reol       Time;         /* (now+dUsec(aMaxReceiveLifetime) @ 1st Mpdu */
    rsdu       Frame;        /* buffer where Mpdu's are concatenated */
    default (. false, nullAddr, 0, 0, 0, null.);
endnewtype PartialSdu;

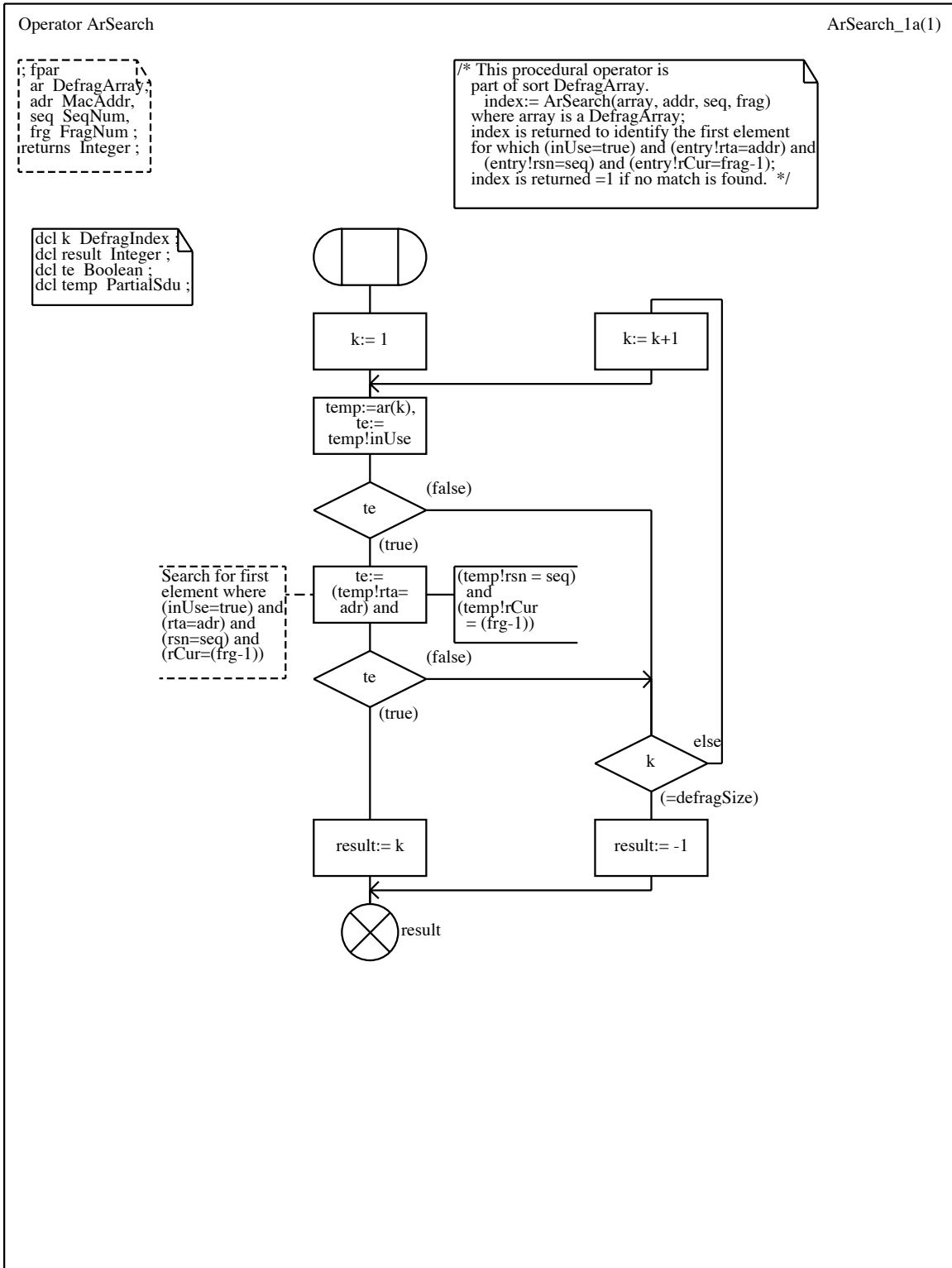
newtype PartialSduKeys struct          /* if aPrivacyOptionImplemented=true */
    wDefKeys   KeyVector;              /* default keys when 1st frag received */
    wKeyMap    KeyMapArray;            /* key mappings when 1st frag received */
    wExclude   Boolean;                /* aExcludeUnencrypted @ 1st frag rx */
endnewtype PartialSduKeys;

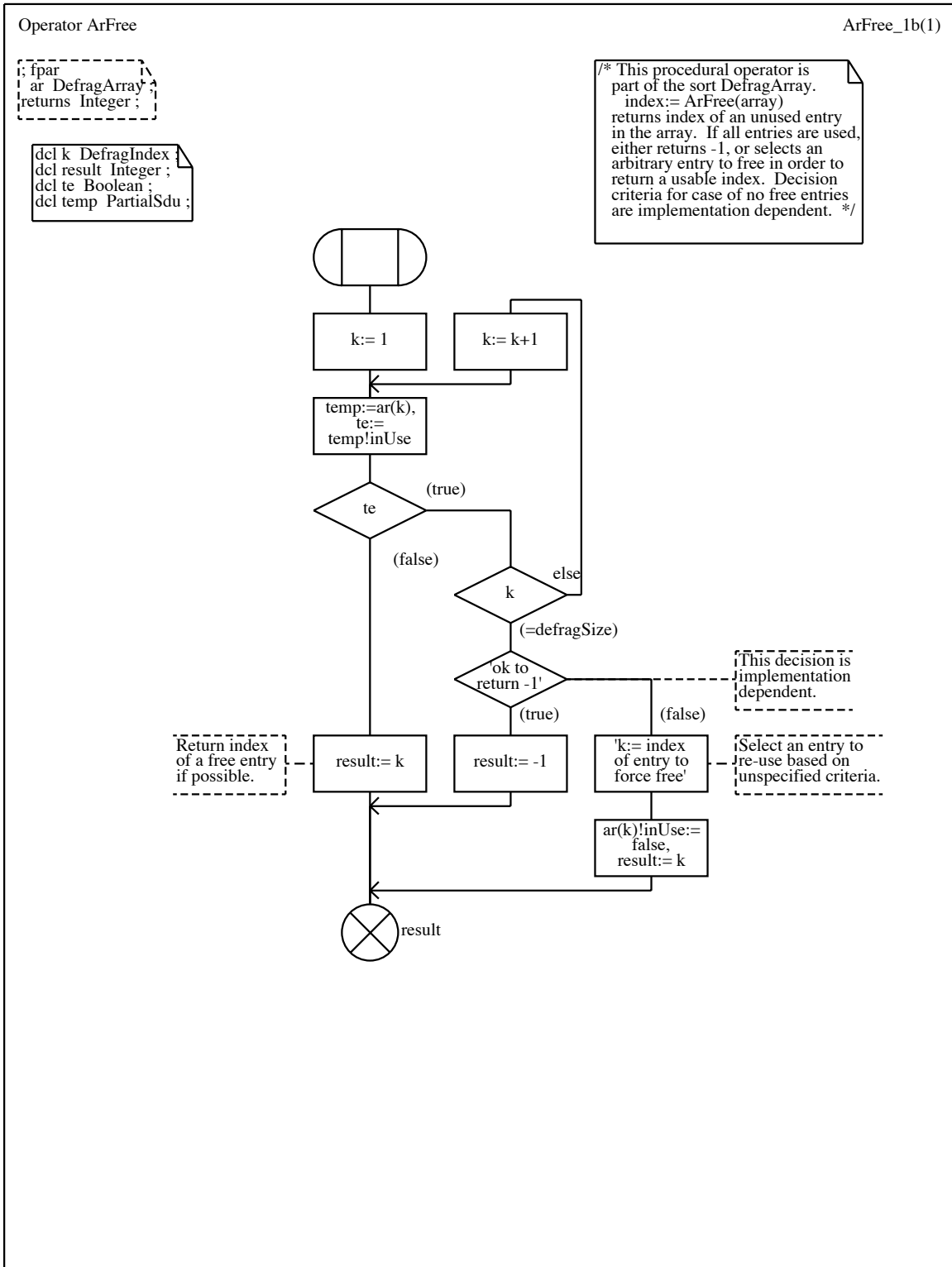
/* Number of entries in defragmentation array at this station.
/* The value is implementation dependent (min=3, see 9.5). */
synonym defragSize Integer = 6;
syntype defragIndex = Integer constants 1:defragSize
endsyntype defragIndex;

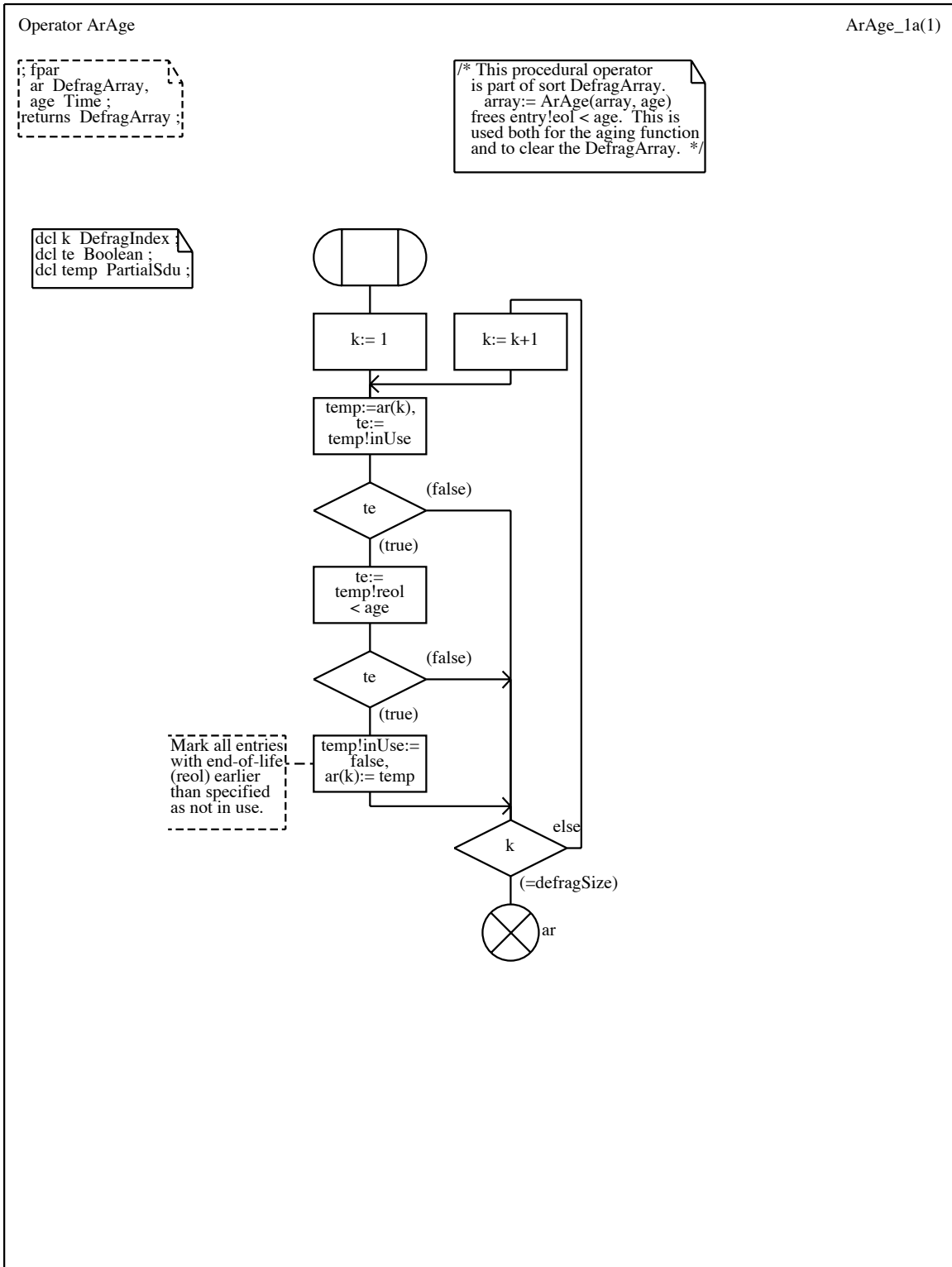
/* Array of PartialSdu for use defragmenting Msdus and Mmpdus.
/* Searchable using the ArSearch operator
/*   index:= ArSearch(array, addr, seq, frag)
/* where index is returned to identify the first element for which
/* ((inUse = true) and (entry!rta = addr) and (entry!rsn = seq)
/* and (entry!rCur = (frag-1))); or as =1 if no match found.
/*   index:= ArFree(array) returns the index of a free entry,
/* or -1 if no entries free. May free an entry, selected using
/* an unspecified algorithm, to avoid returning -1.
/*   array:= ArAge(array, age)
/* frees where (entry!eol < age), also used to clear array. */
newtype DefragArray Array( defragIndex, PartialSdu);
adding operators
    ArSearch : DefragArray, MacAddr, SeqNum, FragNum -> Integer;
    ArFree   : DefragArray -> Integer;
    ArAge    : DefragArray, Time -> DefragArray;
operator ArSearch;
    fpar ar DefragArray,  adr MacAddr,  seq SeqNum,  frg FragNum;
    returns Integer; referenced;
operator ArFree; fpar ar DefragArray; returns Integer; referenced;
operator ArAge; fpar ar DefragArray,  age Time;
    returns DefragArray; referenced;
endnewtype DefragArray;

newtype DefragKeysArray Array( defragIndex, PartialSduKeys);
endnewtype DefragKeysArray;

```







```

/*****
*   CRC-32 sorts (for FCS and ICV)
*****/

/* Crc is a subtype of Octetstring with added operators:
/*   crc:= Crc32(crc,octet)
/* updates the crc value to include the new octet, and
/*   Mirror(crc), which returns a Crc value with the order
/* of the octets, and of the bits in each octet, reversed for
/* MSb-first transmission (see 7.1.1). Crc variables must have
/* exactly 4 octets, which is done using initCrc or S4. */

newtype Crc inherits Octetstring operators all;
adding operators
  Crc32 : Crc, Octet -> Crc;
  mirror : Crc -> Octetstring;
operator Crc32; fpar crcin Crc, val Octet; returns Crc; referenced;
axioms for all c in Crc(
  mirror(c) == S4(flip(c(3)),flip(c(2)),flip(c(1)), flip(c(0))); );
endnewtype Crc;

synonym initCrc Crc = /* Initial Crc value (all 1s) */
  << type Crc>> S4(0xFF,0xFF,0xFF,0xFF);
synonym goodCrc Crc = /* Unique remainder for valid CRC-32 */
  << type Crc>> S4(0x7B,0xDD,0x04,0xC7);

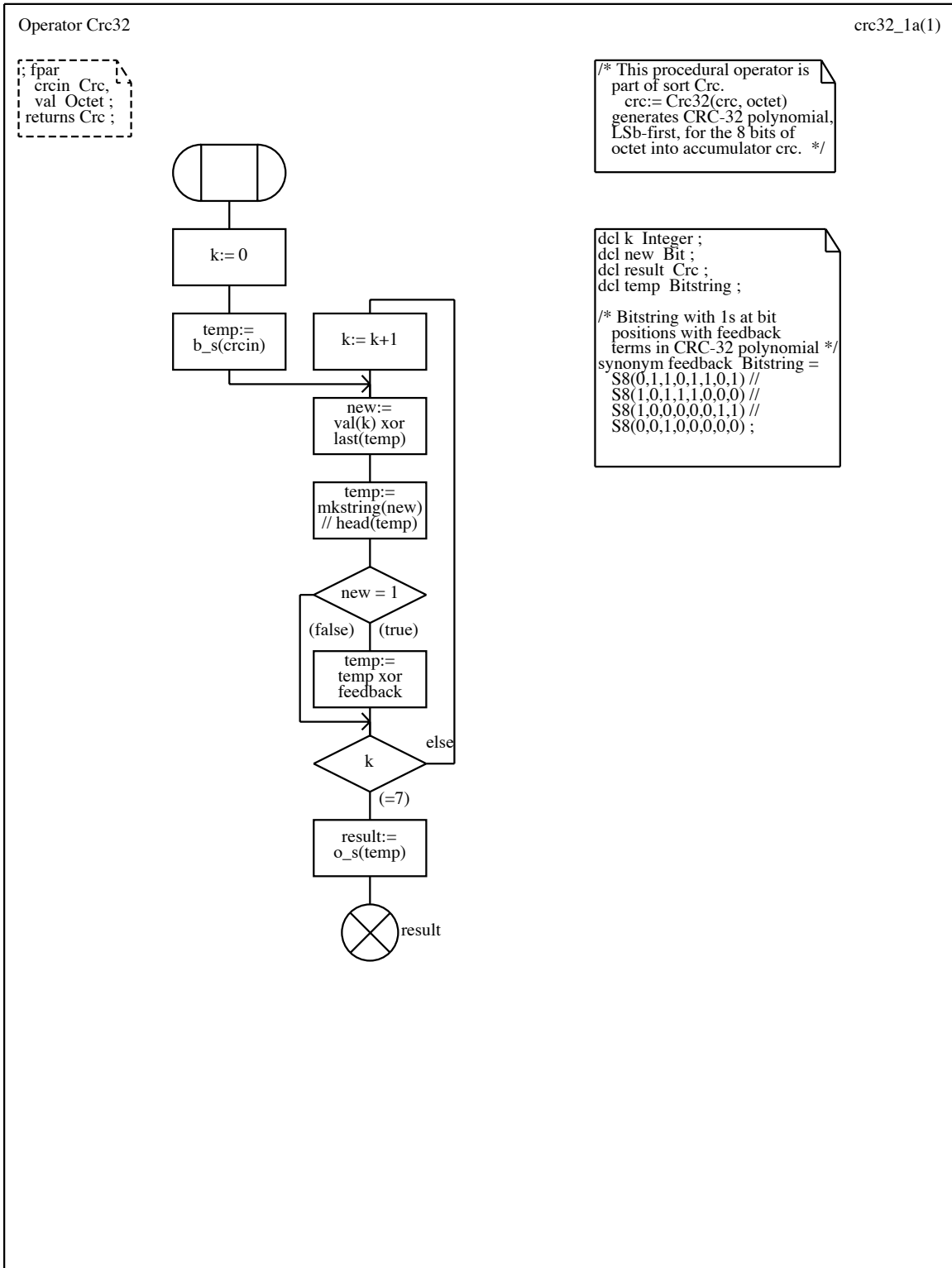
/*****
*   WEP support sorts
*****/

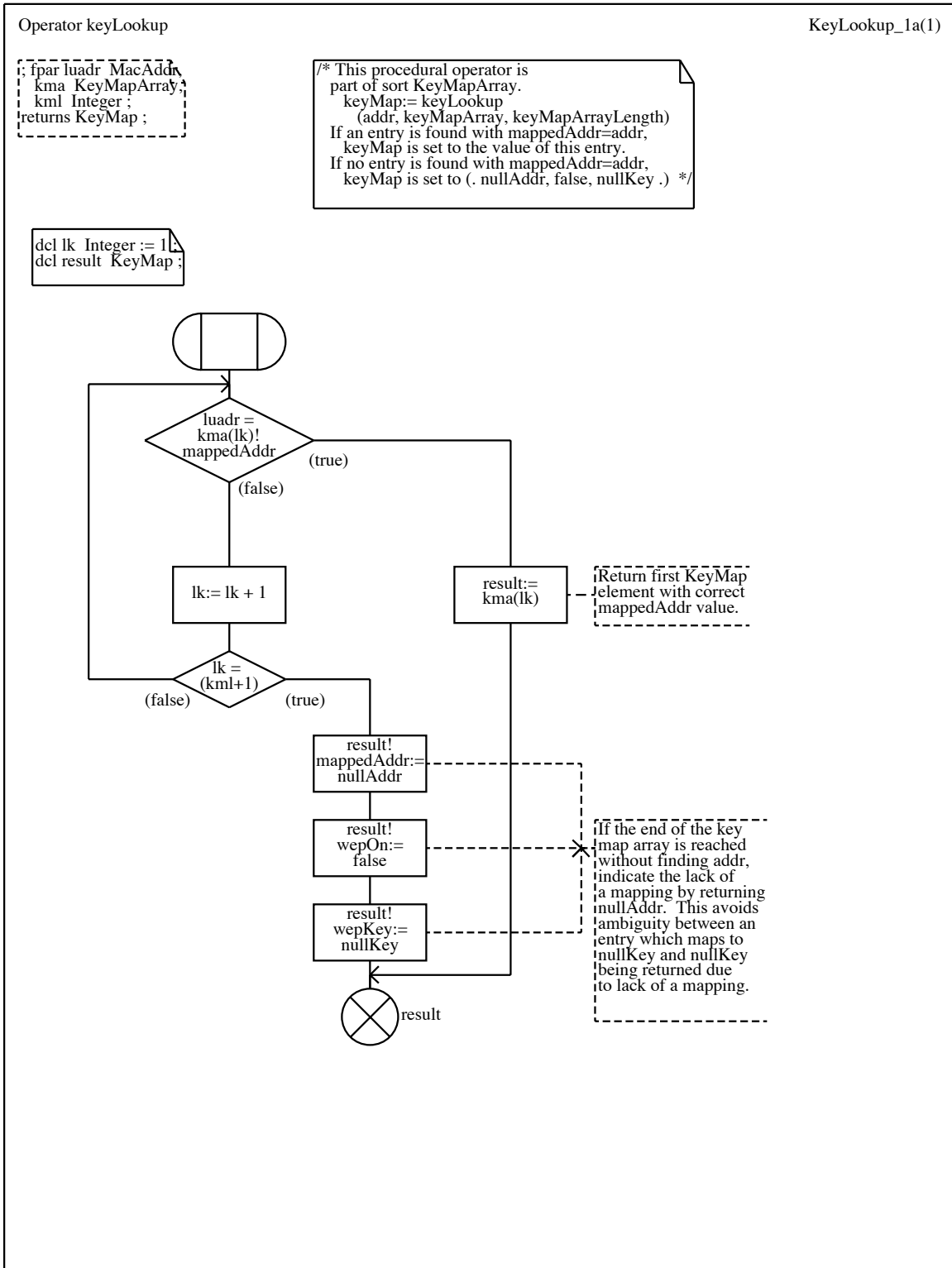
syntype KeyIndex = Integer constants 0:3 endsyntype KeyIndex;
newtype PrngKey inherits Octetstring operators all;
adding literals nullKey; /* nullKey is not any of 2^40 key values */
axioms nullKey == null; default nullKey; endnewtype PrngKey;
newtype KeyVector /* vector of default WEP keys */
  Array( KeyIndex, PrngKey); endnewtype KeyVector;

/* Number of entries in aWepKeyMappings array at this station.
/* implementation dependent value, minimum=10 (see 8.3.2). */
synonym sWepKeyMappingLength Integer = 10;
syntype KeyMappingRange = Integer
  constants 1:sWepKeyMappingLength endsyntype KeyMappingRange;
newtype KeyMap struct /* structure used for entries in KeyMapArray */
  mappedAddr MacAddr;
  wepOn Boolean;
  wepKey PrngKey;
endnewtype KeyMap;

/* KeyMapArray -- used for aWepKeyMapping table;
/* an array of KeyMap indexed by KeyMappingRange, with operator
/*   KeyMap := keyLookup(addr, keyMapArray, keyMapArrayLength)
/* returns the KeyMap entry for the specified addr, or
/* (. nullAddr, false, nullKey .) if no mapping for addr. */
newtype KeyMapArray Array( KeyMappingRange, KeyMap);
adding operators
  keyLookup : MacAddr, KeyMapArray, Integer -> KeyMap;
operator keyLookup;
fpar luadr MacAddr, kma KeyMapArray, kml Integer;
returns KeyMap; referenced;
endnewtype KeyMapArray;

```





```

/*****
 *   FRAME sort (the basic definition of fields in MAC frames)
 *****/

/* Frame is a subtype of Octetstring with operators for creating
/* MAC headers, extracting each of the header fields and some
/* management frame fields, and modifying most of these fields.
/* There are operators to create and extract management frame
/* elements, but no operators for the frame body, IV, ICV, and FCS
/* fields, which are handled directly as Octetstrings. */

newtype Frame inherits Octetstring operators all;
adding operators
mkFrame      : TypeSubtype, MacAddr, MacAddr, Octetstring -> Frame;
mkCtl        : TypeSubtype, Octetstring, MacAddr -> Frame;
protocolVer  : Frame -> Integer; /* Protocol version (2 bits) */
basetype     : Frame -> BasicType; /* Type field (2 bits) */
ftype        : Frame -> TypeSubtype; /* Type & Subtype (6 bits) */
setFtype     : Frame, TypeSubtype -> Frame;
toDs         : Frame -> Bit; /* To DS bit (1 bit) */
setToDs      : Frame, Bit -> Frame;
frDs         : Frame -> Bit; /* From DS bit (1 bit) */
setFrDs      : Frame, Bit -> Frame;
moreFrag     : Frame -> Bit; /* More Fragments bit (1 bit) */
setMoreFrag  : Frame, Bit -> Frame;
retryBit     : Frame -> Bit; /* Retry bit (1 bit) */
setRetryBit  : Frame, Bit -> Frame;
pwrMgt       : Frame -> Bit; /* Power Management bit (1 bit) */
setPwrMgt    : Frame, Bit -> Frame;
moreData     : Frame -> Bit; /* More Data bit (1 bit) */
setMoreData  : Frame, Bit -> Frame;
wepBit       : Frame -> Bit; /* WEP bit (1 bit) */
setWepBit    : Frame, Bit -> Frame;
orderBit     : Frame -> Bit; /* {strictly}Order{ed} (1 bit) */
setOrderBit  : Frame, Bit -> Frame;
durId        : Frame -> Integer; /* Duration/ID field (2) */
setDurId     : Frame, Integer -> Frame;
addr1        : Frame -> MacAddr; /* Address 1 [DA/RA] field (6) */
setAddr1     : Frame, MacAddr -> Frame;
addr2        : Frame -> MacAddr; /* Address 2 [SA/TA] field (6) */
setAddr2     : Frame, MacAddr -> Frame;
addr3        : Frame -> MacAddr; /* Address 3 [Bss/DA/SA] field */
setAddr3     : Frame, MacAddr -> Frame;
addr4        : Frame -> MacAddr; /* Address 4 [WDS-SA] field (6) */
insAddr4     : Frame, MacAddr -> Frame;
seq          : Frame -> SeqNum; /* Sequence Number (12 bits) */
setSeq       : Frame, SeqNum -> Frame;
frag         : Frame -> FragNum; /* Fragment Number (4 bits) */
setFrag      : Frame, FragNum -> Frame;
ts           : Frame -> Time; /* Timestamp field (8) */
setTs        : Frame, Time -> Frame;
mkElem       : ElementID, Octetstring -> Frame; /* make element */
GetElem      : Frame, ElementID -> Frame; /* get element if aval */
status       : Frame -> StatusCode; /* Status Code field (2) */
setStatus    : Frame, StatusCode -> Frame;
authStat     : Frame -> StatusCode; /* Status Code in Auth frame */
reason       : Frame -> ReasonCode; /* Reason Code field (2) */
authSeqNum   : Frame -> Integer; /* Auth Sequence Number (2) */
authAlg      : Frame -> AuthType; /* Auth Algorithm field (2) */
beaconInt    : Frame -> TU; /* Beacon Interval field (2) */
listenInt    : Frame -> TU; /* Listen Interval field (2) */
AID          : Frame -> AssocId; /* Association ID field (2) */
setAid       : Frame, AssocId -> Frame;
curApAddr    : Frame -> MacAddr; /* Current AP Addr field (6) */
capA         : Frame, Capability -> Bit; /* Capability (Re)Asoc */
setCapA      : Frame, Capability, Bit -> Frame;

```

```

capB           : Frame, Capability -> Bit; /* Capability Bcn/Probe */
setCapB       : Frame, Capability, Bit -> Frame;
keyId         : Frame -> KeyIndex; /* Key ID subfield (2 bits) */
setKeyId: Frame, KeyIndex -> Frame;
operator GetElem;
fpar  fr Frame,  el ElementID;  returns Frame;  referenced;
axioms
for all f in Frame(
  for all a, sa, da, ra, ta, bssa in MacAddr(
    for all body, dur, sid, info in Octetstring(
      addr1(f) == SubStr(f,4,6);
      setAddr1(f,a) == SubStr(f,0,4) // a // SubStr(f,10,Length(f)-10);
      addr2(f) == SubStr(f,10,6);
      setAddr2(f,a) == SubStr(f,0,10) // a // SubStr(f,16,Length(f)-16);
      addr3(f) == SubStr(f,16,6);
      setAddr3(f,a) == SubStr(f,0,16) // a // SubStr(f,22,Length(f)-22);
      addr4(f) == SubStr(f,24,6);
      insAddr4(f,a) == SubStr(f,0,24) // a // SubStr(f,24,Length(f)-24);
      curApAddr(f) == SubStr(f,28,6);
      for all ft in TypeSubtype(
        mkFrame(ft, da, bssa, body) ==
          ft // 03 // da // aMacAddress // bssa // 02 // body;
        (ft = rts) ==> mkCtl(ft, dur, ra) ==
          ft // 01 // dur // ra // aStationID;
        (ft = ps_poll) ==> mkCtl(ft, sid, bssa) ==
          ft // 01 // sid // bssa // aStationID;
        (ft = cts) or (ft = ack) ==> mkCtl(ft, dur, ra) ==
          ft // 01 // dur // ra;
        (ft = cfend) or (ft = cfend_ack) ==> mkCtl(ft, bssa, ra) ==
          ft // 03 // ra // bssa;
        ftype(f) == MkString(f(0) and 0xFC);
        setFtype(f, ft) == Modify!(f, 0, MkString((f(0) and 0x03) or
          ft)); );
      for all bt in BasicType( basetype(f) == f(0) and 0x0C; );
      for all i in Integer(
        protocolVer(f) == octetVal(f(0) and 0x03);
        authSeqNum(f) == octetVal(f(26)) + (octetVal(f(27)) * 256);
        durId(f) == octetVal(f(2)) + (octetVal(f(3)) * 256);
        setDurId(f, i) == SubStr(f, 0, 2) // mkOS(i mod 256, 1) //
          mkOS(i / 256, 1) // SubStr(f, 4, Length(f) - 4); );
      for all e in ElementID(
        mkElem(e, info) == e // mkOS(Length(info) + 2, 1) // info; );
      for all b in Bit(
        toDs(f) == if (f(1) and 0x01) then 1 else 0 fi;
        setToDs(f, b) ==
          Modify!(f, 1, (f(1) and 0xFE) or S8(0,0,0,0,0,0,0,b));
        frDs(f) == if (f(1) and 0x02) then 1 else 0 fi;
        setFrDs(f, b) ==
          Modify!(f, 1, (f(1) and 0xFD) or S8(0,0,0,0,0,0,b,0));
        moreFrag(f) == if (f(1) and 0x04) then 1 else 0 fi;
        setMoreFrag(f, b) ==
          Modify!(f, 1, (f(1) and 0xFB) or S8(0,0,0,0,0,b,0,0));
        retryBit(f) == if (f(1) and 0x08) then 1 else 0 fi;
        setRetryBit(f, b) ==
          Modify!(f, 1, (f(1) and 0xF7) or S8(0,0,0,0,b,0,0,0));
        pwrMgt(f) == if (f(1) and 0x10) then 1 else 0 fi;
        setPwrMgt(f, b) ==
          Modify!(f, 1, (f(1) and 0xFB) or S8(0,0,0,b,0,0,0,0));
        moreData(f) == if (f(1) and 0x20) then 1 else 0 fi;
        setMoreData(f, b) ==
          Modify!(f, 1, (f(1) and 0xFB) or S8(0,0,b,0,0,0,0,0));
        wepBit(f) == if (f(1) and 0x40) then 1 else 0 fi;
        setWepBit(f, b) ==
          Modify!(f, 1, (f(1) and 0xFB) or S8(0,b,0,0,0,0,0,0));
        orderBit(f) == if (f(1) and 0x80) then 1 else 0 fi;

```

```

setOrderBit(f, b) ==
    Modify!(f, 1, (f(1) and 0xFB) or S8(b,0,0,0,0,0,0,0));
for all c in Capability(
    capA(f,c) == if (B_S(SubStr(f,24,2)) and c) then 1 else 0 fi;
    setCapA(f,c,b) == SubStr(f,0,24) // (B_S(SubStr(f,24,2) and
        (not c)) or (if b then c else 02 fi)) //
        SubStr(f,26,Length(f) - 26);
    capB(f,c) == if (B_S(SubStr(f,34,2)) and c) then 1 else 0 fi;
    setCapB(f,c,b) == SubStr(f,0,34) // (B_S(SubStr(f,34,2) and
        (not c)) or (if b then c else 02 fi)) //
        SubStr(f,36,Length(f) - 36); );
for all sq in SeqNum(
    seq(f) == (octetVal(f(22) and 0xF0)/16)+(octetVal(f(23)*16));
    setSeq(f, sq) == SubStr(f, 0, 22) // MkString((f(22) and 0x0F)
        or mkOctet((sq mod 16) * 16)) // mkOS(sq / 16, 1) //
        SubStr(f, 24, Length(f) - 24); );
for all fr in FragNum(
    frag(f) == octetVal(f(22) and 0x0F);
    setFrag(f, fr) ==
        SubStr(f, 0, 22) // MkString((f(22) and 0xF0) or
            mkOctet(fr)) // SubStr(f, 23, Length(f) - 23); );
for all tm in Time(
    ts(f) == tUsec( Usec!(octetVal(f(24)) +
        (256 * (octetVal(f(25)) +
            (256 * (octetVal(f(26)) +
                (256 * (octetVal(f(27)) +
                    (256 * (octetVal(f(28)) +
                        (256 * (octetVal(f(29)) +
                            (256 * (octetVal(f(30)) +
                                (256 * octetVal(f(31)))))))))))))) ) );
    setTs(f, tm) == SubStr(f, 0, 24) // mkOS(fix(tm), 1) //
        mkOS((fix(tm) / 256), 1) // mkOS((fix(tm) / 65536), 1) //
        mkOS((fix(tm) / 16777216), 1) //
        mkOS((fix(tm) / 4294967296), 1) //
        mkOS(((fix(tm) / 4294967296) / 256), 1) //
        mkOS(((fix(tm) / 4294967296) / 65536), 1) //
        mkOS(((fix(tm) / 4294967296) / 16777216), 1) //
        SubStr(f, 32, Length(f) - 32); );
for all stat in StatusCode(
    status(f) == SubStr(f, 26, 2);
    setStatus(f, stat) ==
        SubStr(f, 0, 26) // stat // SubStr(f, 28, Length(f) - 28);
    authStat(f) == SubStr(f, 28, 2); );
for all rea in ReasonCode(    reason(f) == SubStr(f, 24, 2); );
for all alg in AuthType(    AuthType(f) == SubStr(f, 24, 2); );
for all u in TU(
    beaconInt(f) == octetVal(f(32)) + (octetVal(f(33)) * 256);
    listenInt(f) == octetVal(f(26)) + (octetVal(f(27)) * 256); );
for all sta in AssocId(
    AId(f) == octetVal(f(28)) + (octetVal(f(29)) * 256);
    setAId(f, sta) == SubStr(f, 0, 28) // mkOS(sta mod 256, 1) //
        mkOS(sta / 256, 1) // SubStr(f, 30, Length(f) - 30); );
for all kid in KeyIndexRange(
    keyId(f) == octetVal(f(27)) / 64;
    setKeyId(f, kid) == Modify!(f, 27, mkOS(kid * 64)); ); );
endnewtype Frame;

```

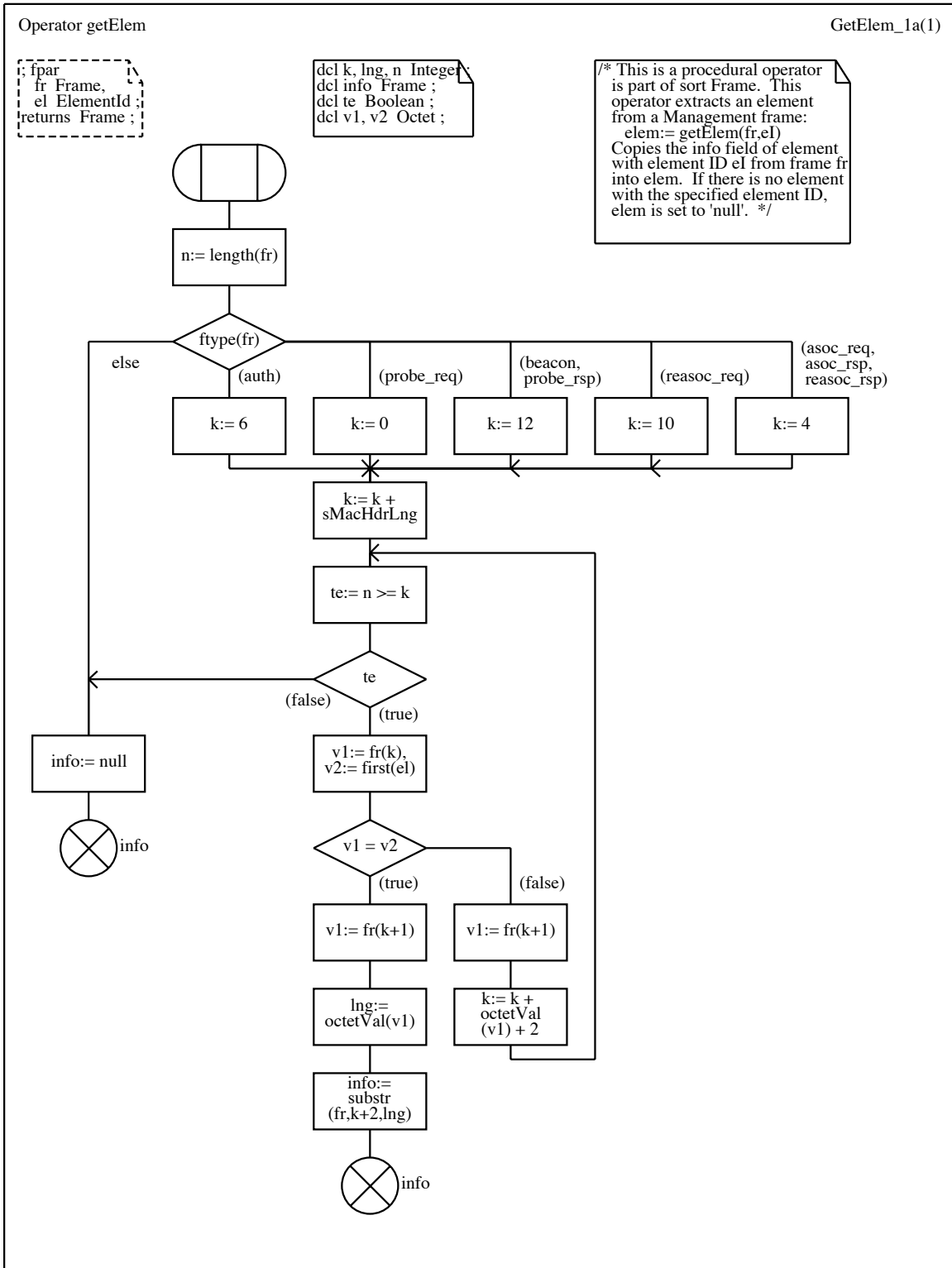


```

/*****
*   ReasonCode sort
*****/
newtype ReasonCode inherits Octetstring operators all;
adding literals unspec_reason, auth_not_valid, deauth_lv_ss,
    inactivity, ap_overload, class2_err, class3_err,
    disas_lv_ss, asoc_not_auth;
axioms
unspec_reason == mkOS(1, 2);      auth_not_valid == mkOS(2, 2);
deauth_lv_ss == mkOS(3, 2);      inactivity == mkOS(4, 2);
ap_overload == mkOS(5, 2);      class2_err == mkOS(6, 2);
class3_err == mkOS(7, 2);      disas_lv_ss == mkOS(8, 2);
asoc_not_auth == mkOS(9, 2);
endnewtype ReasonCode;

/*****
*   StatusCode sort
*****/
newtype StatusCode inherits Octetstring operators all;
adding literals successful, unspec_fail, unsup_cap,
    reasoc_no_asoc, fail_other, unsupt_alg, auth_seq_fail,
    chlng_fail, auth_timeout, ap_full, unsup_rate;
axioms
successful == mkOS(0, 2);      unspec_failure == mkOS(1, 2);
unsup_cap == mkOS(10, 2);      reasoc_no_asoc == mkOS(11, 2);
fail_other == mkOS(12, 2);      unsupt_alg == mkOS(13, 2);
auth_seq_fail == mkOS(14, 2);    chlng_fail == mkOS(15, 2);
auth_timeout == mkOS(16, 2);    ap_full == mkOS(17, 2);
unsup_rate == mkOS(18, 2);
endnewtype StatusCode;

```



```

/*****
*   Frame Type sorts
*****/
/* TypeSubtype defines the full, 6-bit frame type identifiers.
/* These values are useful with ftype operator of Frame sort. */
newtype TypeSubtype inherits Octetstring operators all;
adding literals      asoc_req,      asoc_rsp,      reasoc_req,   reasoc_rsp,
                    probe_req,    probe_rsp,    beacon,      atim,        disasoc,
                    auth,        deauth,      ps_poll,    rts,         cts,
                    ack,         cfend,      cfend_ack,  data,        data_ack,
                    data_poll,   data_poll_ack, null_frame,  cfack,
                    cfpoll,     cfpoll_ack;

axioms
asoc_req           == MkString(S8(0,0,0,0,0,0,0,0));
asoc_rsp           == MkString(S8(0,0,0,0,0,1,0,0));
reasoc_req         == MkString(S8(0,0,0,0,0,0,1,0));
reasoc_rsp         == MkString(S8(0,0,0,0,0,1,1,0));
probe_req          == MkString(S8(0,0,0,0,0,0,0,1));
probe_rsp          == MkString(S8(0,0,0,0,0,1,0,1));
beacon             == MkString(S8(0,0,0,0,0,0,0,1));
atim               == MkString(S8(0,0,0,0,0,1,0,1));
disasoc            == MkString(S8(0,0,0,0,0,0,1,0));
auth               == MkString(S8(0,0,0,0,0,1,1,0));
deauth             == MkString(S8(0,0,0,0,0,0,0,1));
ps_poll           == MkString(S8(0,0,1,0,0,1,0,1));
rts                == MkString(S8(0,0,1,0,1,1,0,1));
cts                == MkString(S8(0,0,1,0,0,0,1,1));
ack                == MkString(S8(0,0,1,0,1,0,1,1));
cfend              == MkString(S8(0,0,1,0,0,1,1,1));
cfend_ack         == MkString(S8(0,0,1,0,1,1,1,1));
data               == MkString(S8(0,0,0,1,0,0,0,0));
data_ack          == MkString(S8(0,0,0,1,1,0,0,0));
data_poll         == MkString(S8(0,0,0,1,0,1,0,0));
data_poll_ack     == MkString(S8(0,0,0,1,1,1,0,0));
null_frame        == MkString(S8(0,0,0,1,0,0,1,0));
cfack              == MkString(S8(0,0,0,1,1,0,1,0));
cfpoll            == MkString(S8(0,0,0,1,0,1,1,0));
cfpoll_ack       == MkString(S8(0,0,0,1,1,1,1,0));
endnewtype TypeSubtype;

/* BasicTypes defines the 2-bit frame type groups */
newtype BasicType inherits Bitstring operators all;
adding literals    control,    data,    management,    reserved;
axioms
control           == S8(0,0,1,0,0,0,0,0);
data              == S8(0,0,0,1,0,0,0,0);
management        == S8(0,0,0,0,0,0,0,0);
reserved          == S8(0,0,1,1,0,0,0,0);
endnewtype BasicType;

```

```

/*****
 *      ElementID sort
 *****/
newtype ElementID inherits Octetstring operators all;
adding literals eSsId, eSupRates, eFhParms, eDsParms,
    eCfParms, eTim, eIbParms, eCtext;
axioms
    eSsId == mkOS(0, 1); /* service set identifier (0:32) */
    eSupRates == mkOS(1, 1); /* supported rates (1:8) */
    eFhParms == mkOS(2, 1); /* FH parameter set (5) */
    eDsParms == mkOS(3, 1); /* DS parameter set (1) */
    eCfParms == mkOS(4, 1); /* CF parameter set (6) */
    eTim == mkOS(5, 1); /* Traffic Information Map (4:254) */
    eIbParms == mkOS(6, 1); /* IBSS parameter set (2) */
    eCtext == mkOS(16, 1); /* challenge text (128, see 8.1.2.2) */
endnewtype ElementID;

/*****
 *      Capability field bit assignments sort
 *****/
newtype Capability inherits Bitstring operators all;
adding literals cEss, cIbss, cPollable, cPollReq, cPrivacy;
axioms
    cEss == S8(1,0,0,0,0,0,0,0) // 0x00; /* ESS capability */
    cIbss == S8(0,1,0,0,0,0,0,0) // 0x00; /* IBSS capability */
    cPollable == S8(0,0,1,0,0,0,0,0) // 0x00; /* CF-pollable (sta),
        PC present (ap) */
    cPollReq == S8(0,0,0,1,0,0,0,0) // 0x00; /* not CF poll req (sta),
        PC polls (ap) */
    cPrivacy == S8(0,0,0,0,1,0,0,0) // 0x00; /* WEP required */
endnewtype Capability;

/*****
 *      IBSS parameter set sort
 *****/
newtype IbssParms inherits Octetstring operators all;
adding operators
    atimWin : IbssParms -> TU;
    setAtimWin : IbssParms, TU -> IbssParms;
axioms
    for all ib in IbssParms( for all u in TU(
        atimWin(ib) == octetVal(ib(0)) + (octetVal(ib(1)) * 256);
        setAtimWin(ib, u) == mkOS(u mod 256, 1) // mkOS(u / 256, 1); ));
endnewtype IbssParms;

/*****
 *      CF parameter set sort
 *****/
newtype CfParms inherits Octetstring operators all;
adding operators
    cfpCount : CfParms -> Integer; /* CfpCount field (1) */
    setCfpCount : CfParms, Integer -> CfParms;
    cfpPeriod : CfParms -> Integer; /* CfpPeriod field (1) */
    setCfpPeriod : CfParms, Integer -> CfParms;
    cfpMaxDur : CfParms -> TU; /* CfpMaxDuration field (2) */
    setCfpMaxDur : CfParms, TU -> CfParms;
    cfpDurRem : CfParms -> TU; /* CfpDurRemaining field (2) */
    setCfpDurRem : CfParms, TU -> CfParms;
axioms
    for all cf in CfParms( for all i in Integer( for all u in TU(
        cfpCount(cf) == octetVal(cf(0));
        setCfpCount(cf, i) == mkOS(i, 1) // Tail(cf);
        cfpPeriod(cf) == octetVal(cf(1));
        setCfpPeriod(cf, i) == cf(0) // mkOS(i, 1) // SubStr(cf,2,4);

```

```

        cfpMaxDur(cf) == octetVal(cf(2)) + (octetVal(cf(3)) * 256);
        setCfpMaxDur(cf, u) == SubStr(cf, 0, 2) // mkOS(u mod 256, 1)
            // mkOS(u / 256, 1) // SubStr(cf, 4, 2);
        cfpDurRem(cf) == octetVal(cf(4)) + (octetVal(cf(5)) * 256);
        setCfpDurRem(cf, u) == SubStr(cf, 0, 4) // mkOS(u mod 256, 1)
            // mkOS(u / 256, 1); ));
endnewtype CfParms;

/*****
 *      Sorts for association management at AP
 *****/
synonym sMaxAId Integer = 2007;      /* 2007 is largest allowable value */
                                      /* implementation limit may be lower */
syntype AsocId = Integer constants 0:sMaxAId endsyntype AsocId;
    /* Station Association Record -- only used at APs */
newtype AsocData struct
    adAddr    MacAddr;      /* address of associated station */
    adPsm     PwrSave;     /* power save mode of the station */
    adCfPoll  Boolean;     /* true if station is CfPollable */
    adPollRq  Boolean;     /* true if station requested polling */
    adNoPoll  Boolean;     /* true if station requested no polling */
    adMsduIP  Boolean;     /* true if partial Msdu outstanding to sta */
    adAuth    AuthType;    /* authentication type used by station */
    adRates   RateSet;     /* supported rates from association request */
    adAge     Time;        /* time of association */
endnewtype AsocData;
    /* Association table -- array of AsocData, only used at APs
    /*      index:= AIdLookup(table, addr)
    /* returns the index of location where table(x)!adAddr=addr
    /* or 0 if no such location found. */
newtype AIdTable  Array(AsocId, AsocData);
    adding operators
    AIdLookup : AIdTable, MacAddr -> AsocId;
    operator AIdLookup;
    fpar tbl AIdTable, val MacAddr; returns AsocId; referenced;
endnewtype AIdTable;

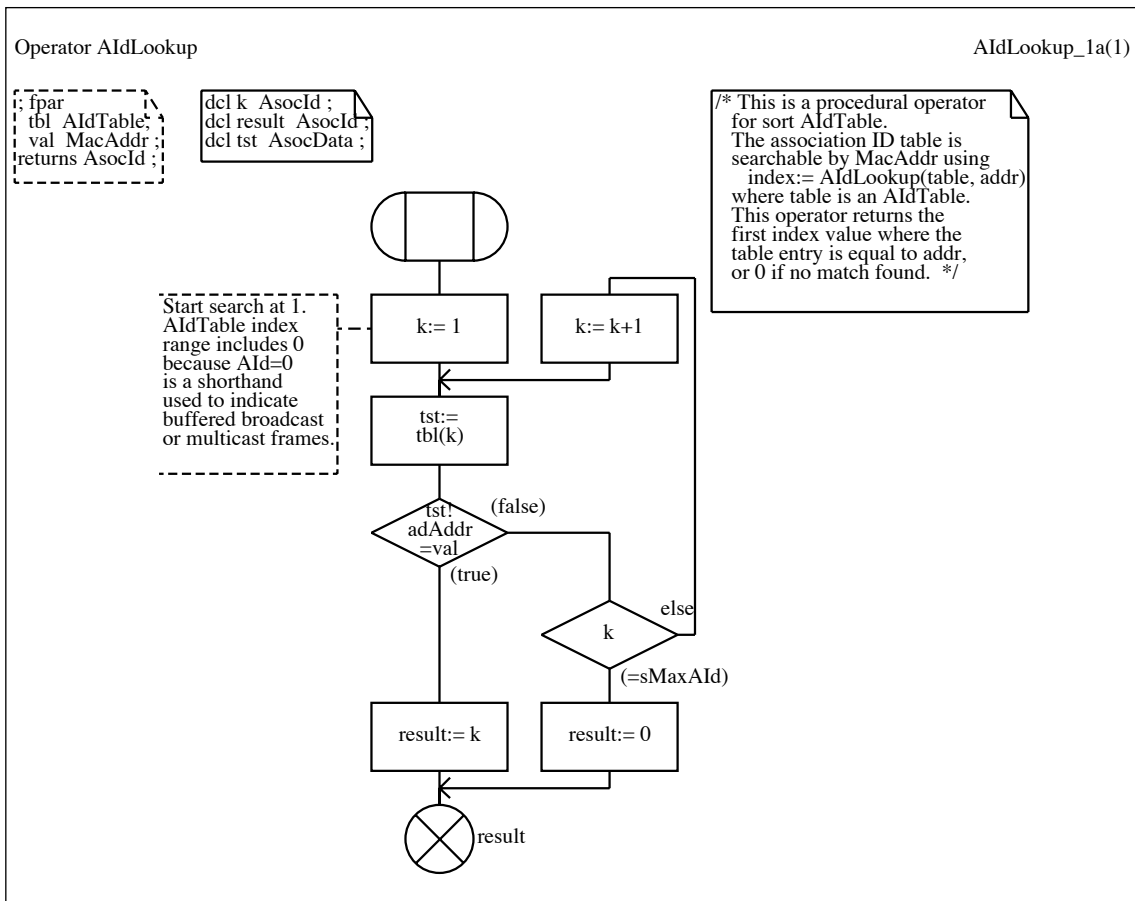
/*****
 *      Traffic Information Map (TIM) support sorts
 *****/
    /* TrafficMap is an Array of Bit indexed by AId.
    /* Bits =1 in TrafficMap denote the presence of buffered frame(s)
    /* for the station assigned that AId. TrafficMap operators are:
    /*      mkTim(trafficMap, dtimCnt, dtimPer, lowAId, highAId, bcst)
    /* returns Octetstring to use as the info field of a TIM element
    /* The TIM will contain bits =1 for TrafficMap locations in the
    /* range (lowAId):(highAId). Buffered broadcasts and multicasts
    /* (AId 0) are indicated if dtimCnt=0 and if bcst=true.
    /*      nextAId(trafficMap, currentAId)
    /* returns index greater than currentAId at which TrafficMap=1.
    /* If no locations before sMaxAId are =1, returns 0. */
newtype TrafficMap Array( AsocId, Bit);
    adding operators
    mkTim : TrafficMap, Integer, Integer, AsocId, AsocId, Boolean ->
        Octetstring;
    nextAId : TrafficMap, AsocId -> AsocId;
    operator mkTim;
    fpar trf TrafficMap, dtc Integer, dtp Integer, xlo AsocId,
        xhi AsocId, bc Boolean; returns Octetstring; referenced;
    operator nextAId;
    fpar trf TrafficMap, x AsocId; returns AsocId; referenced;
endnewtype TrafficMap;
    /* TIM is a subtype of Octetstring with operators:
    /*      bufFrame(tim,AId) returns true if the TIM info field

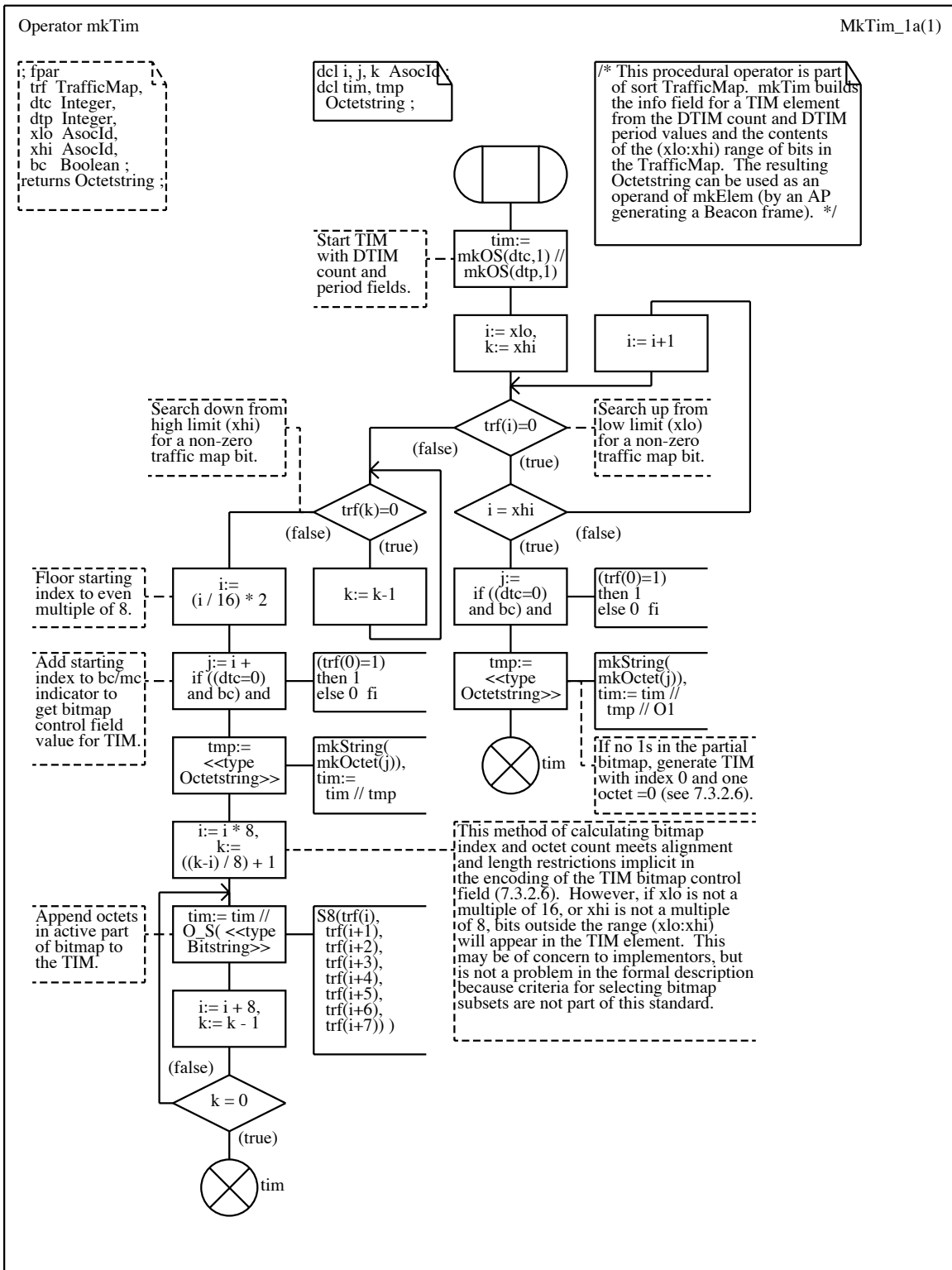
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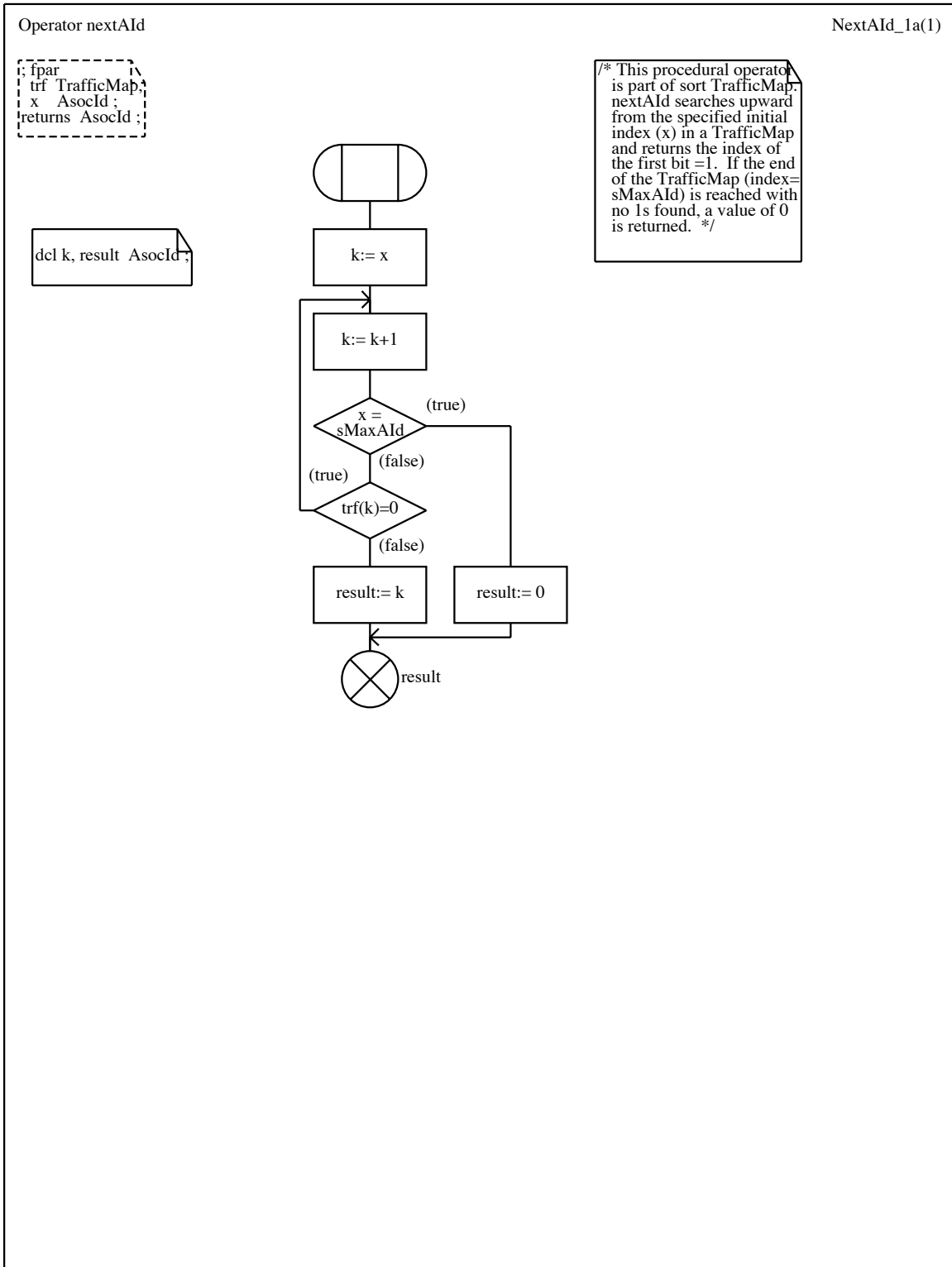
```

/*          (obtained using getElem) is =1 at tim(AId).
/*      bufBcst(tim)  returns true if the TIM info field
/*          indicates buffered broadcast/multicast traffic
/*      dtCount(tim)  returns DTIM count value from TIM
/*      dtPeriod(tim) returns DTIM period value from TIM */
newtype TIM inherits Octetstring operators all;
adding operators
  bufFrame : TIM, AsocId -> Boolean;
  bufBcst  : TIM -> Boolean;
  dtCount  : TIM -> Integer;
  dtPeriod : TIM -> Integer;
axioms
  for all el in TIM(    for all a in AsocId(
    bufFrame(el, a) ==
      if a < (octetVal(el(2) and 0xFE) * 8) then false
      else
        if a >= ((octetVal(el(2) and 0xFE)*8) + ((Length(el)-3)*8))
          then false
          else
            Extract!(B_S(el), (a-(octetVal(el(2) and 0xFE)*8)+24)) = 1
        fi fi;
    bufBcst(el) == (el(2) and 0x01) = 0x01;
    dtCount(el) == octetVal(el(0));
    dtPeriod(el) == octetVal(el(1)); );
endnewtype TIM;

```








```

/*****
 * Multi-rate support sorts
 *****/
newtype Rate inherits Octet operators all;
adding operators
  calcDur : Rate, Integer -> Integer;
          /* converts (rate,bitCount) to integer microseconds */
  rateVal : Rate -> Rate; /* clears high-order bit */
  basicRate : Rate -> Rate; /* sets high-order bit */
  isBasic : Rate -> Boolean; /* true if high-order bit set */
axioms
  for all r in Rate( for all i in Integer( for all b in Boolean(
    calcDur(r, i) == (((10000000 + (octetVal(r and 0x7F) - 1)) /
      (500 * octetVal(r and 0x7F))) * i) + 9999) / 10000;
    rateVal(r) == r and 0x7F;
    basicRate(r) == r or 0x80;
    isBasic(r) == (r and 0x80) = 0x80; ));
endnewtype Rate;

syntype RateString = Octetstring endsyntype RateString;

/*****
 * MPDU duration factor support sort
 *****/
/* These operators support the encoding used to allow
/* an Integer to represent the value of aMpduDurationFactor.
/* calcDF(PlcpBits, MpduBits) returns an Integer which is
/* the fractional part of ((PlcpBits/MpduBits)-1)*(1e9).
/* stuff(durFactor, MpduBits) returns the number of PlcpBits
/* which result from MpduBits at the specified durFactor. */
newtype DurFactor inherits Integer operators all;
adding operators
  calcDF : Integer, Integer -> DurFactor;
  stuff : DurFactor, Integer -> Integer;
axioms
  for all df in DurFactor( for all mb, pb in Integer(
    calcDF(pb, mb) == ((pb * 1000000000) / mb) - 1000000000;
    stuff(df, mb) == ((mb * df) + (mb - 1)) / 1000000000; ));
endnewtype DurFactor;

/*****
 * Generic PHY parameter set sort
 *****/
/* Generic PHY parameter element for signals related to Beacons
/* and Probe Responses that are PHY-type independent. */
syntype PhyParms = Octetstring endsyntype PhyParms;

/*****
 * FH parameter set sort
 *****/
newtype FhParms inherits Octetstring operators all;
adding operators
  dwellTime : FhParms -> TU; /* Dwell Time field (2) */
  setDwellTime : FhParms, TU -> FhParms;
  hopSet : FhParms -> Integer; /* Hop Set field (1) */
  setHopSet : FhParms, Integer -> FhParms;
  hopPattern : FhParms -> Integer; /* Hop Pattern field (1) */
  setHopPattern : FhParms, Integer -> FhParms;
  hopIndex : FhParms -> Integer; /* Hop Index field (1) */
  setHopIndex : FhParms, Integer -> FhParms;
axioms
  for all fh in FhParms( for all i in Integer( for all u in TU(
    dwellTime(fh) == octetVal(fh(0)) + (octetVal(fh(1)) * 256);
    setDwellTime(fh, u) ==

```

```

        mkOS(u mod 256, 1) // mkOS(u / 256, 1) // SubStr(fh, 2, 3);
        hopSet(fh) == octetVal(fh(2));
        setHopSet(fh,i) == SubStr(fh,0,2) // mkOS(i,1) // SubStr(fh,3,2);
        hopPattern(fh) == octetVal(fh(3));
        setHopPattern(fh, i) == SubStr(fh,0,3) // mkOS(i,1) // Last(fh);
        hopIndex(fh) == octetVal(fh(4));
        setHopIndex(fh, i) == SubStr(fh, 0, 4) // mkOS(i, 1);));
endnewtype FhParms;

/*****
 *      DS parameter set sort
 *****/
newtype DsParms inherits Octetstring operators all;
adding operators
    curChannel: DsParms -> Integer; /* Current Channel (1) */
    setCurChannel: DsParms, Integer -> DsParms;
axioms
    for all ds in DsParms(      for all i in Integer(
        curChannel(ds) == octetVal(ds(0));
        setCurChannel(ds, i) == mkOS(i); ));
endnewtype DsParms;

endpackage;
/*****/

/*****/
use macsorts;
Package macmib;

    /* This Package contains definitions of the MAC MIB attributes
    /* and the subset of the PHY MIB attributes used by the MAC state
    /* machines. These are needed under Z.100 to permit analysis of
    /* the state machine definitions. In future revisions these may
    /* be replaced with the ASN.1 MIB definition, from Annex D, if
    /* a Z.105-compliant SDL tool is available. */

/*****/
 *      StationConfig Group
 *****/
remote aMediumOccupancyLimit TU nodelay;
remote aReceiveDTIMs Boolean nodelay;
synonym aCfPollable Boolean = <<package macsorts>> sCfPollable;
remote aCfPeriod Integer nodelay;
remote aCfMaxDuration TU nodelay;
remote aAuthenticationResponseTimeout TU nodelay;
remote aAuthenticationType AuthTypeSet nodelay;
remote aWepUndecryptableCount Counter32 nodelay;

/*****/
 *      AuthenticationAlgorithms Table
 *****/
synonym aAuthenticationAlgorithms AuthTypeSet =
    incl(open_system, incl(shared_key));
    /* NOTE: Only include shared_key in this set
    if aPrivacyOptionImplemented=true. */

/*****/
 *      WepDefaultKeys Table (only if aPrivacyOptionImplemented=true)
 *****/
remote aWepDefaultKeys KeyVector nodelay;

/*****/
 *      WepKeyMappings Table (only if aPrivacyOptionImplemented=true)
 *****/

```

```

*****/
remote aWepKeyMappings KeyMapArray nodelay;
synonym aWepKeyMappingLength Integer =
    <<package macsorts>> sWepKeyMappingLength;

/*****
* Privacy Group (only 1 item if aPrivacyOptionImplemented=false)
*****/
synonym aPrivacyOptionImplemented Boolean = true;
remote aPrivacyInvoked Boolean nodelay;
remote aWepDefaultKeyId KeyIndex nodelay;
remote aExcludeUnencrypted Boolean nodelay;
remote aWepIcvErrorCount Counter32 nodelay;
remote aWepExcludedCount Counter32 nodelay;

/*****
* Operation Group
*****/
synonym aMacAddress MacAddr =
    <<type MacAddr>> S6(0x00, 0x11, 0x22, 0x33, 0x44, 0x55);
    /* each station has a unique globally administered address */
    /* Value may be overwritten with locally administered address at */
    /* MlmeReset, but is always a static value during MAC operation */
remote aRtsThreshold Integer nodelay;
remote aShortRetryLimit Integer nodelay;
remote aLongRetryLimit Integer nodelay;
remote aFragmentationThreshold Integer nodelay;
remote aMaxTransmitMsduLifetime TU nodelay;
remote aMaxReceiveLifetime TU nodelay;
synonym aManufacturerId Charstring = 'name of manufacturer';
synonym aProductId Charstring = 'identifier unique to manufacturer';

/*****
* GroupAddresses Table
*****/
remote aGroupAddresses MacAddrSet nodelay;

/*****
* Counters Group
*****/
remote aTransmittedFragmentCount Counter32 nodelay;
remote aMulticastTransmittedFrameCount Counter32 nodelay;
remote aFailedCount Counter32 nodelay;
remote aRetryCountCounter32 nodelay;
remote aMultipleRetryCount Counter32 nodelay;
remote aRtsSuccessCount Counter32 nodelay;
remote aRtsFailureCount Counter32 nodelay;
remote aAckFailureCount Counter32 nodelay;
remote aReceivedFragmentCount Counter32 nodelay;
remote aMulticastReceivedFrameCount Counter32 nodelay;
remote aFcsErrorCount Counter32 nodelay;
remote aFrameDuplicateCount Counter32 nodelay;

/*****
* PhyOperation Group (values shown are mostly for FH PHY)
*****/
synonym aPHYType Integer = 01;
synonym RegDomainSupported Octetstring = S4(0x10, 0x20, 0x30, 0x00);
remote aCurrentRegDomain Integer nodelay;
synonym aSlotTime Usec = (aCcaTime + aRxTxTurnaroundTime +
    aAirPropagationTime + aMacProcessingTime);
synonym aCcaTime Usec = 27;
synonym aRxTxTurnaroundTime Usec = (aTxPlcpDelay + aRxTxSwitchTime +
    aTxRampOnTime + aTxRfDelay);

```

```
synonym aTxPlcpDelay Usec = 1;
synonym aRxTxSwitchTime Usec = 10;
synonym aTxRampOnTime Usec = 8;
synonym aTxRfDelay Usec = 1;
synonym aSifsTime Usec = (aRxRfDelay + aRxPlcpDelay +
    aMacProcessingTime + aRxTxTurnaroundTime);
synonym aRxRfDelay Usec = 4;
synonym aRxPlcpDelay Usec = 2;
synonym aMacProcessingTime Usec = 2;
synonym aTxRampOffTime Usec = 8;
synonym aPreambleLength Usec = 96;
synonym aPlcpHeaderLength Usec = 32;
synonym aMpduDurationFactor <<package macsorts>> DurFactor = 31250000;
synonym aAirPropagationTime Usec = 1;
synonym aTempType Integer = 01;
synonym aCWmax Integer = 1023;
synonym aCWmin Integer = 15;

/*****
 *   PhyRate Group      (values shown are mostly for FH PHY)
 *****/
synonym aSupportedRatesTx Octetstring = S3(0x82, 0x04, 0x00);
synonym aSupportedRatesRx Octetstring = S3(0x82, 0x04, 0x00);
synonym aMpduMaxLength Integer = 4095;

/*****
 *   PhyFHSS Group     (only used with FH PHY)
 *****/
synonym aHopTime Usec = 224;
remote aCurrentChannelNumber Integer nodelay;
synonym aMaxDwellTime TU = 390;
remote aCurrentSet Integer nodelay;
remote aCurrentPattern Integer nodelay;
remote aCurrentIndex Integer nodelay;

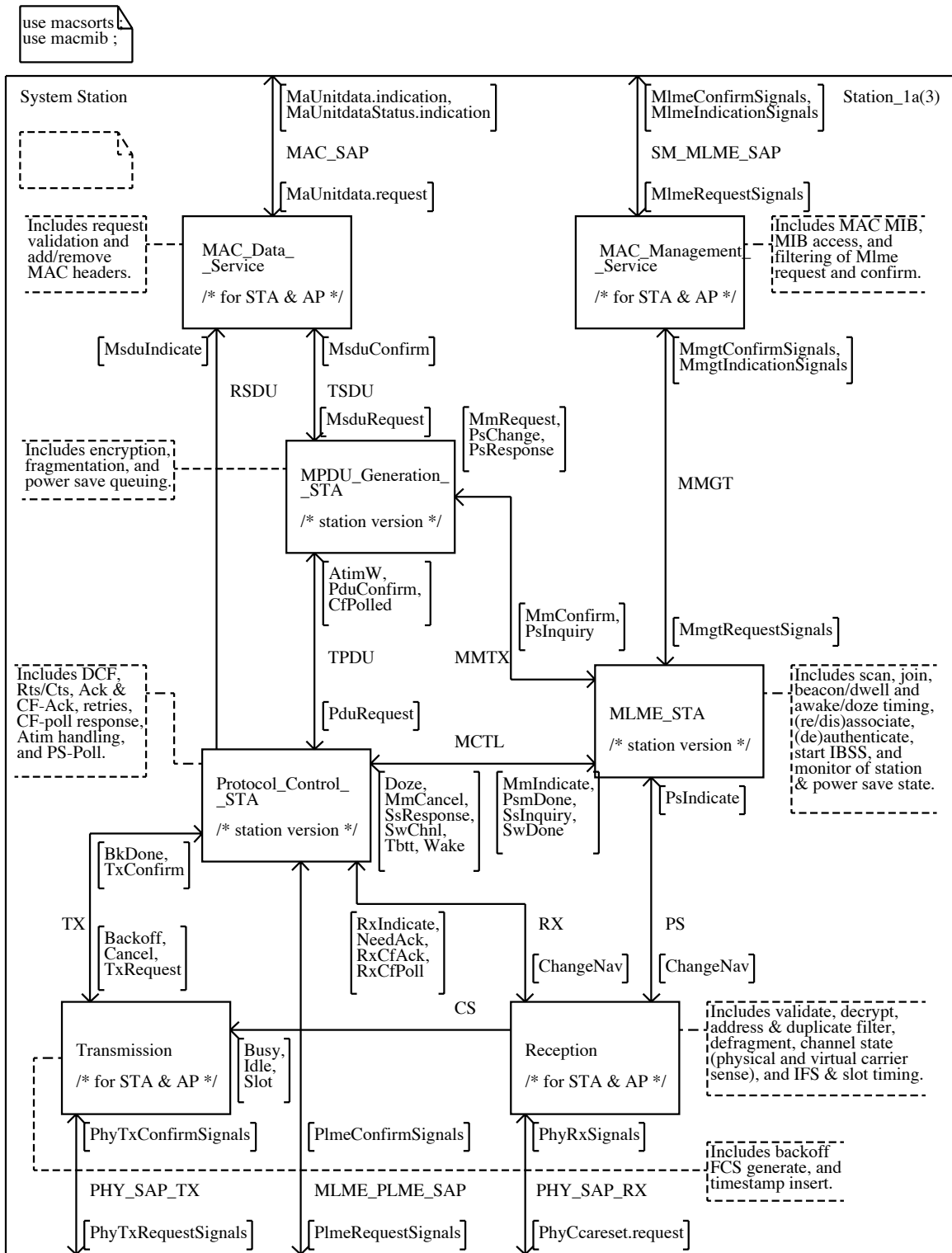
/*****/
/*   The MAC state machines do not reference any attributes in
/*   PhyAntennaGroup,  PhyTxPowerGroup,  PhyDsssGroup,
/*   PhyStatusGroup,  PhyPowerSavingGroup,
/*   PhyIR Group,      AntennasList */

endpackage;
/*****/
```

C.3 State machines for MAC stations

The following SDL-92 system specification defines operation of the MAC protocol at an IEEE 802.11 STA. Many aspects of STA operation also apply to AP operation. These are defined in blocks and processes referenced from both the STA and AP system specifications. Blocks and processes used in both STA and AP are identifiable by the SDL comment `/* for STA & AP */` below the block or process name. Blocks and processes specific to STA operation are identifiable by the SDL comment `/* station version */` below the block or process name. The definitions of all blocks and processes referenced in the station system specification appear in Clause C.3.

The remainder of Clause C.3 is the formal description, in SDL/GR, of an IEEE 802.11 STA.



```
use macsorts;
use macmib;
```

System Station

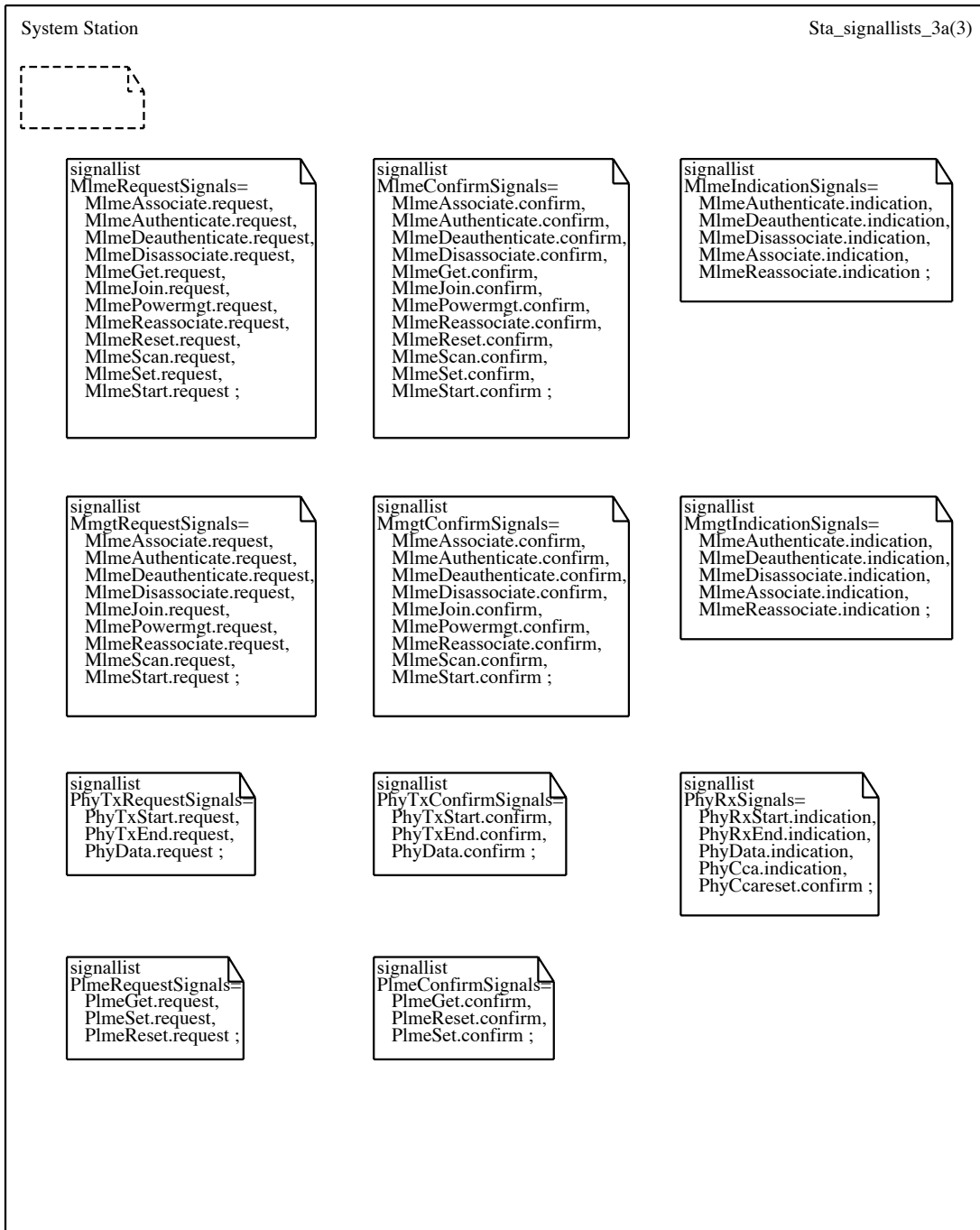
Sta_signals_2b(3)

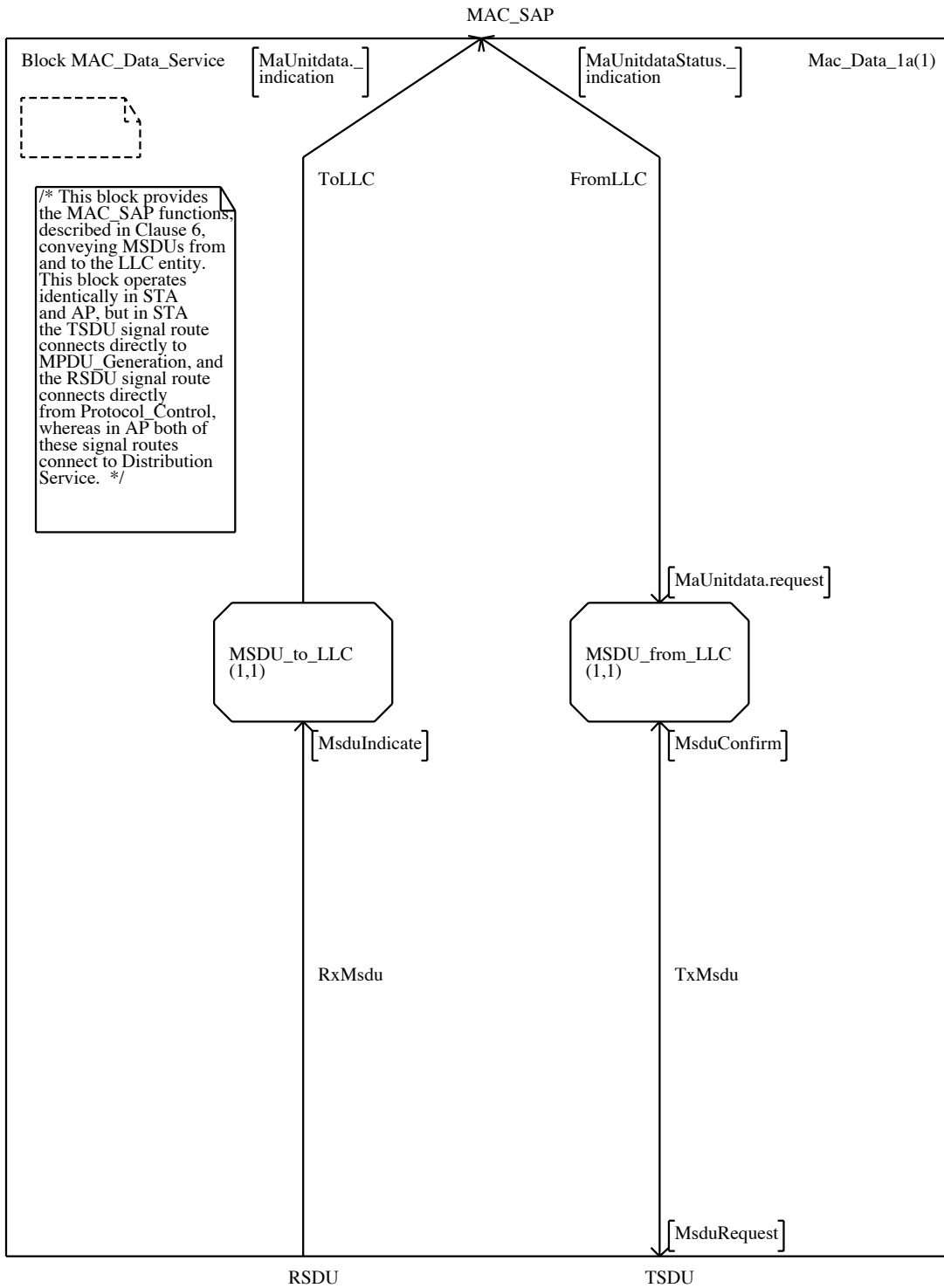


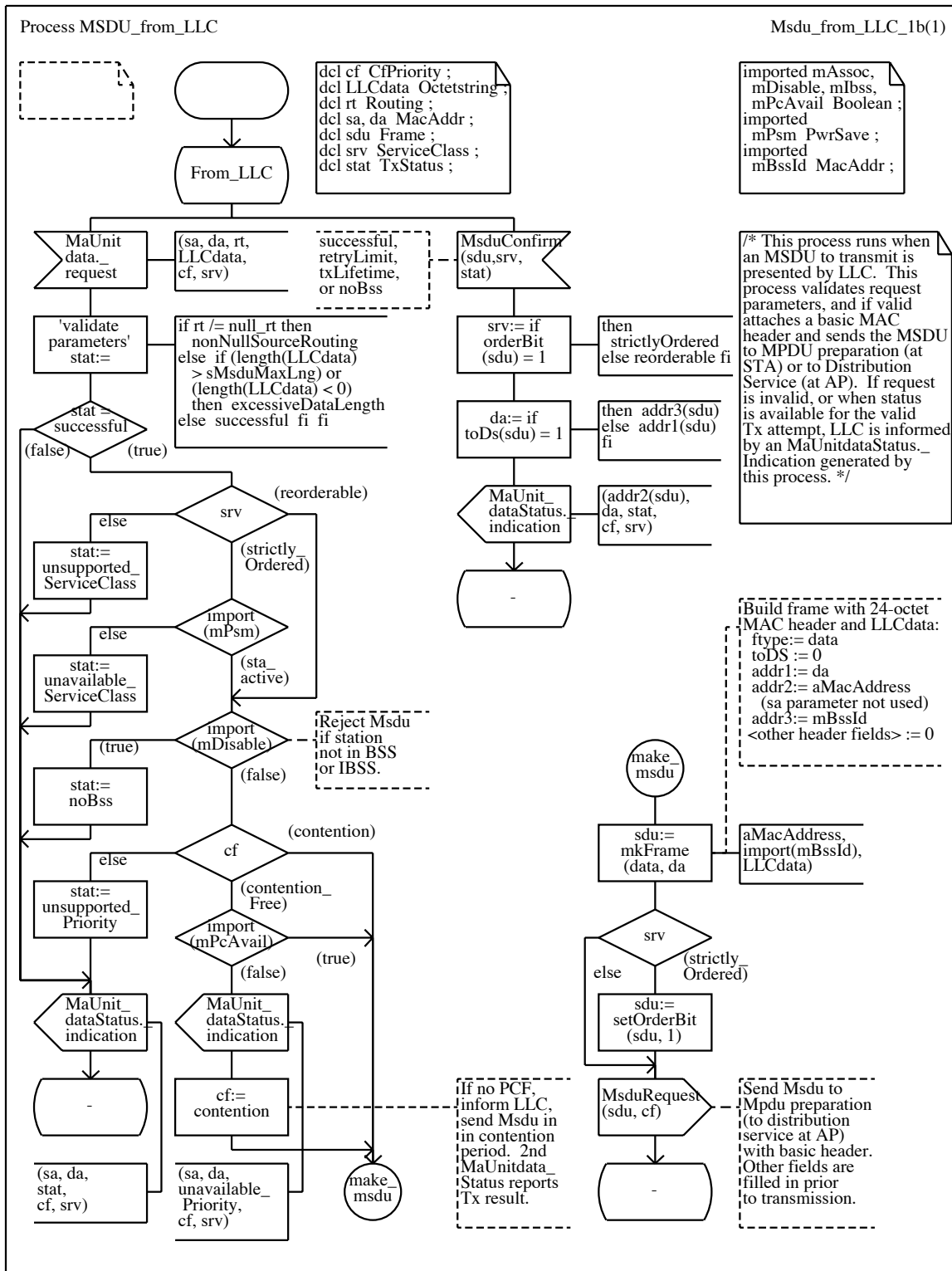
```
signal
  AtimW,
  Backoff(Integer,Integer),
  BkDone(Integer),
  Busy,
  Cancel,
  CfPolled,
  ChangeNav(Time,Duration,NavSrc),
  Doze,
  Idle,
  MaUnitdata.indication(MacAddr,MacAddr,
    Routing,Octetstring,RxStatus,
    CfPriority,ServiceClass),
  MaUnitdata.request(MacAddr,MacAddr,
    Routing,Octetstring,CfPriority,ServiceClass),
  MaUnitdataStatus.indication(MacAddr,
    MacAddr,TxStatus,CfPriority,ServiceClass),
  MlmeAssociate.confirm(MlmeStatus),
  MlmeAssociate.indication(MacAddr),
  MlmeAssociate.request(MacAddr,TU,Capability,Integer),
  MlmeAuthenticate.confirm
    (MacAddr,AuthType,MlmeStatus),
  MlmeAuthenticate.indication(MacAddr,AuthType),
  MlmeAuthenticate.request(MacAddr,AuthType,TU),
  MlmeDeauthenticate.confirm(MacAddr,MlmeStatus),
  MlmeDeauthenticate.indication(MacAddr,ReasonCode),
  MlmeDeauthenticate.request(MacAddr,ReasonCode),
  MlmeDisassociate.confirm(MlmeStatus),
  MlmeDisassociate.indication(MacAddr,ReasonCode),
  MlmeDisassociate.request(MacAddr,ReasonCode),
  MlmeGet.confirm(MibStatus,MibAtrib,MibValue),
  MlmeGet.request(MibAtrib),
  MlmeJoin.confirm(MlmeStatus),
  MlmeJoin.request(BssDscr,Integer,Usec,Ratestring),
  MlmePowermgt.confirm(MlmeStatus),
  MlmePowermgt.request(PwrSave,Boolean,Boolean),
  MlmeReassociate.confirm(MlmeStatus),
  MlmeReassociate.indication(MacAddr),
  MlmeReassociate.request(MacAddr,TU,Capability,Integer),
  MlmeReset.confirm(MlmeStatus),
  MlmeReset.request(MacAddr,Boolean),
  MlmeScan.confirm(BssDscrSet,MlmeStatus),
  MlmeScan.request(BssTypeSet,MacAddr,Octetstring,
    ScanType,Usec,Intstring,TU,TU),
  MlmeSet.confirm(MibStatus,MibAtrib),
  MlmeSet.request(MibAtrib,MibValue),
  MlmeStart.confirm(MlmeStatus),
  MlmeStart.request(Octetstring,BssType,TU,
    Integer,CfParms,PhyParms,IbssParms,Usec,
    Capability,Ratestring,Ratestring);
```

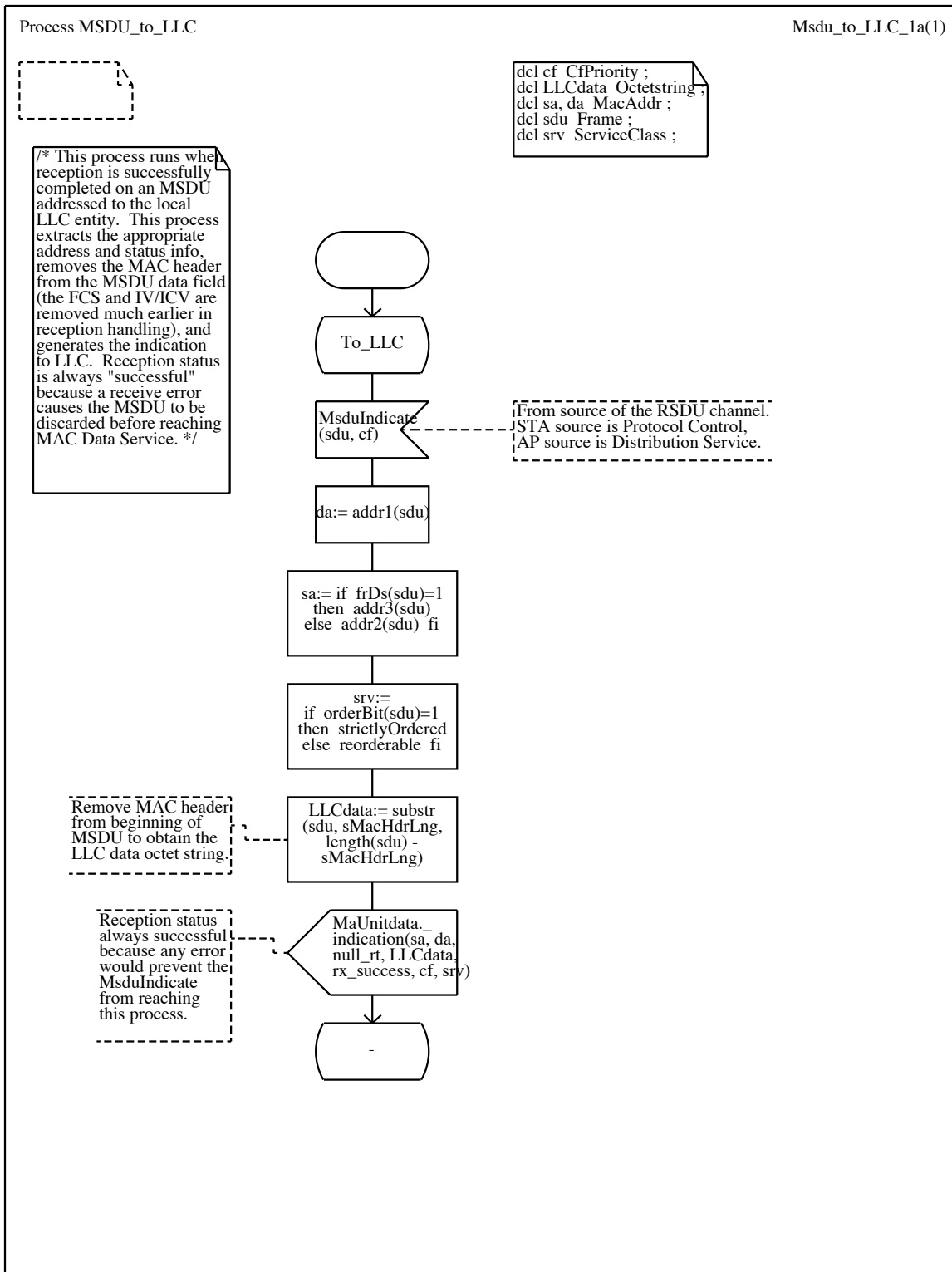
```
signal
  MmCancel,
  MmConfirm(Frame,TxStatus),
  MmIndicate(Frame,Time,Time,StateErr),
  MmRequest(Frame,Imed,Rate),
  MsduConfirm(Frame,CfPriority,TxStatus),
  MsduIndicate(Frame,CfPriority),
  MsduRequest(Frame,CfPriority),
  NeedAck(MacAddr,Time,Duration,Rate),
  PduConfirm(FragSdu,TxResult),
  PduRequest(FragSdu),
  PhyCca.indication(Ccastatus),
  PhyCcarst.confirm,
  PhyCcarst.request,
  PhyData.confirm,
  PhyData.indication(Octet),
  PhyData.request(Octet),
  PhyRxEnd.indication(PhyRxStat),
  PhyRxStart.indication(Integer,Rate),
  PhyTxEnd.confirm,
  PhyTxEnd.request,
  PhyTxStart.confirm,
  PhyTxStart.request(Integer,Rate),
  PlmeGet.confirm(MibStatus,
    MibAtrib,MibValue),
  PlmeGet.request(MibAtrib),
  PlmeReset.confirm(Boolean),
  PlmeReset.request,
  PlmeSet.confirm(MibStatus,MibAtrib),
  PlmeSet.request(MibAtrib,MibValue),
  PsmDone,
  PsChange(MacAddr,PsMode),
  PsIndicate(MacAddr,PsMode),
  PsInquiry(MacAddr),
  PsResponse(MacAddr,PsMode),
  ResetMAC,
  RxCfAck(MacAddr),
  RxIndicate(Frame,Time,Time,Rate),
  Slot,
  SsInquiry(MacAddr),
  SsResponse(MacAddr,
    StationState,StationState),
  SwChnl(Integer,Boolean),
  SwDone,
  TBTT,
  TxConfirm,
  TxRequest(Frame,Rate),
  Wake;
```

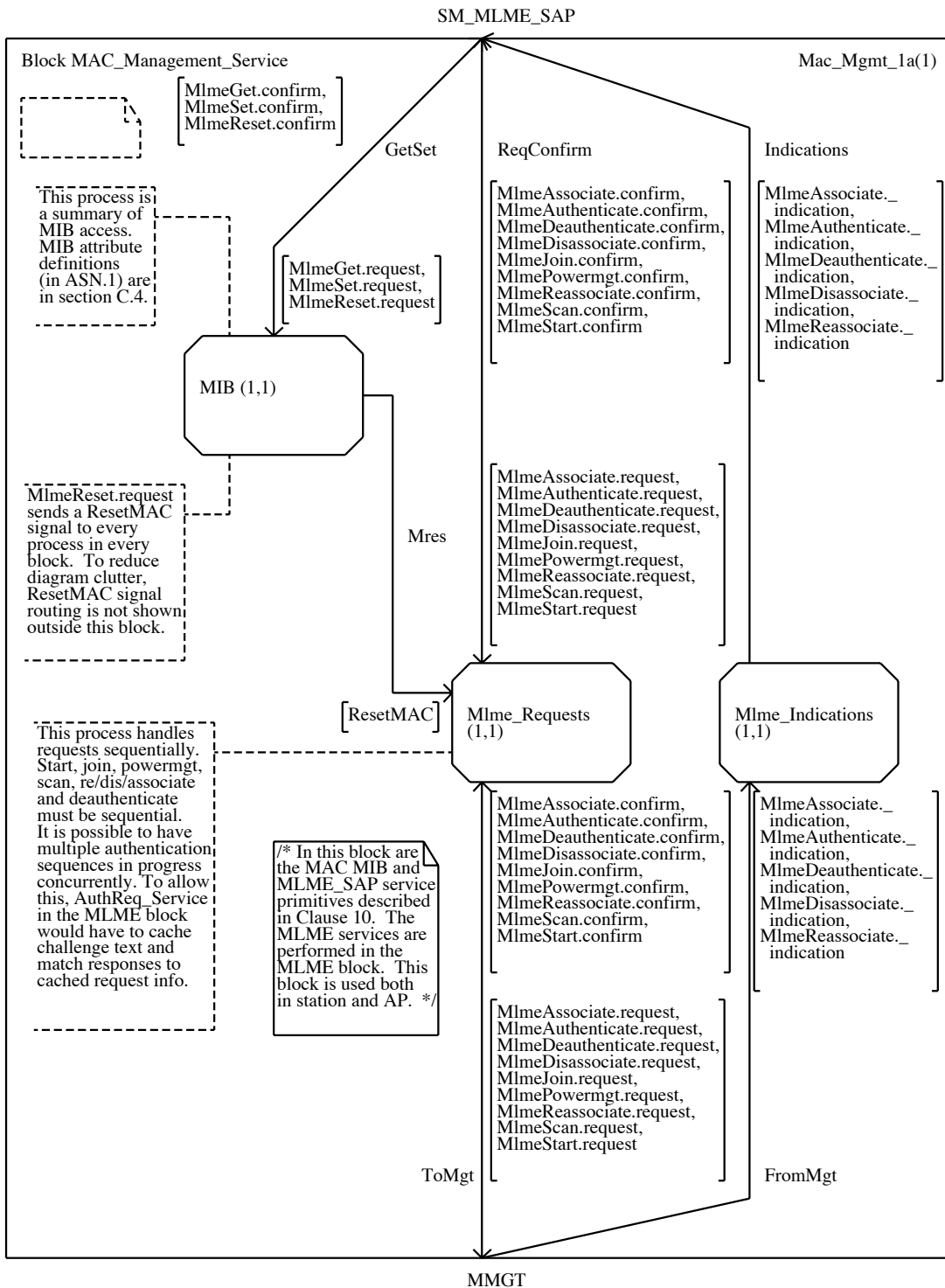
```
use macsorts
use macmib ;
```

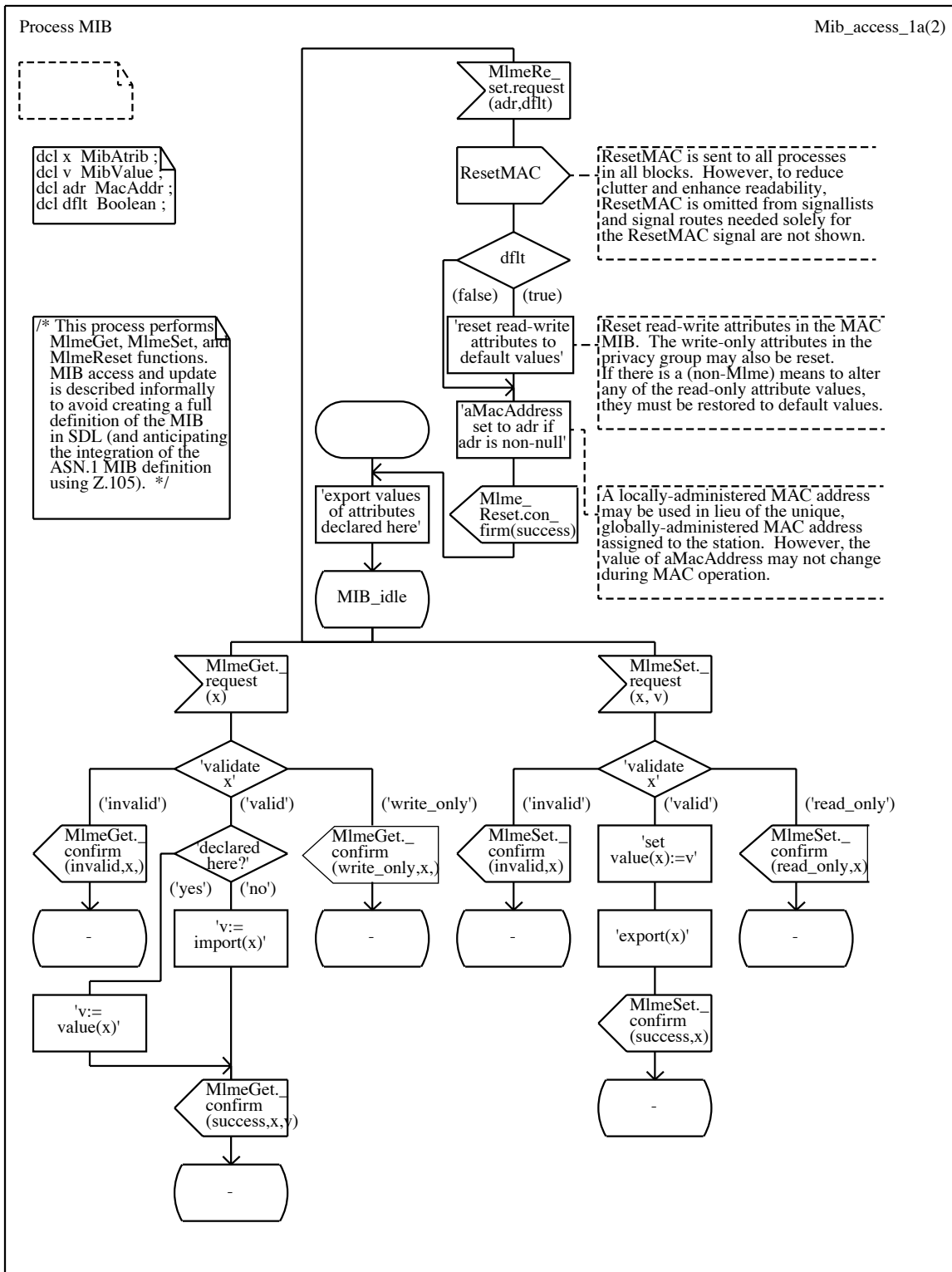


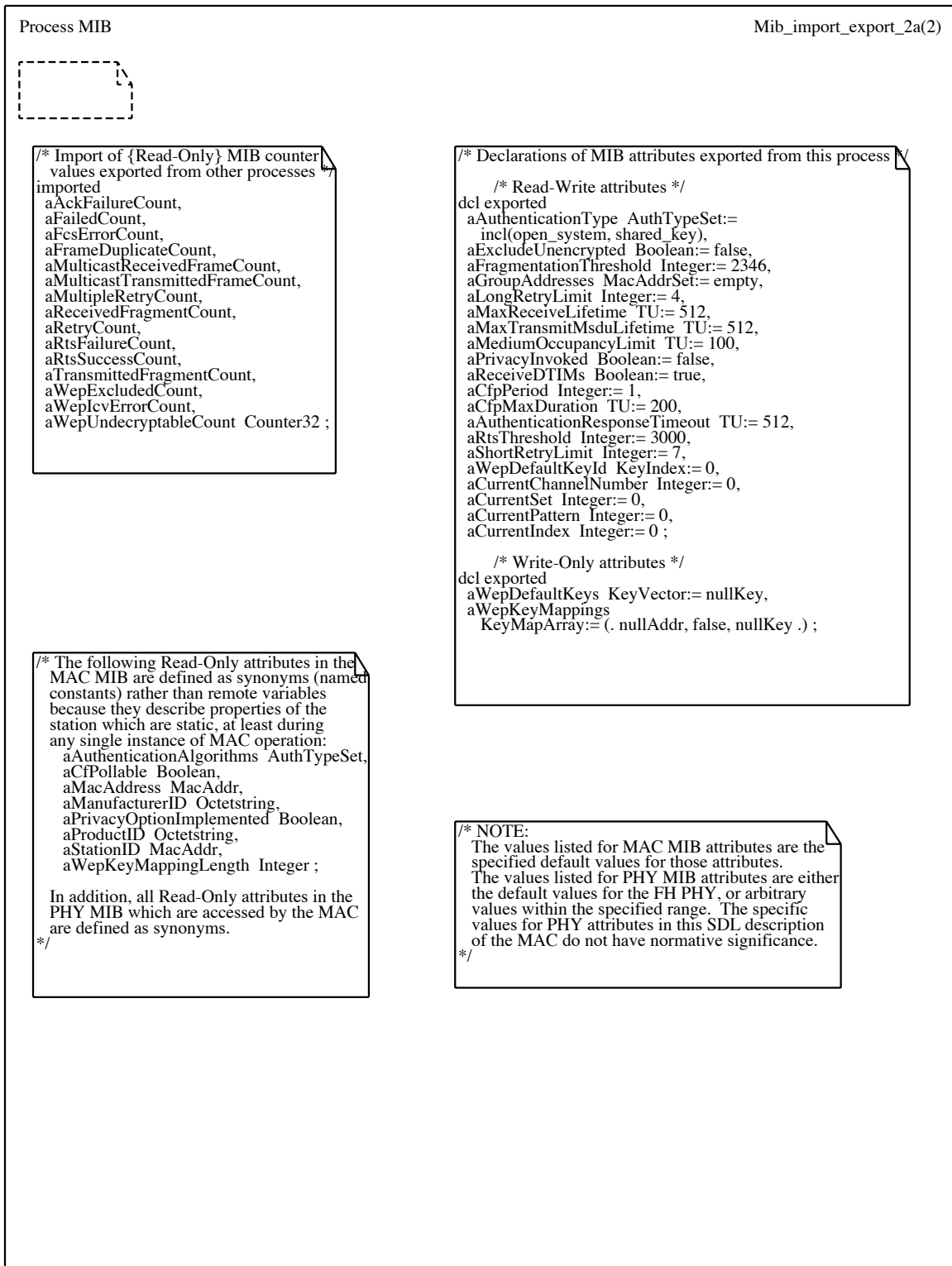


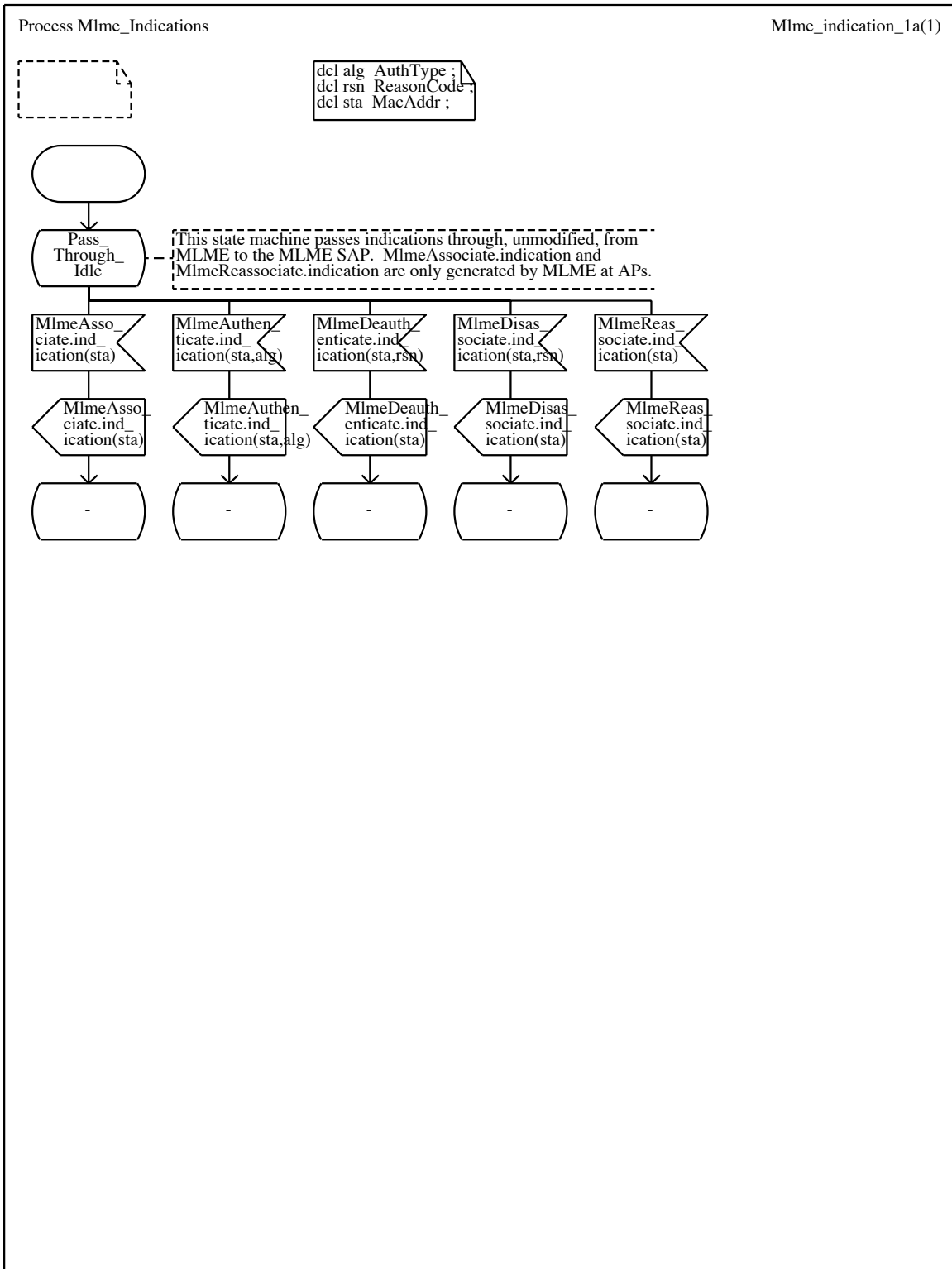


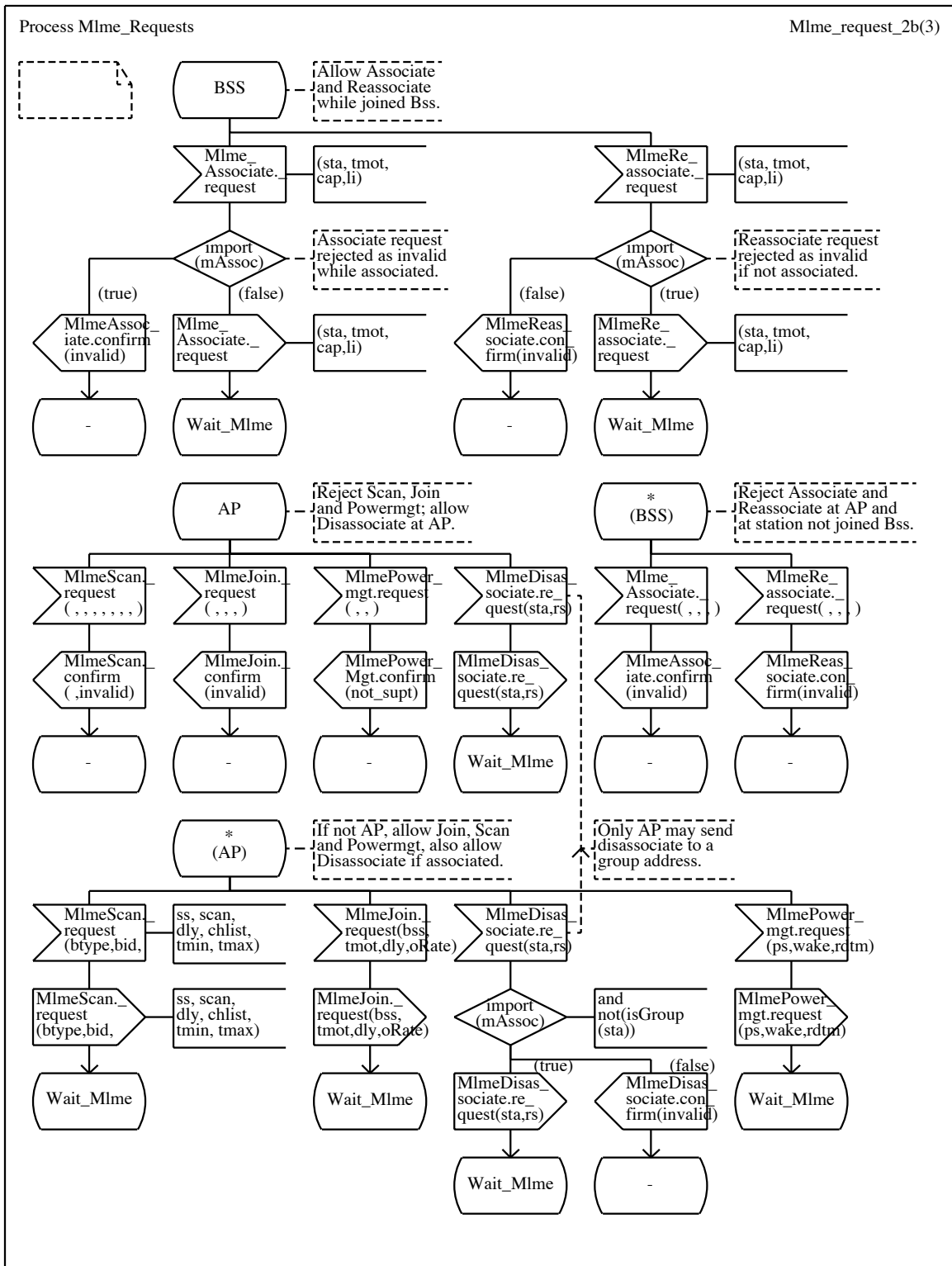


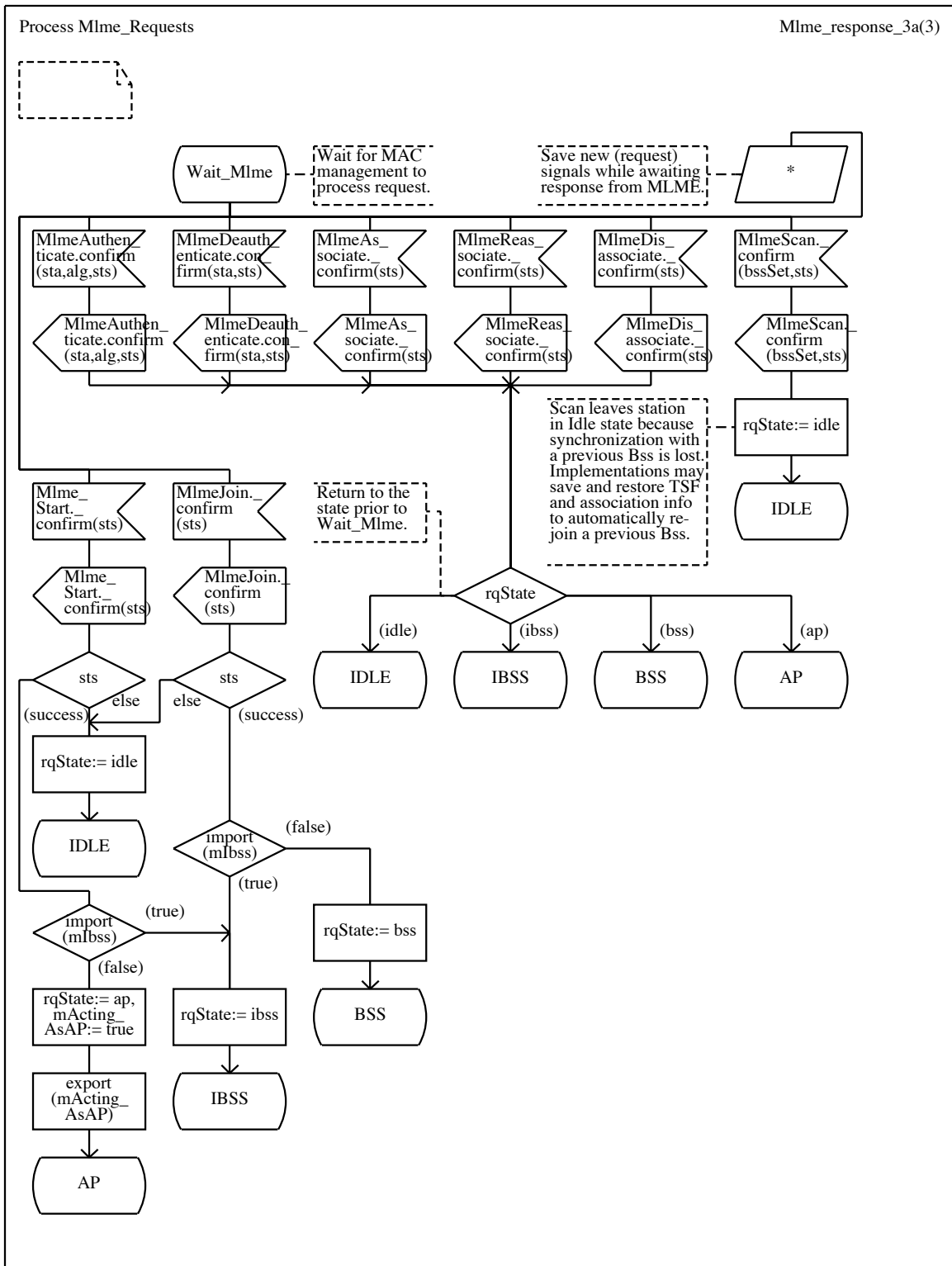


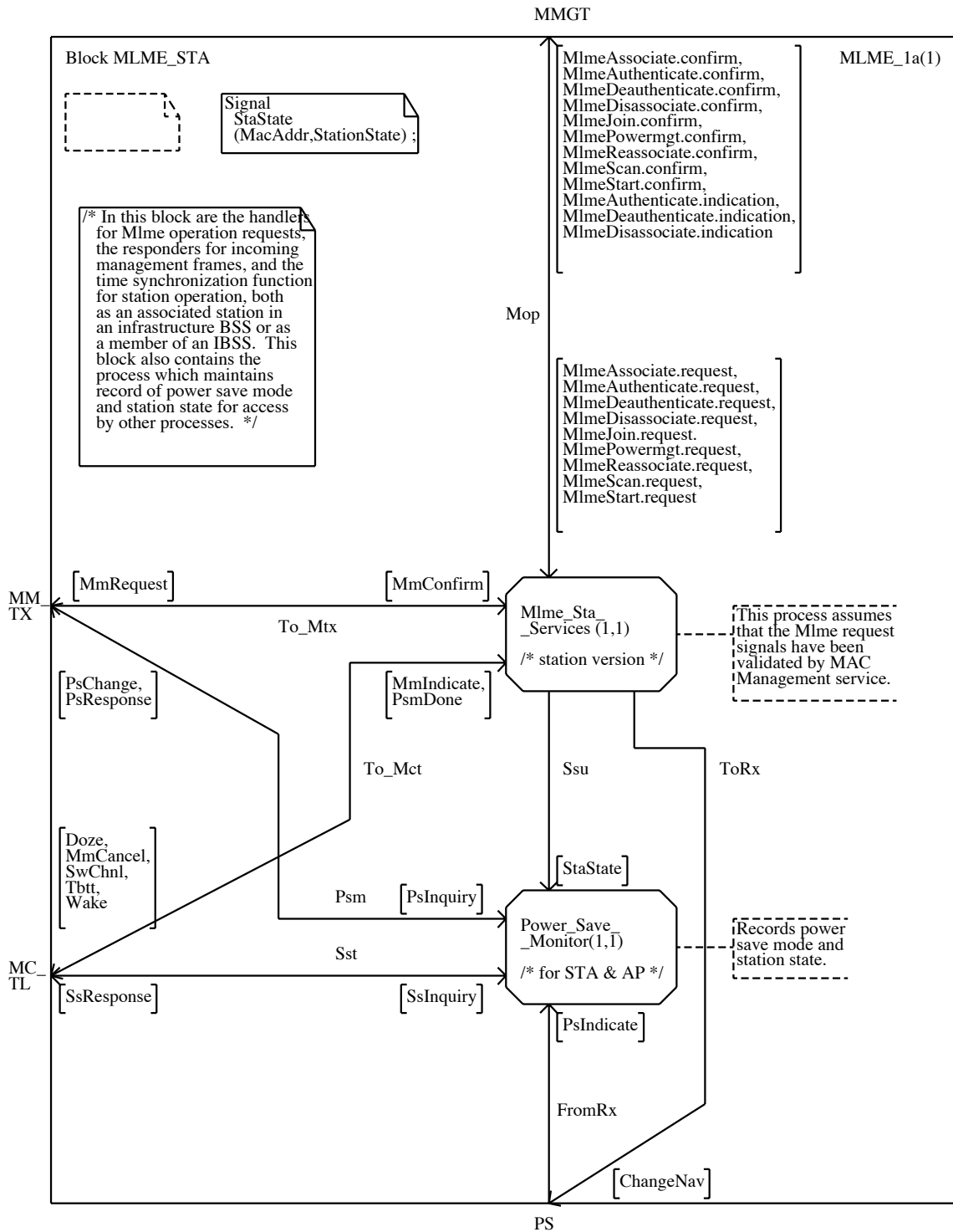


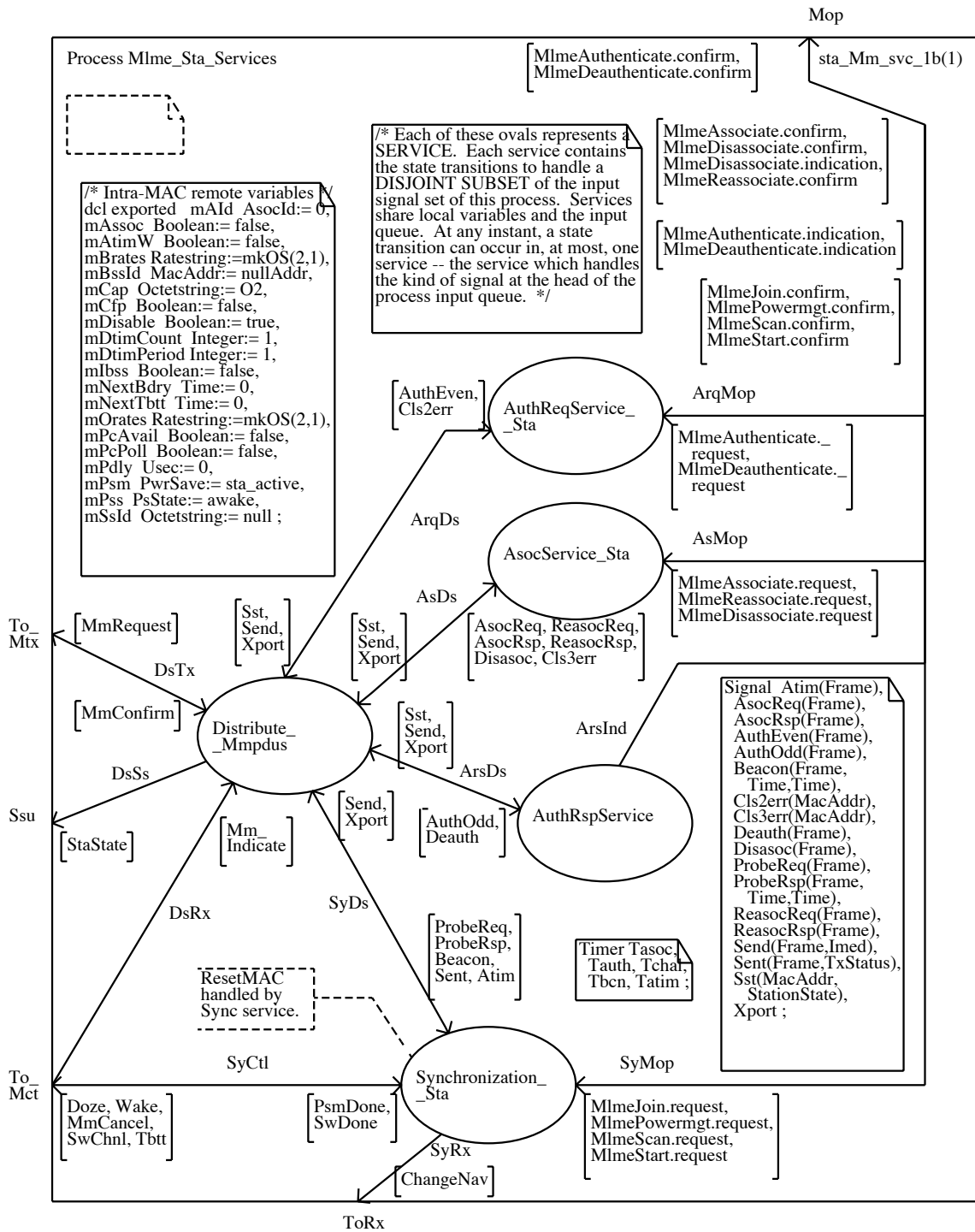


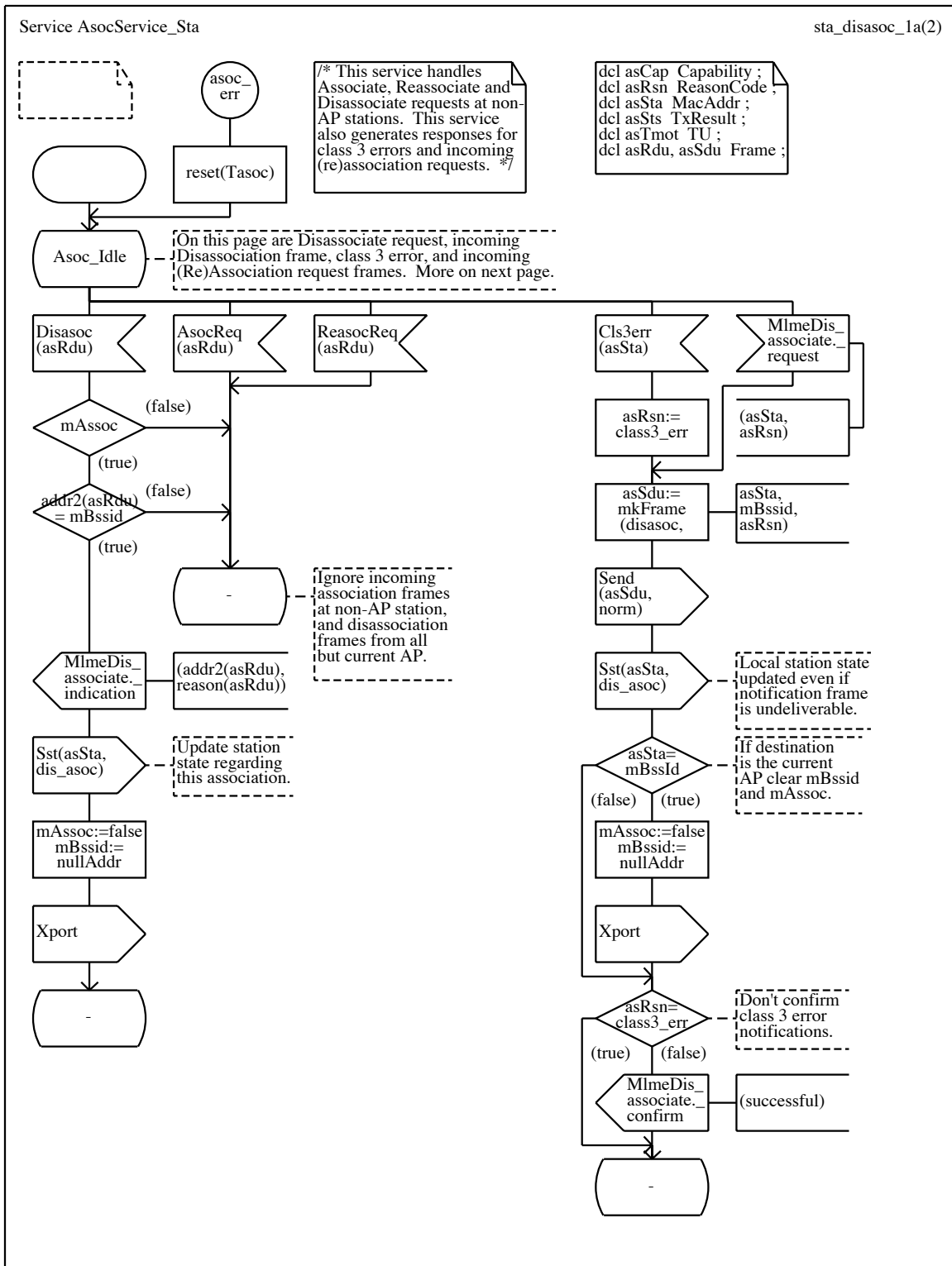


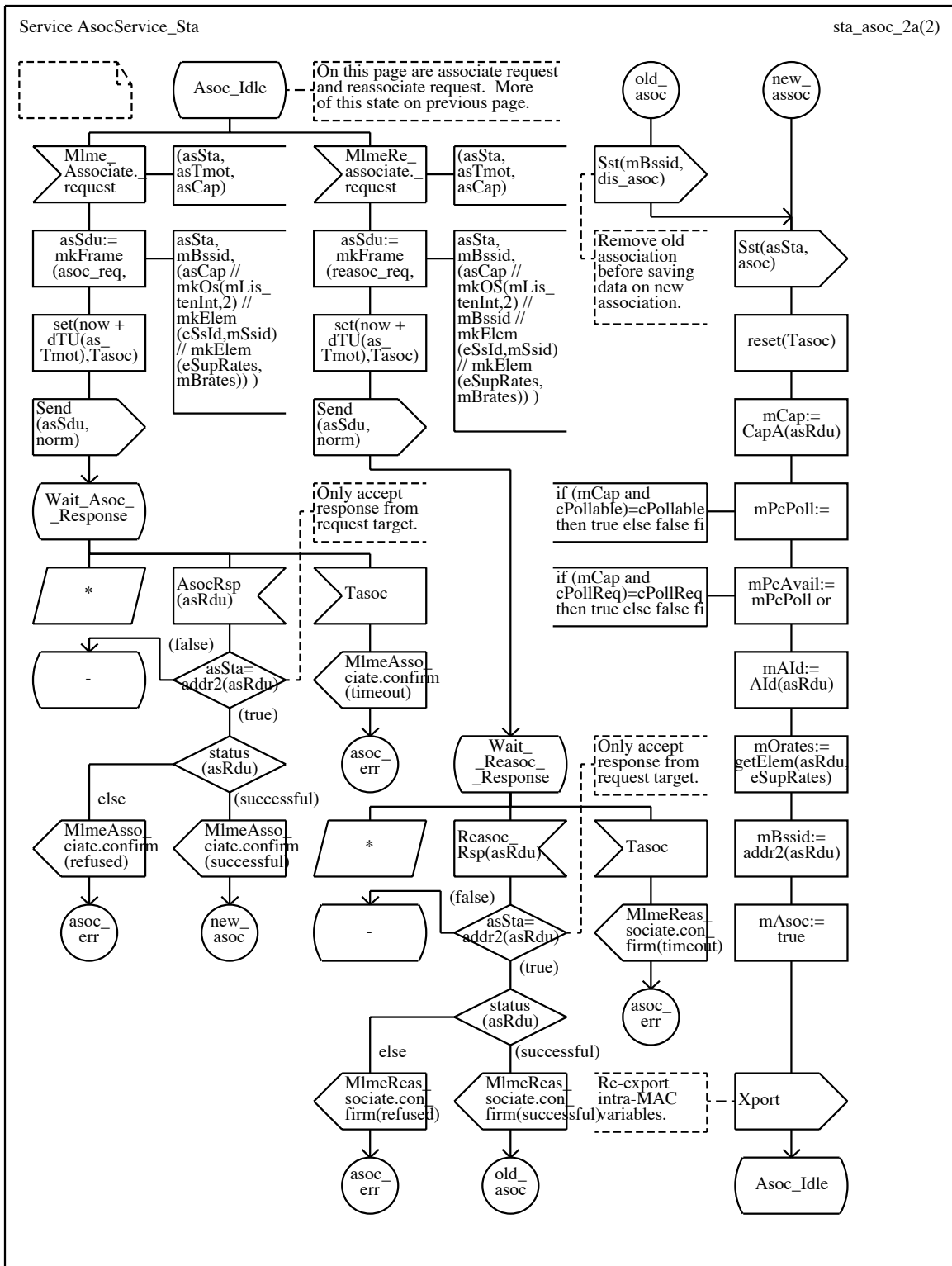


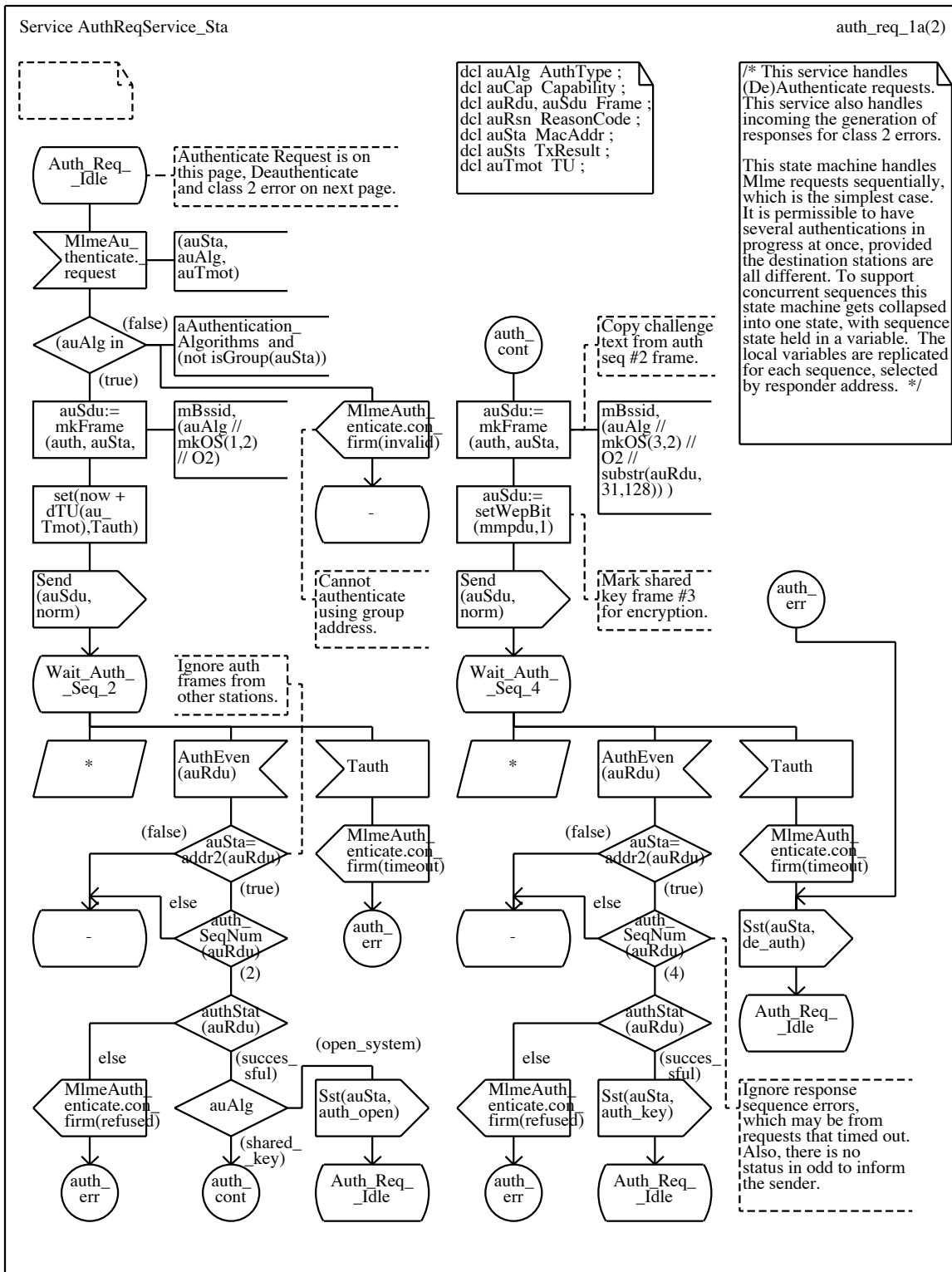


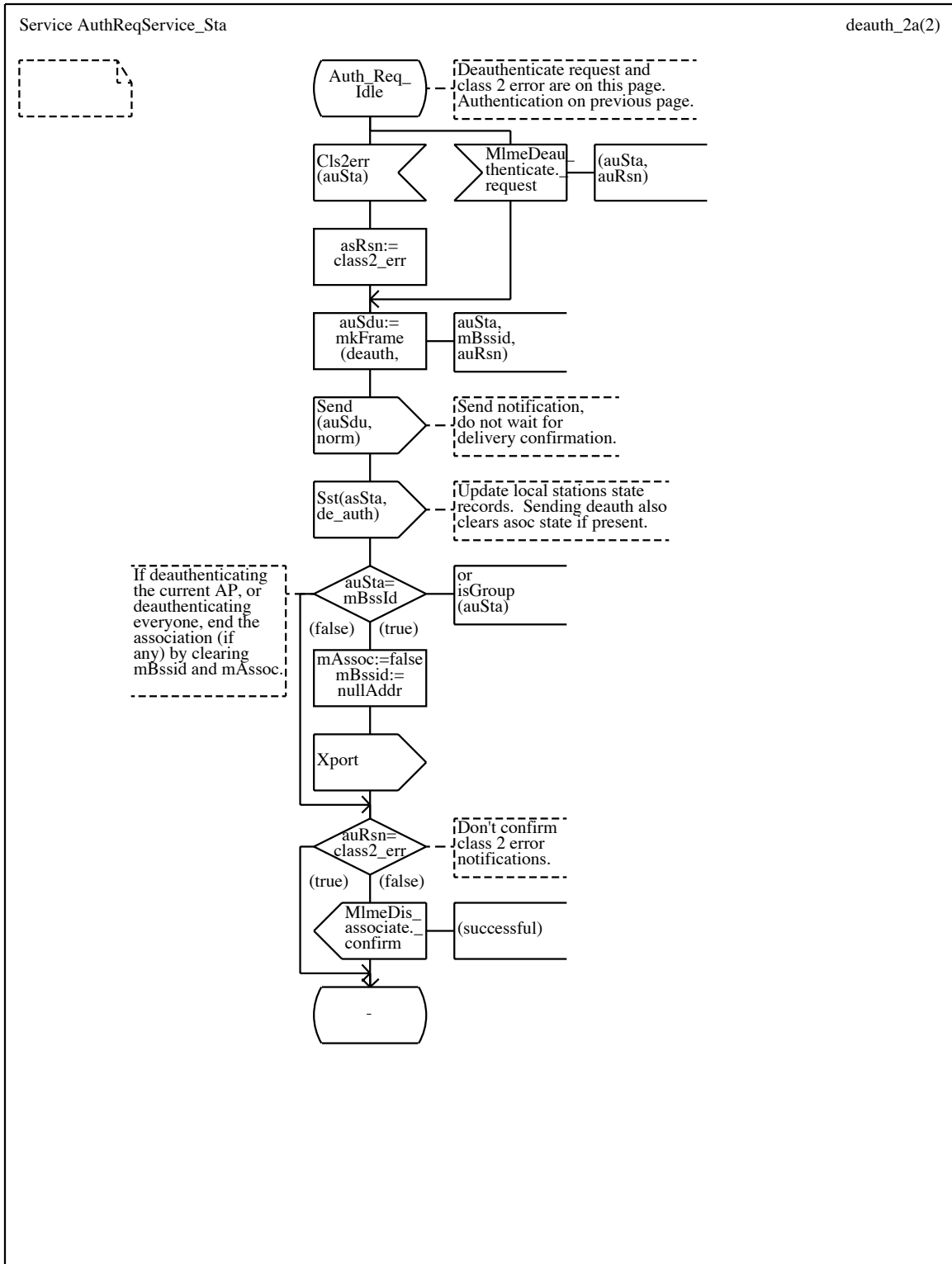


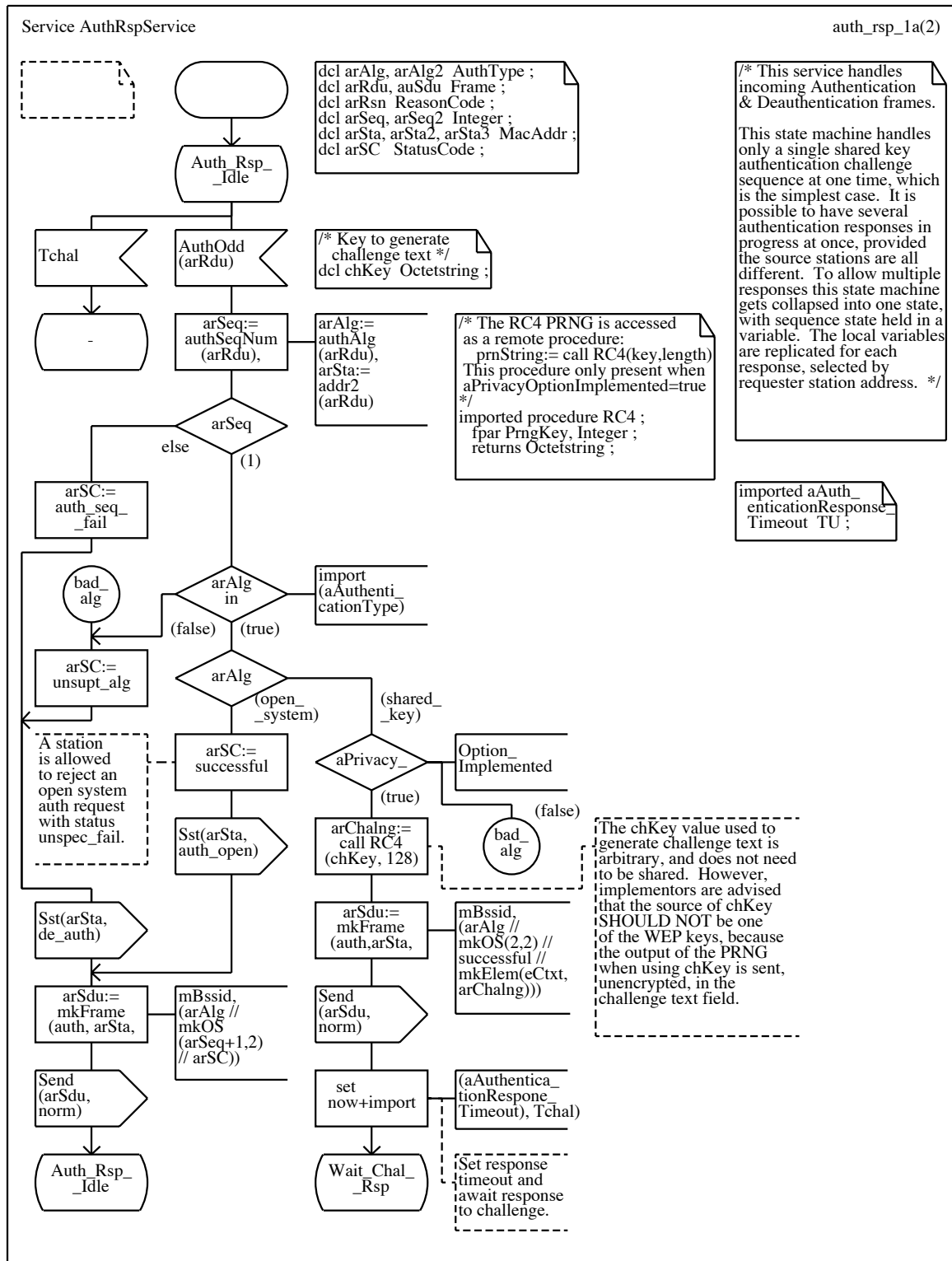


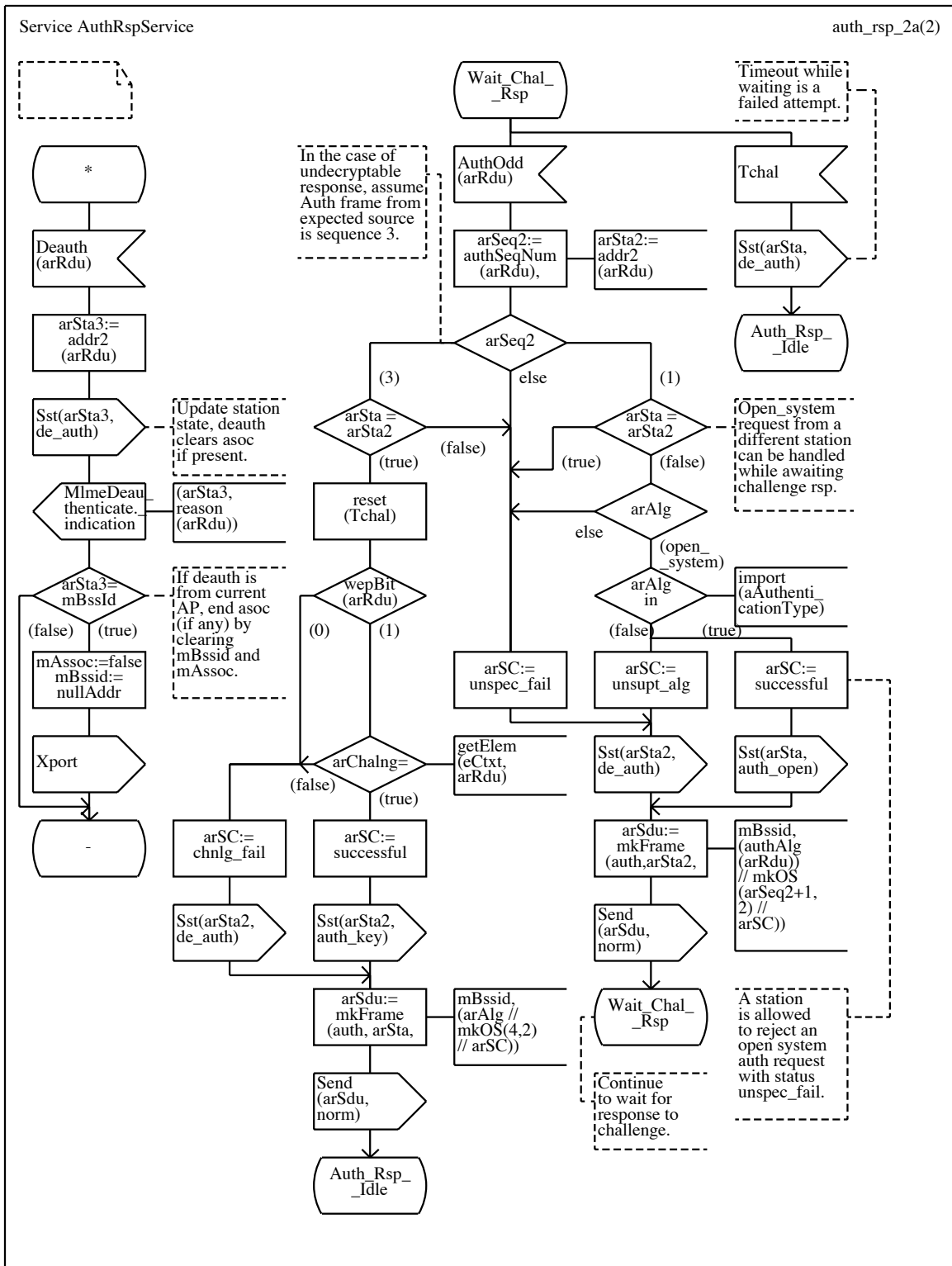


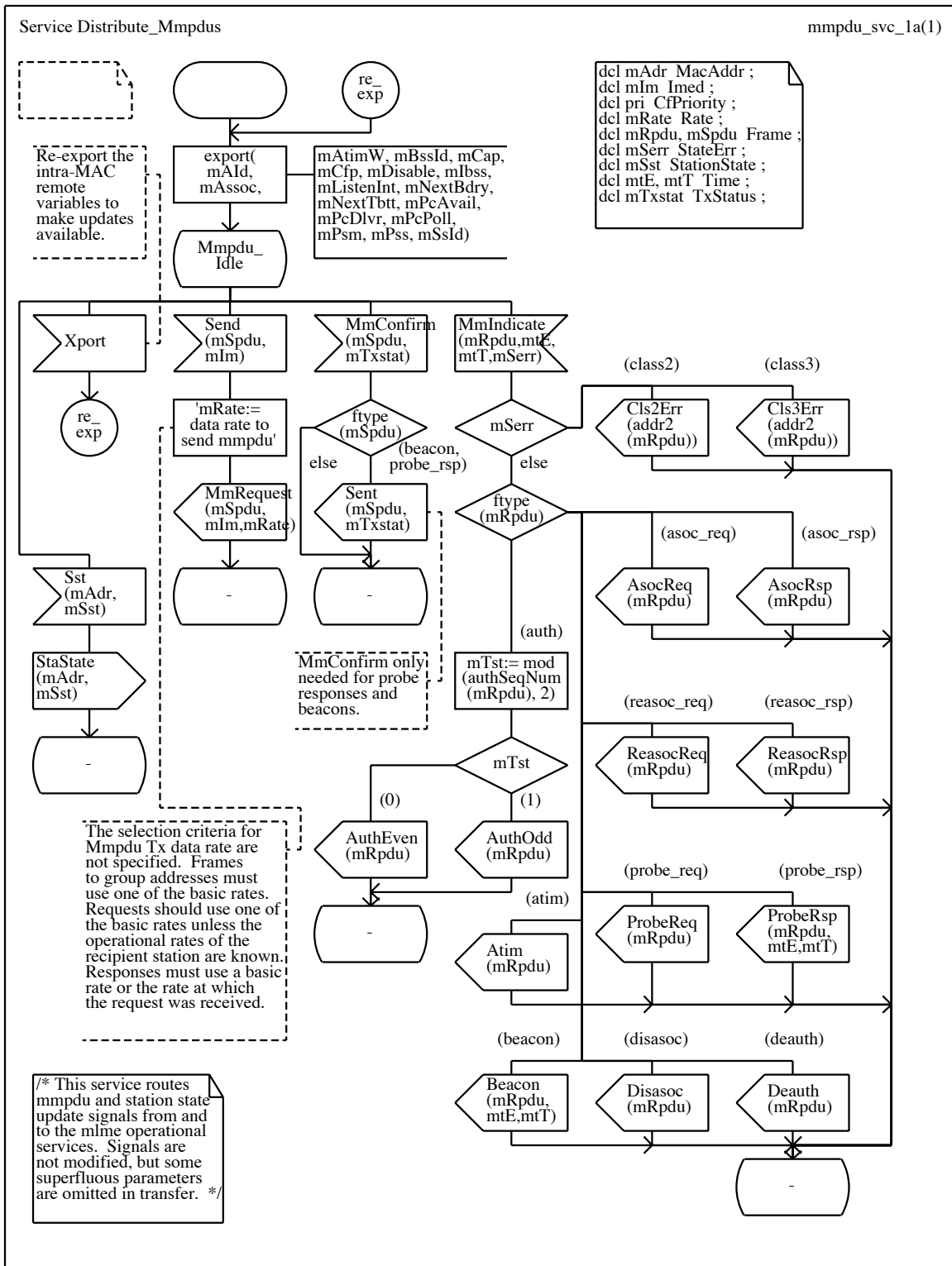


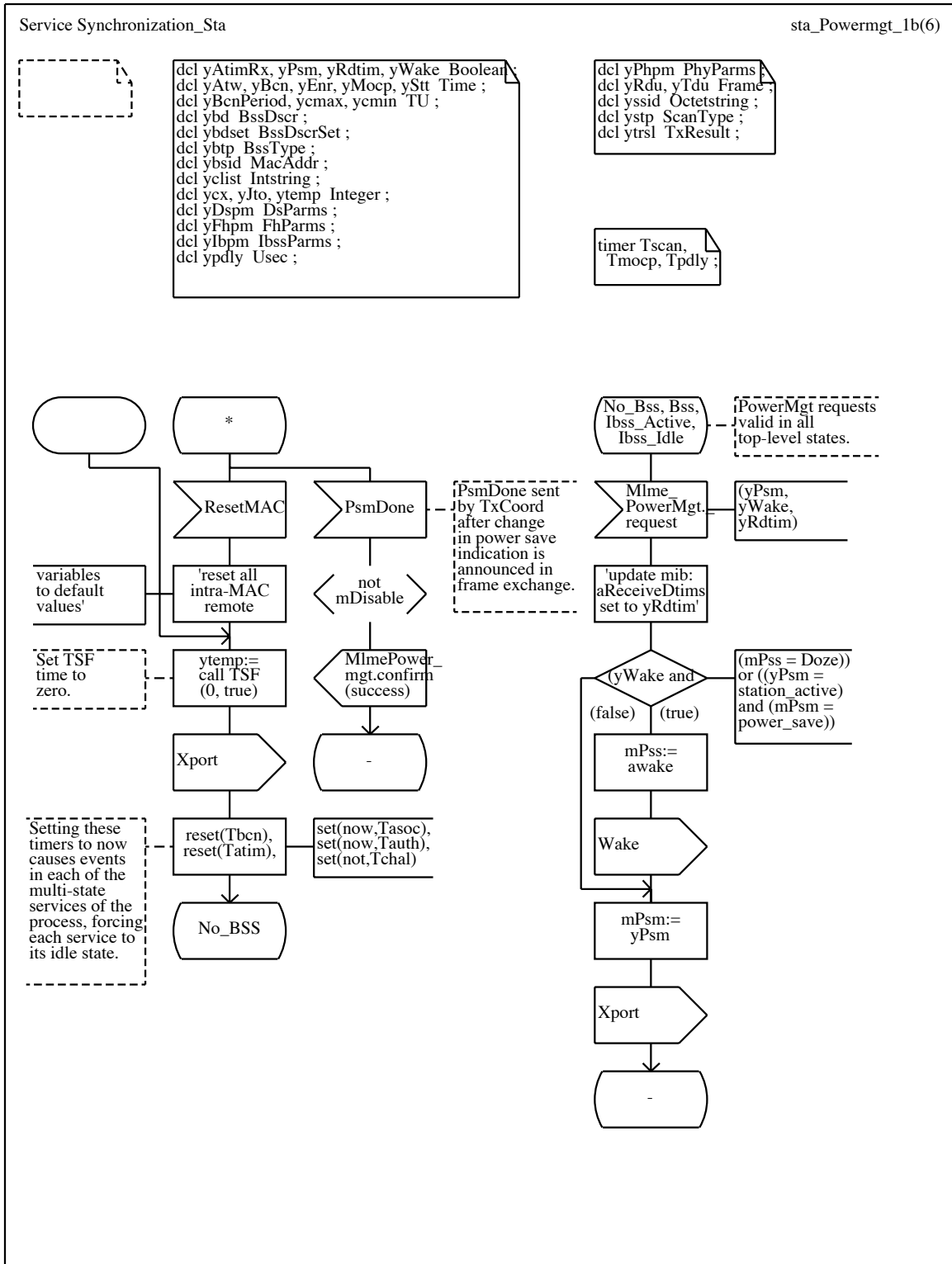


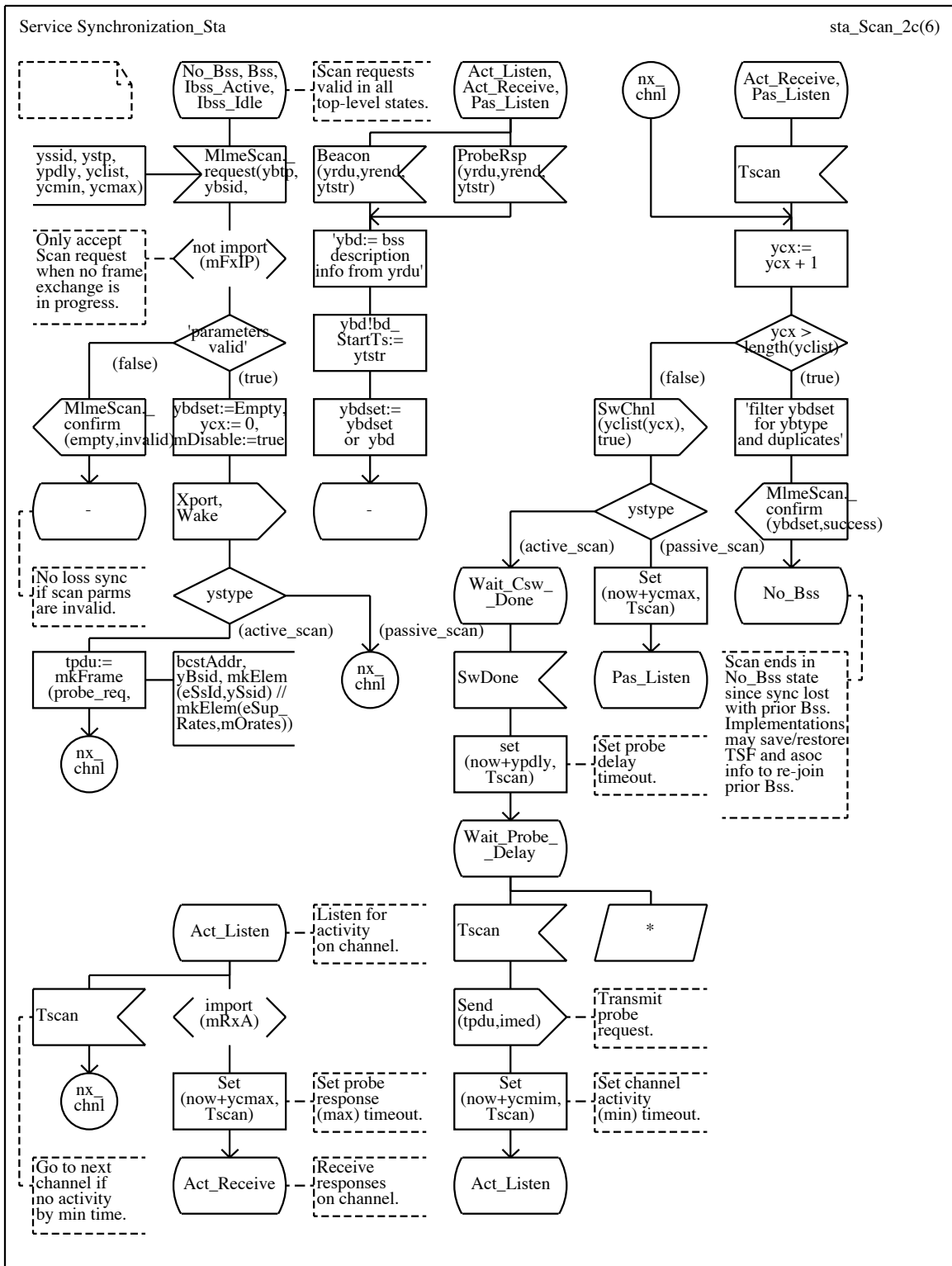


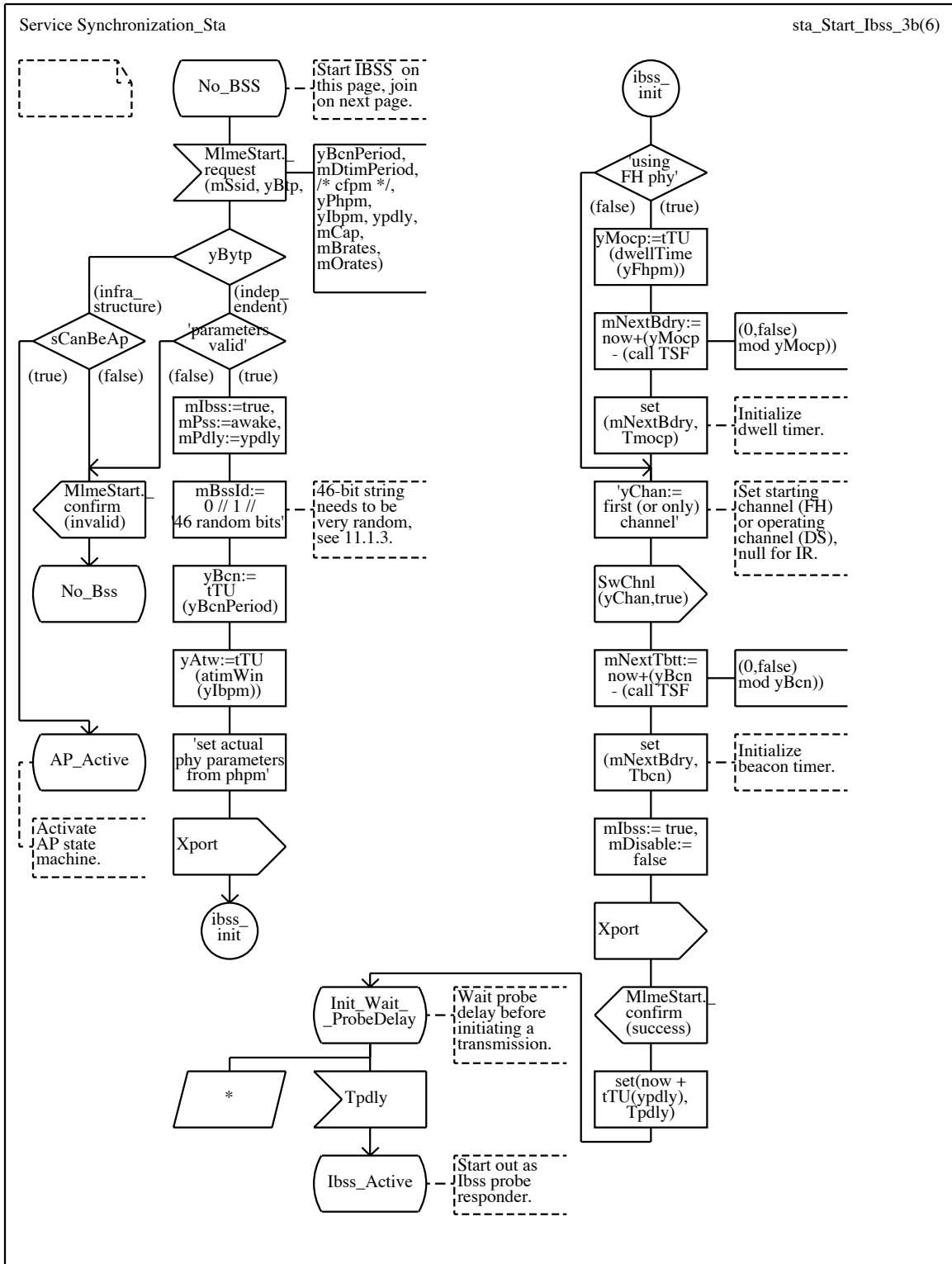


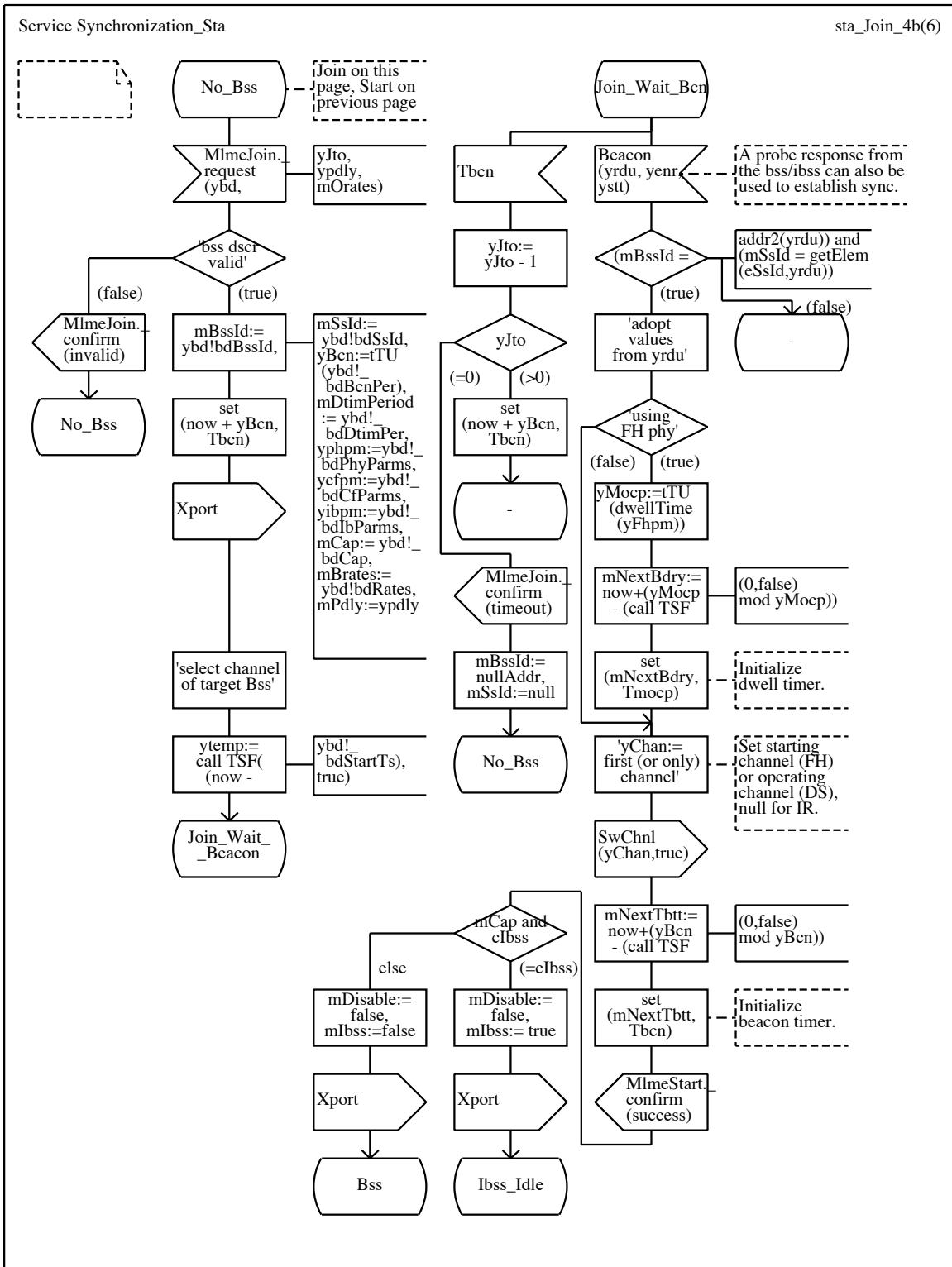


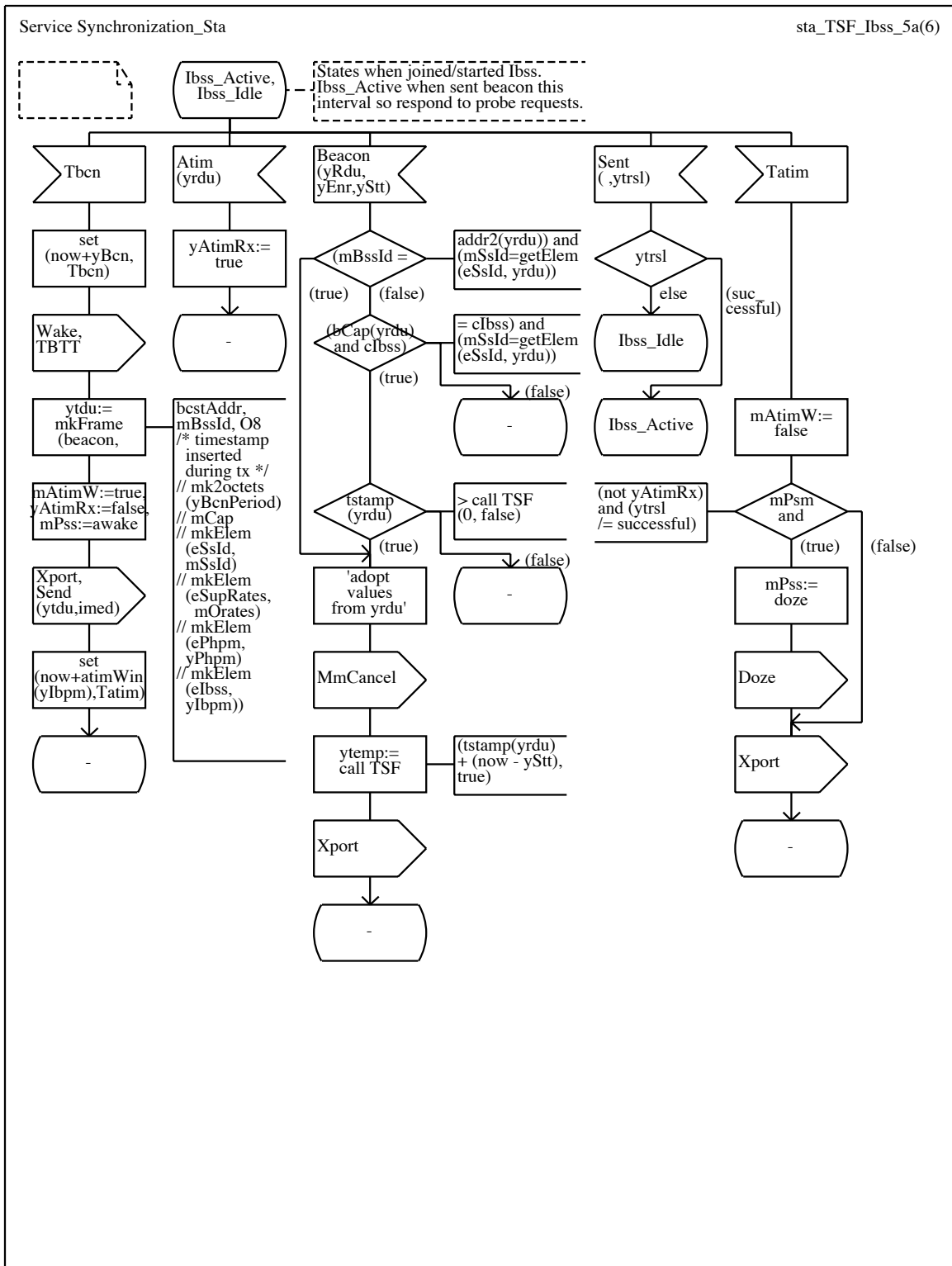


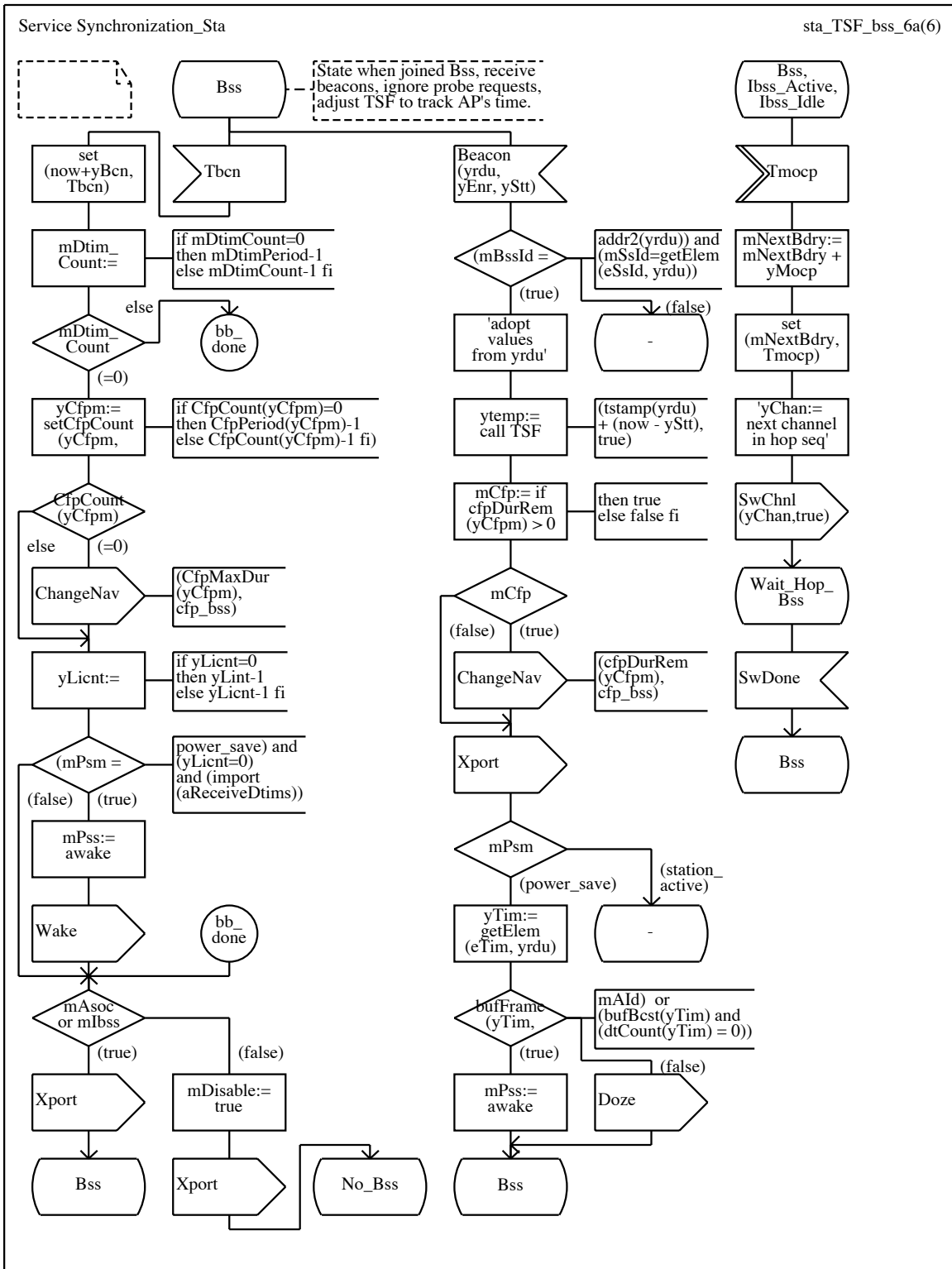


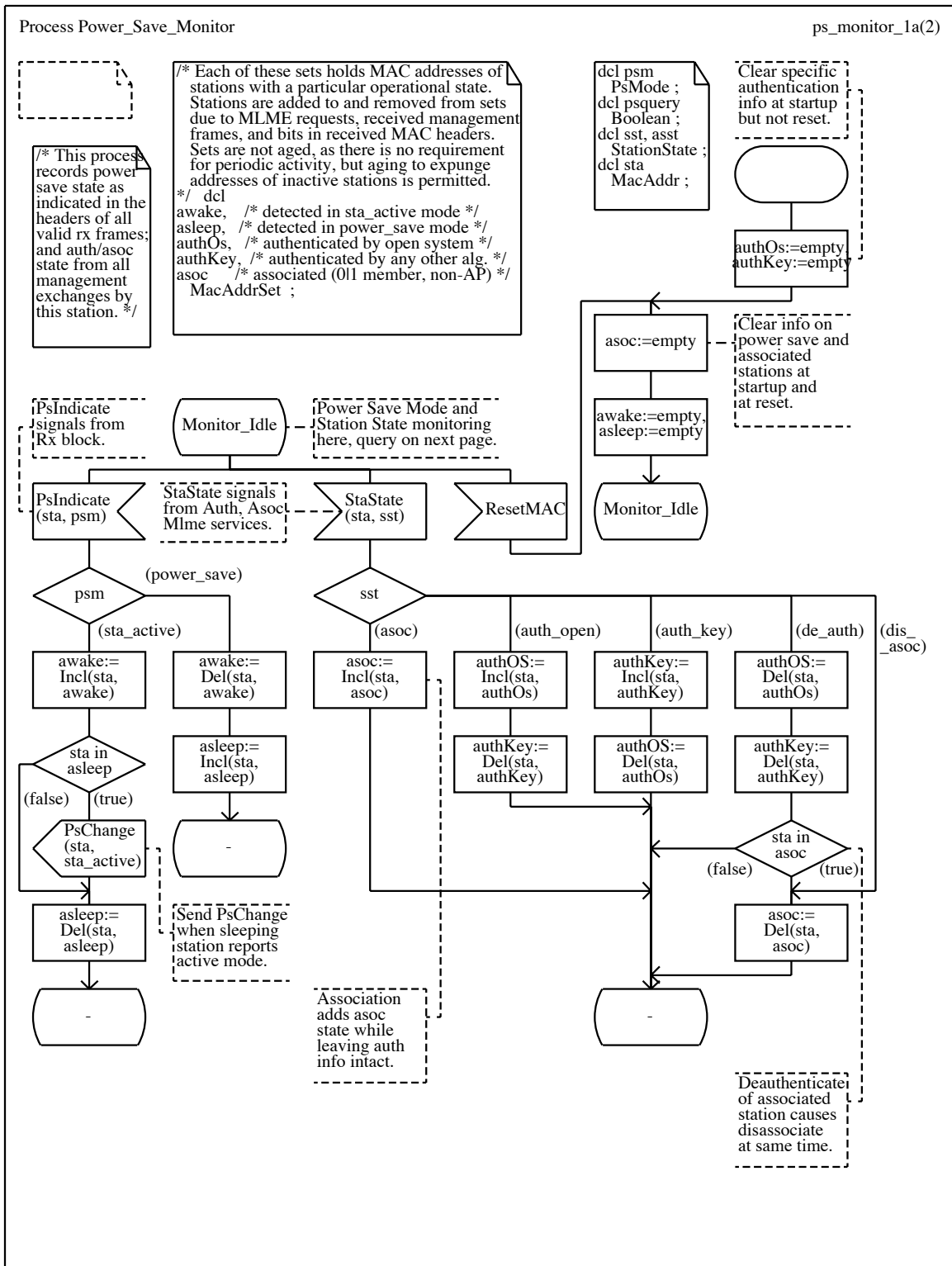


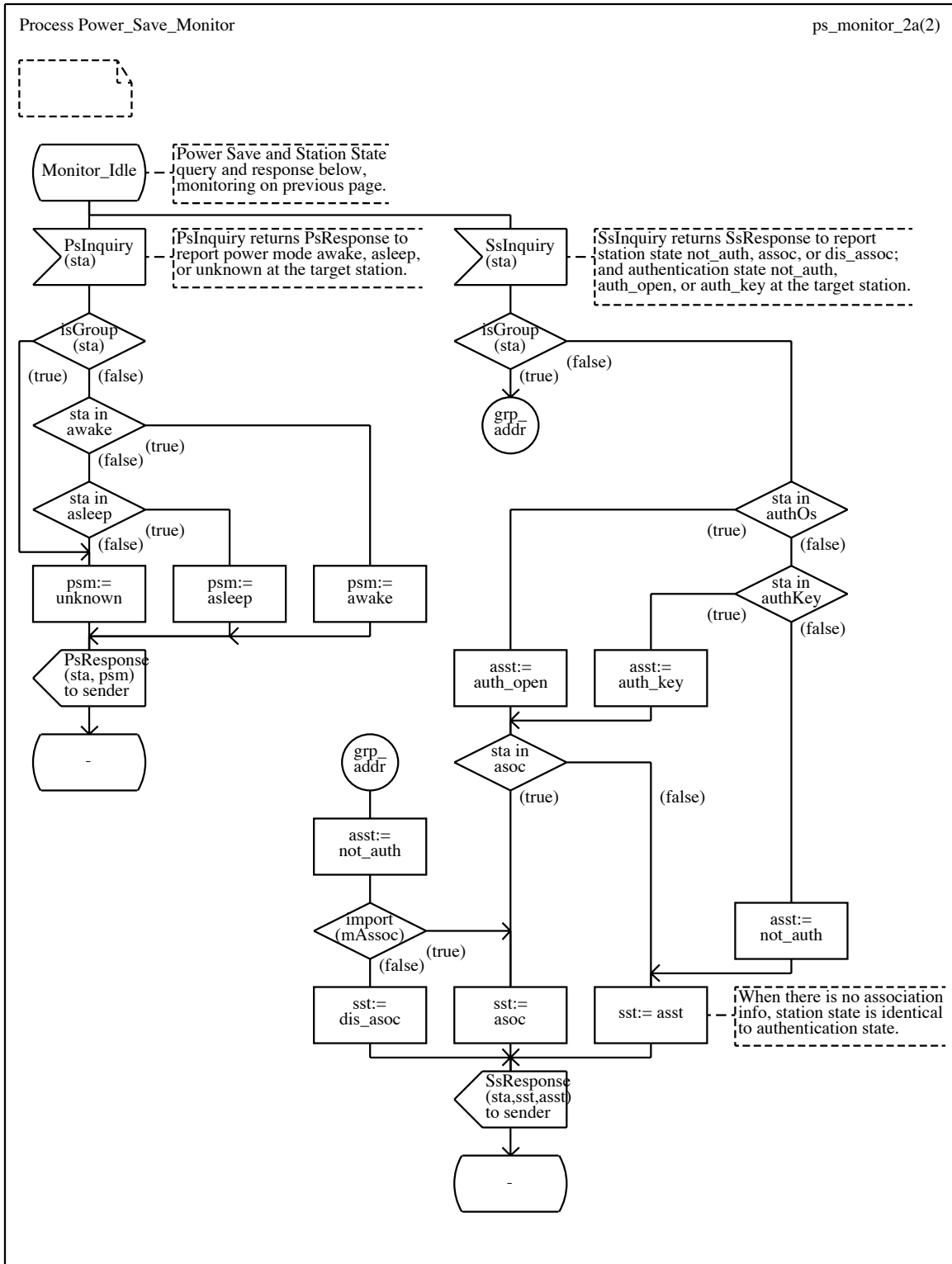


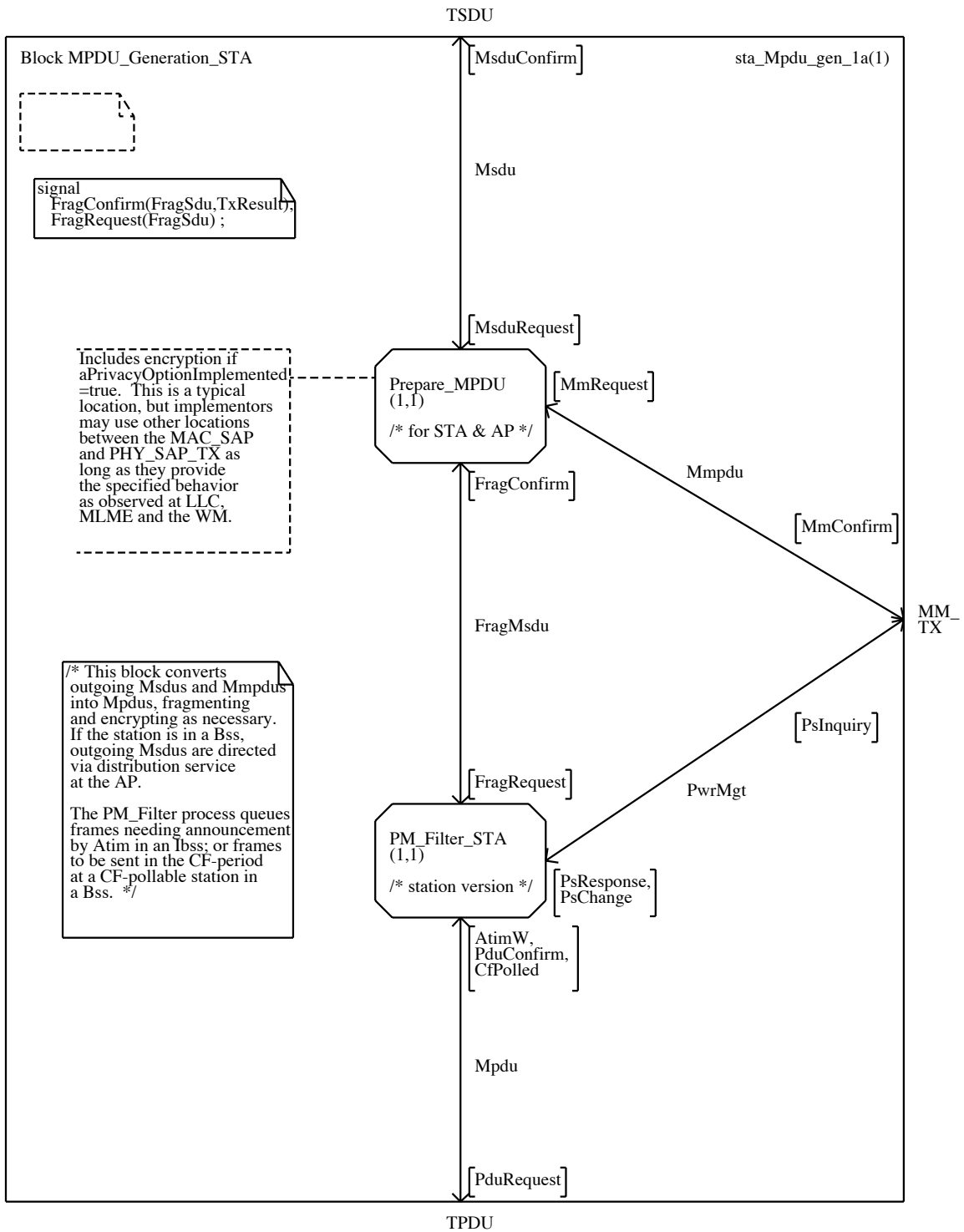


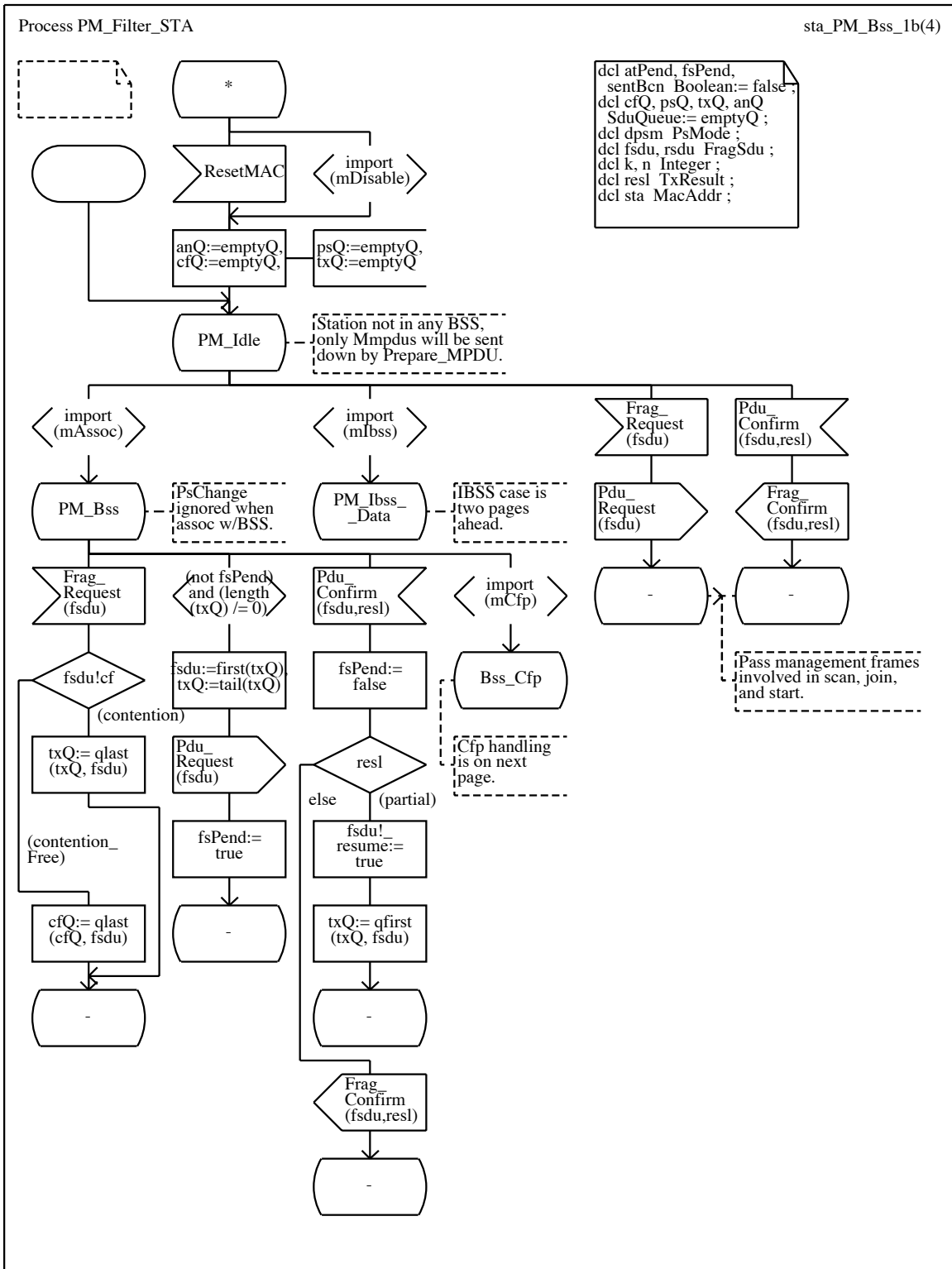


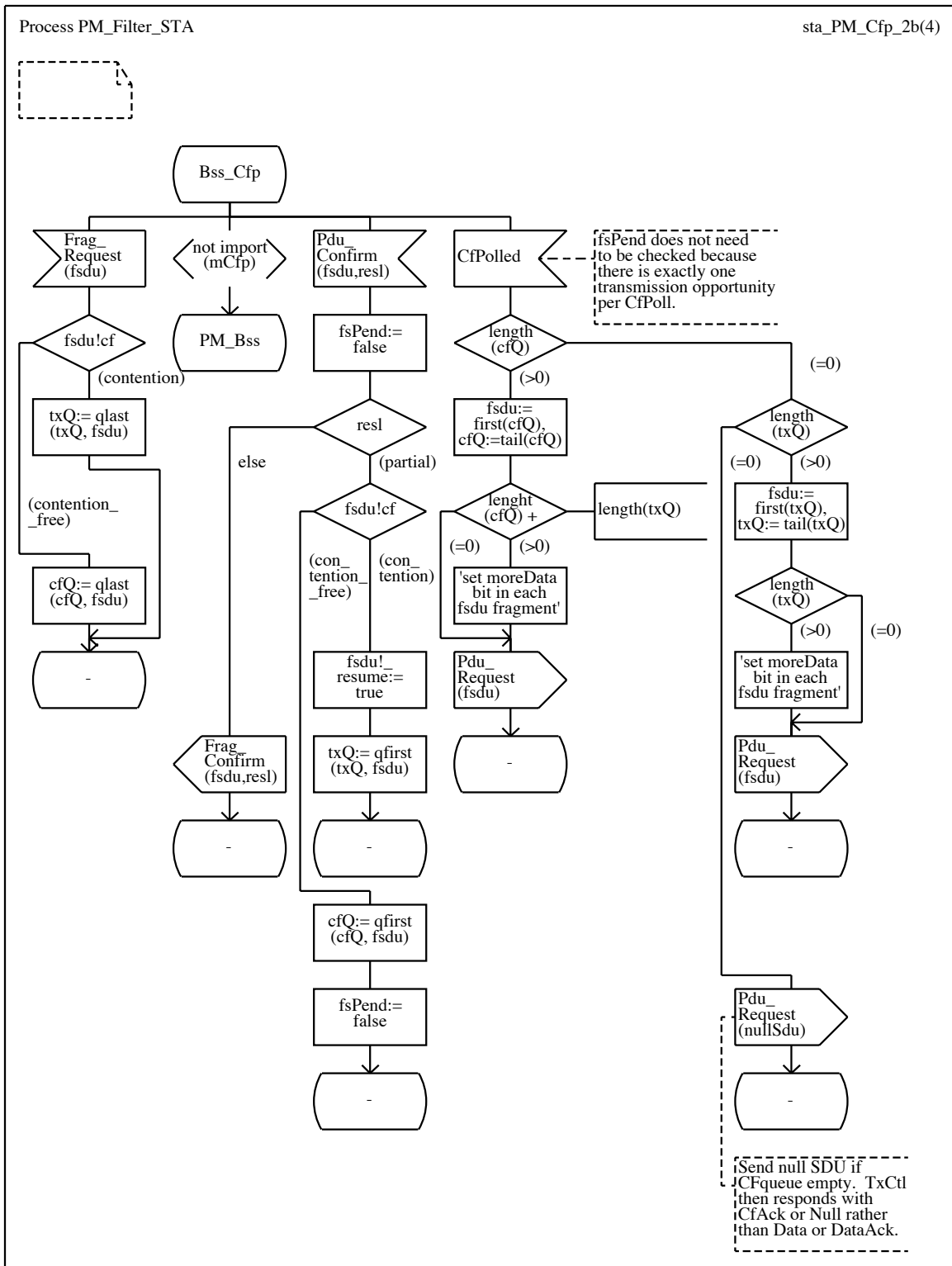


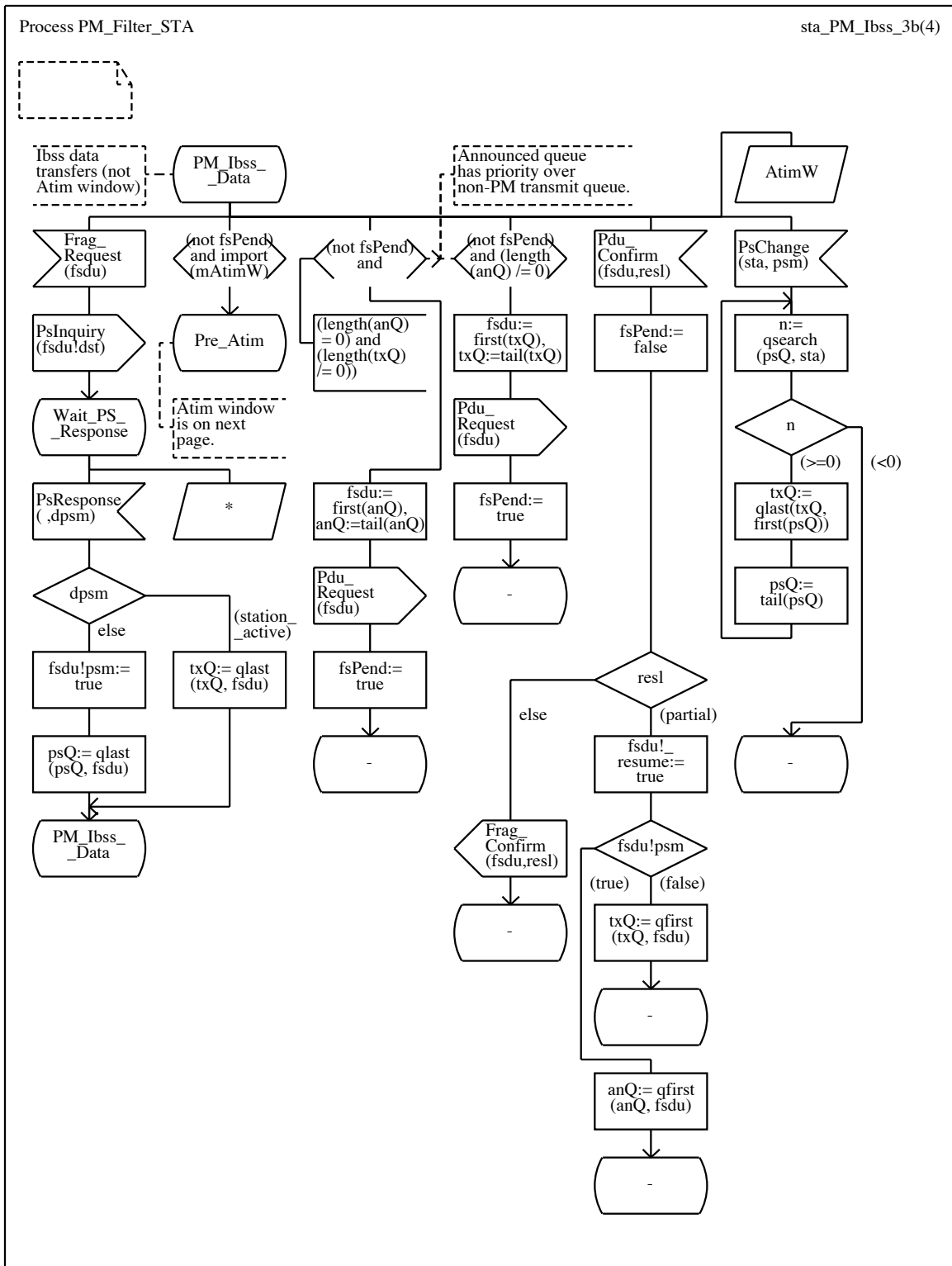


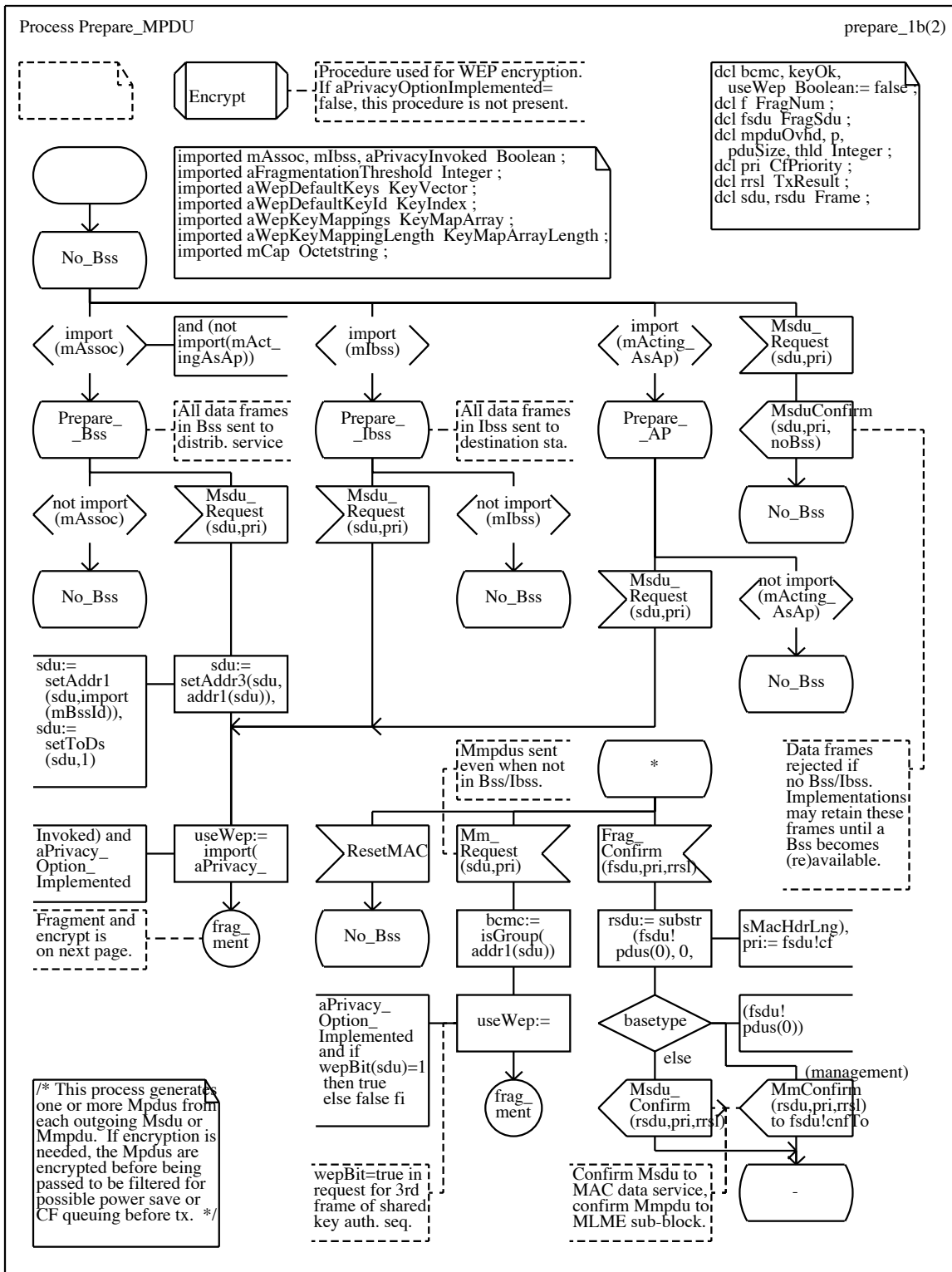


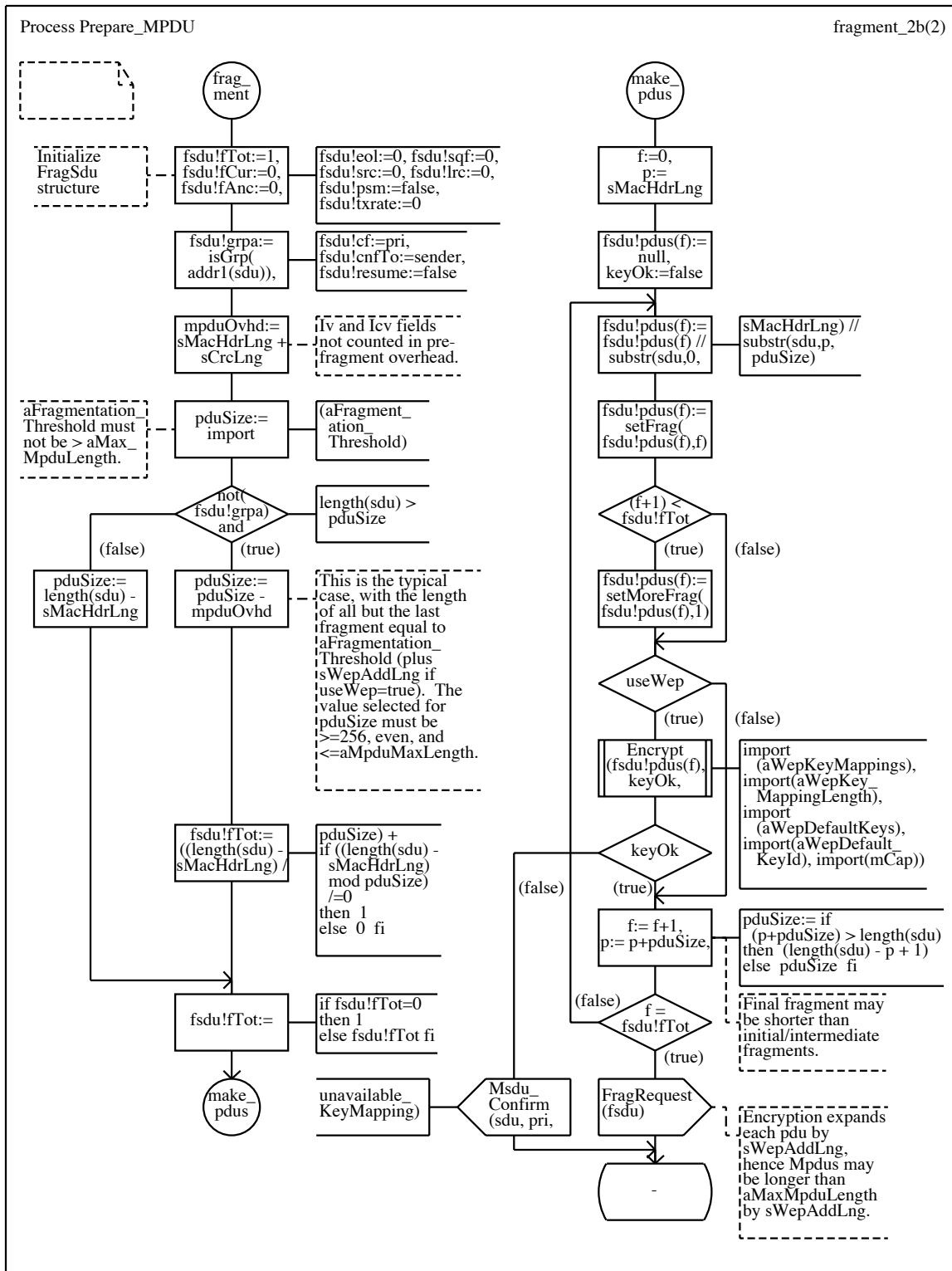


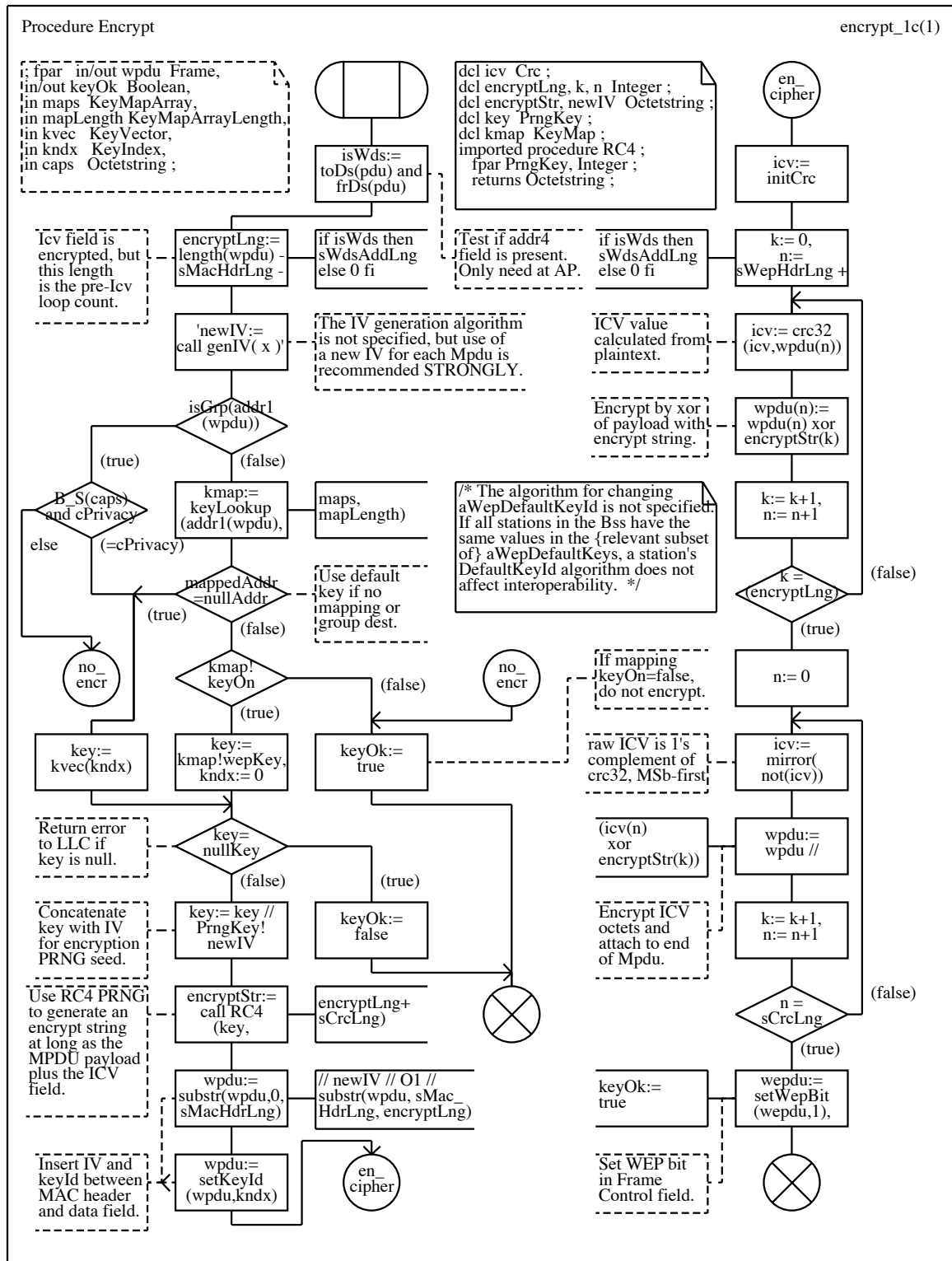


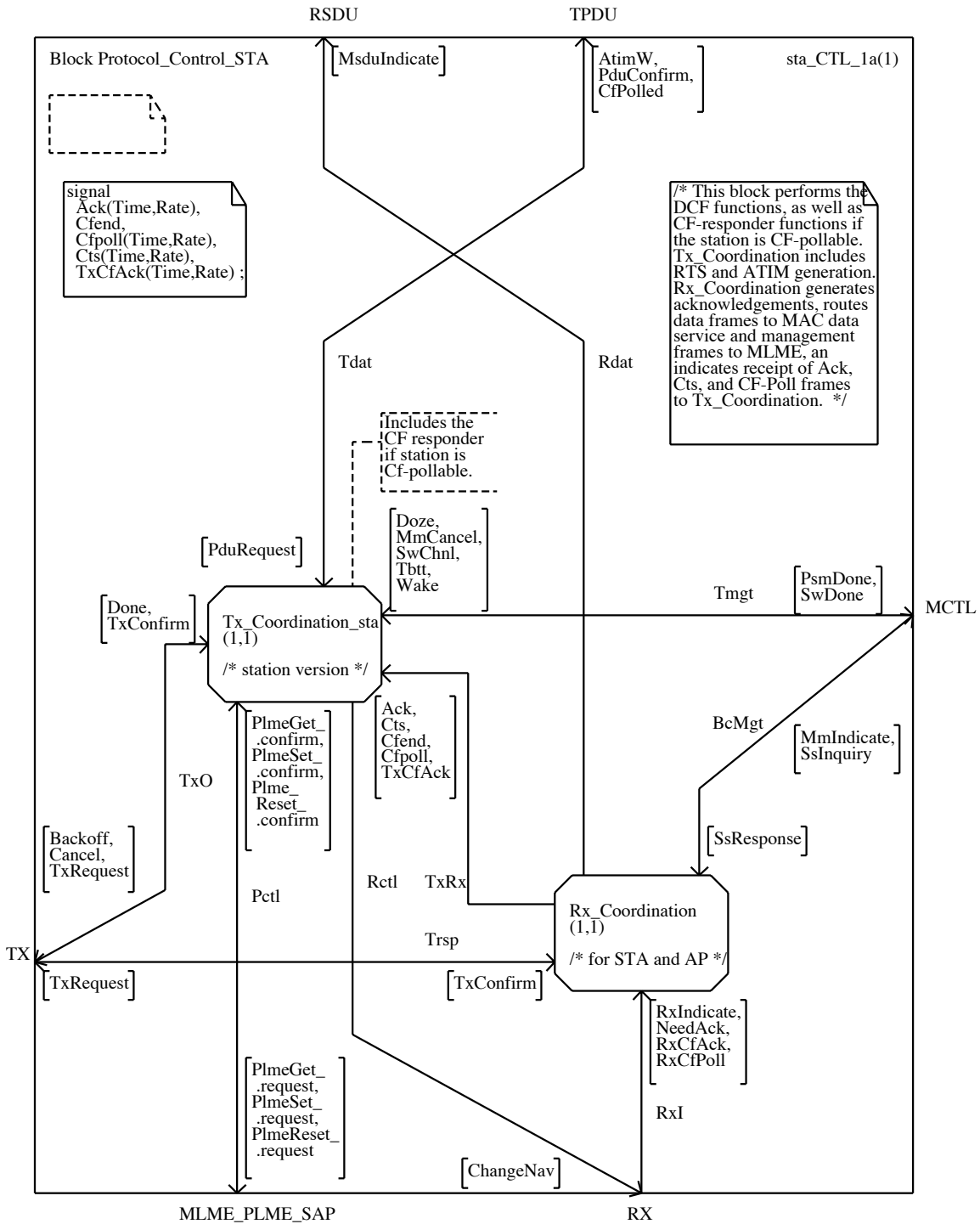


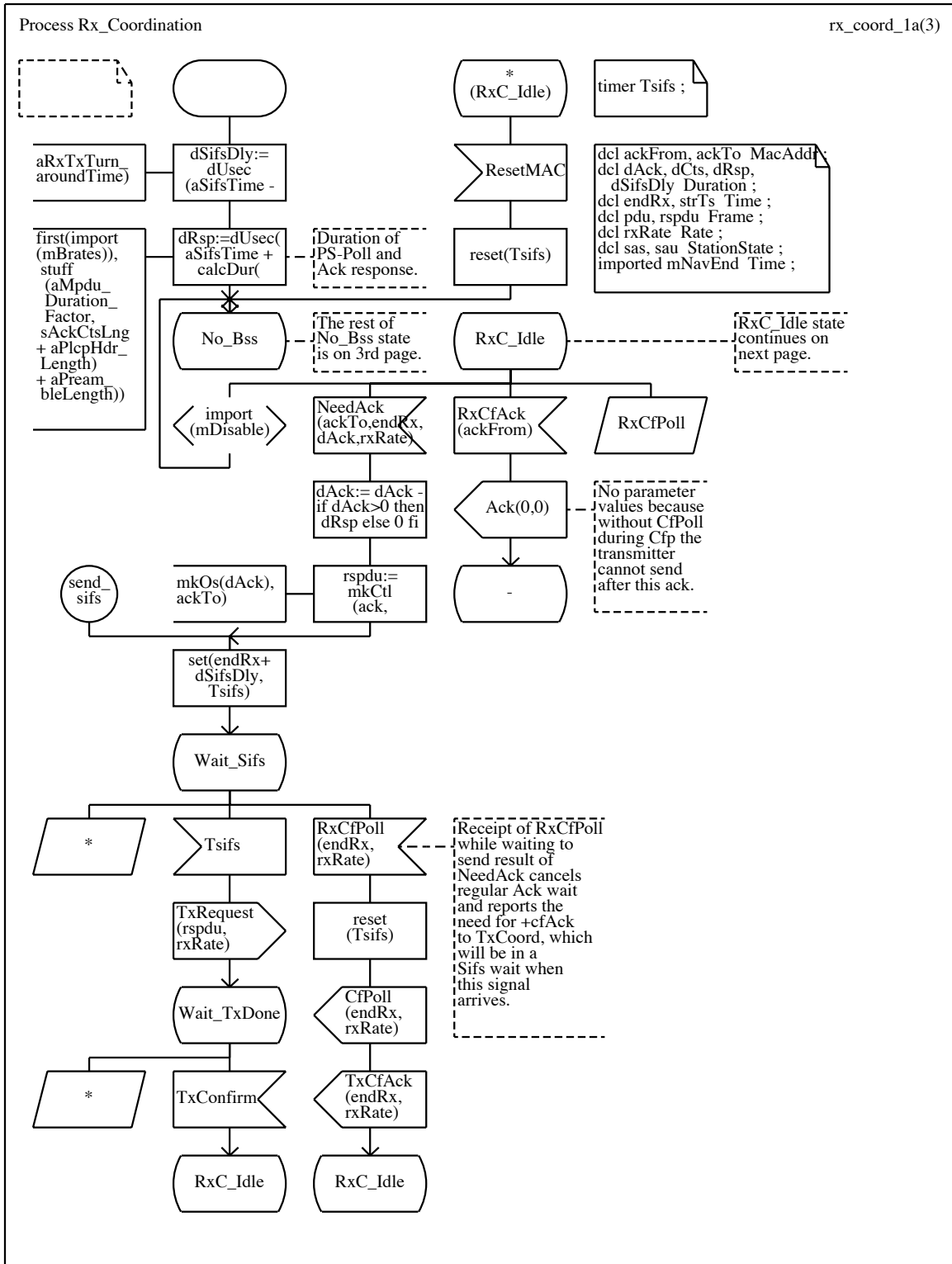


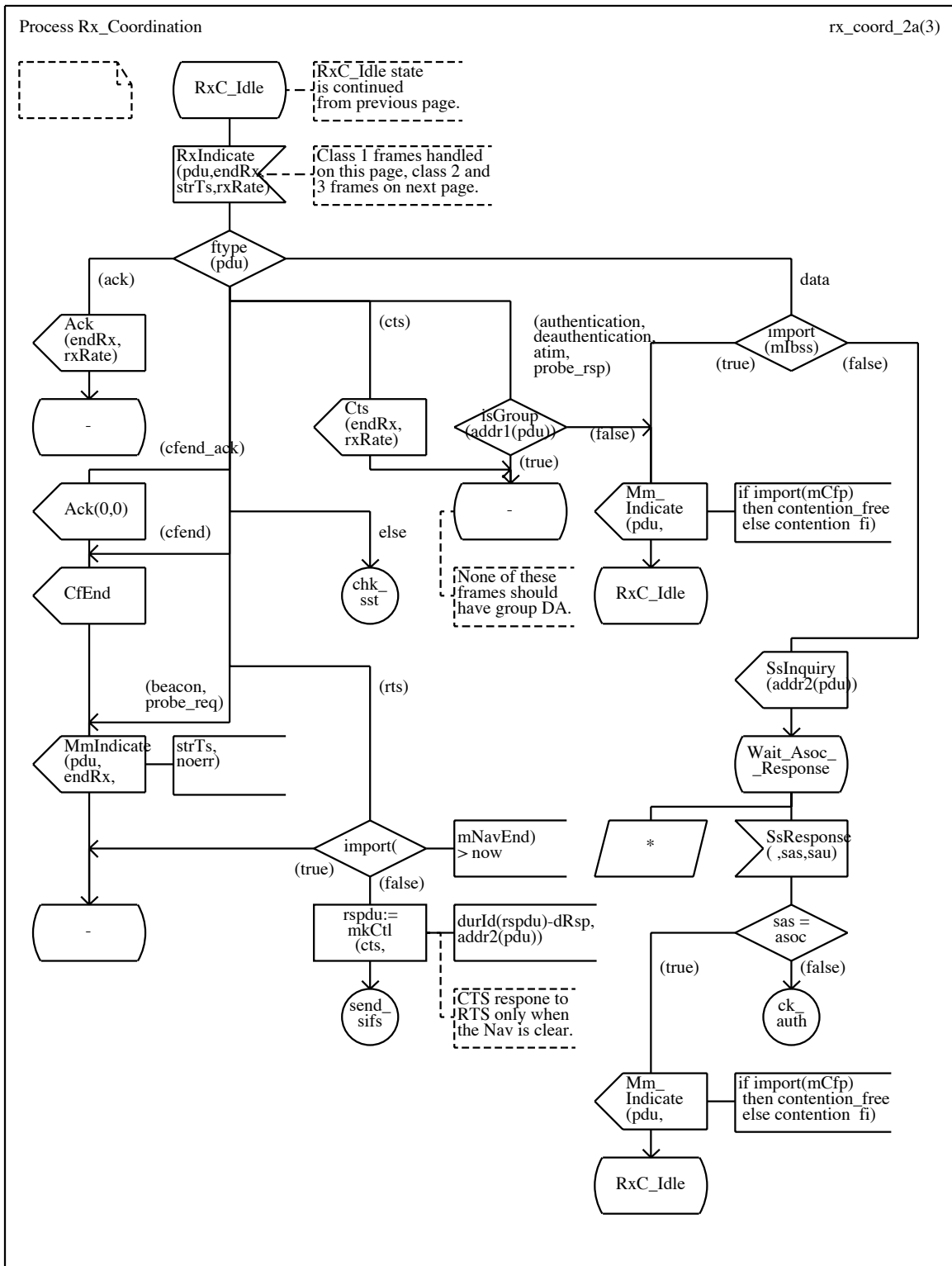


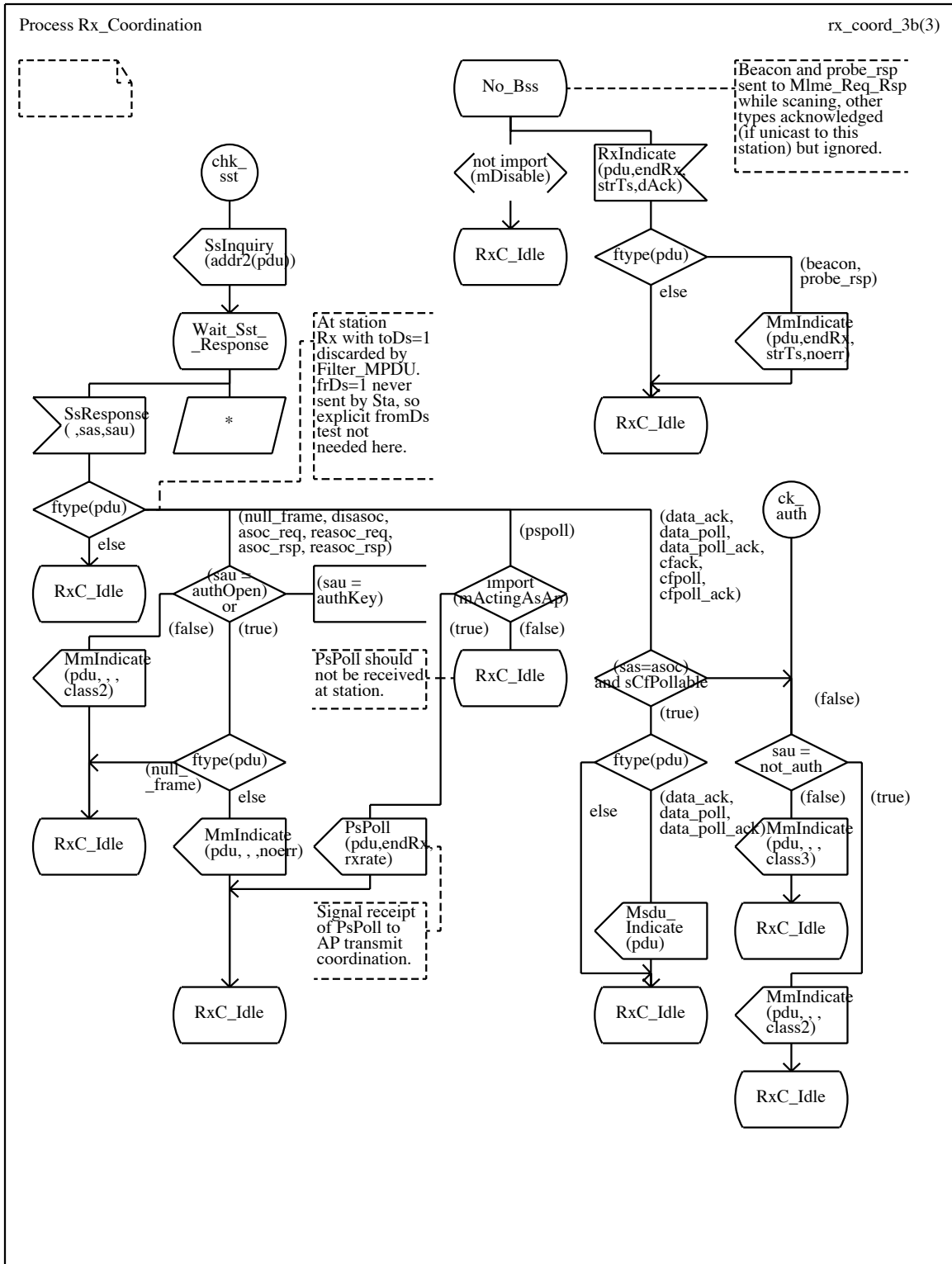


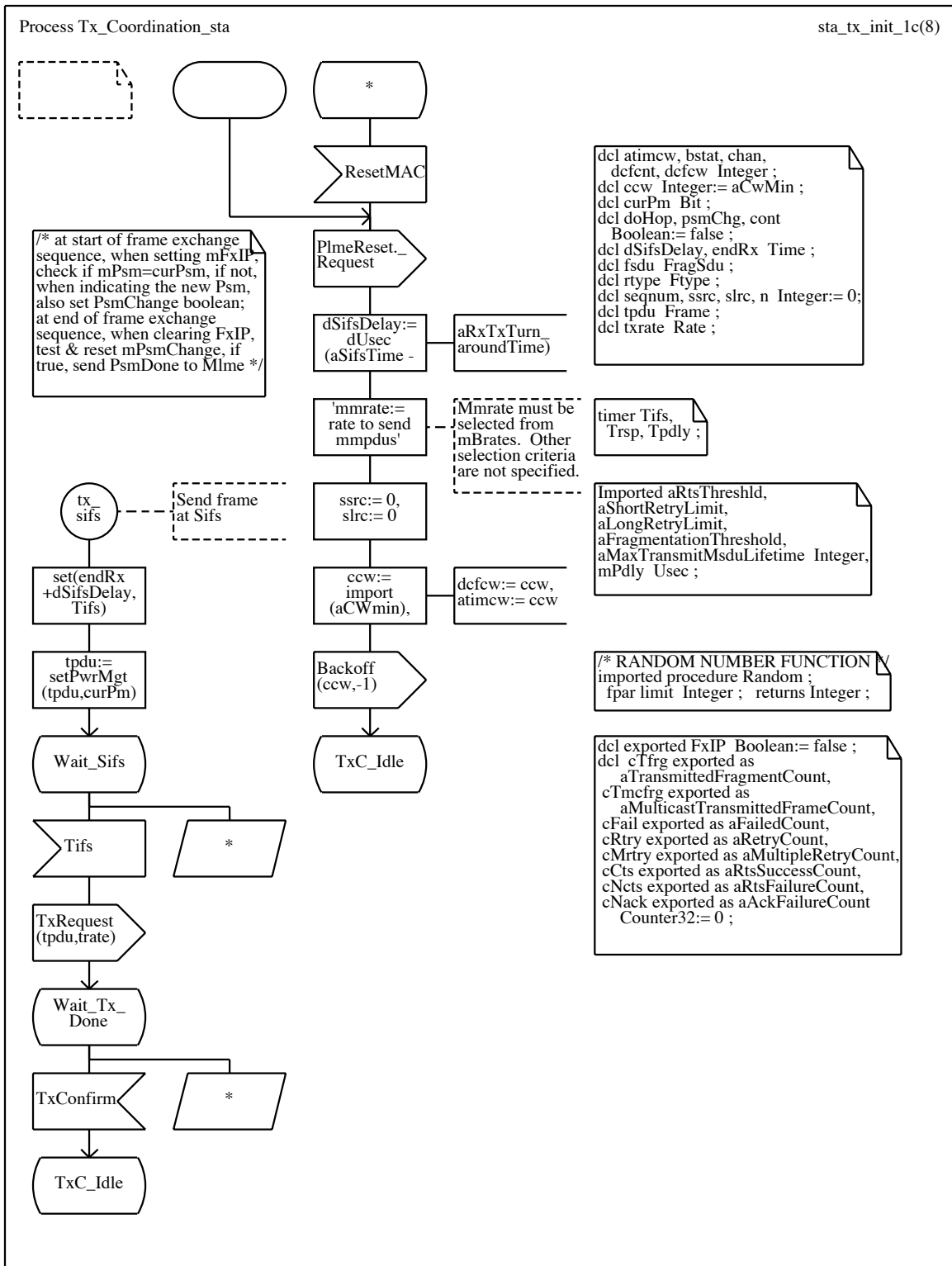


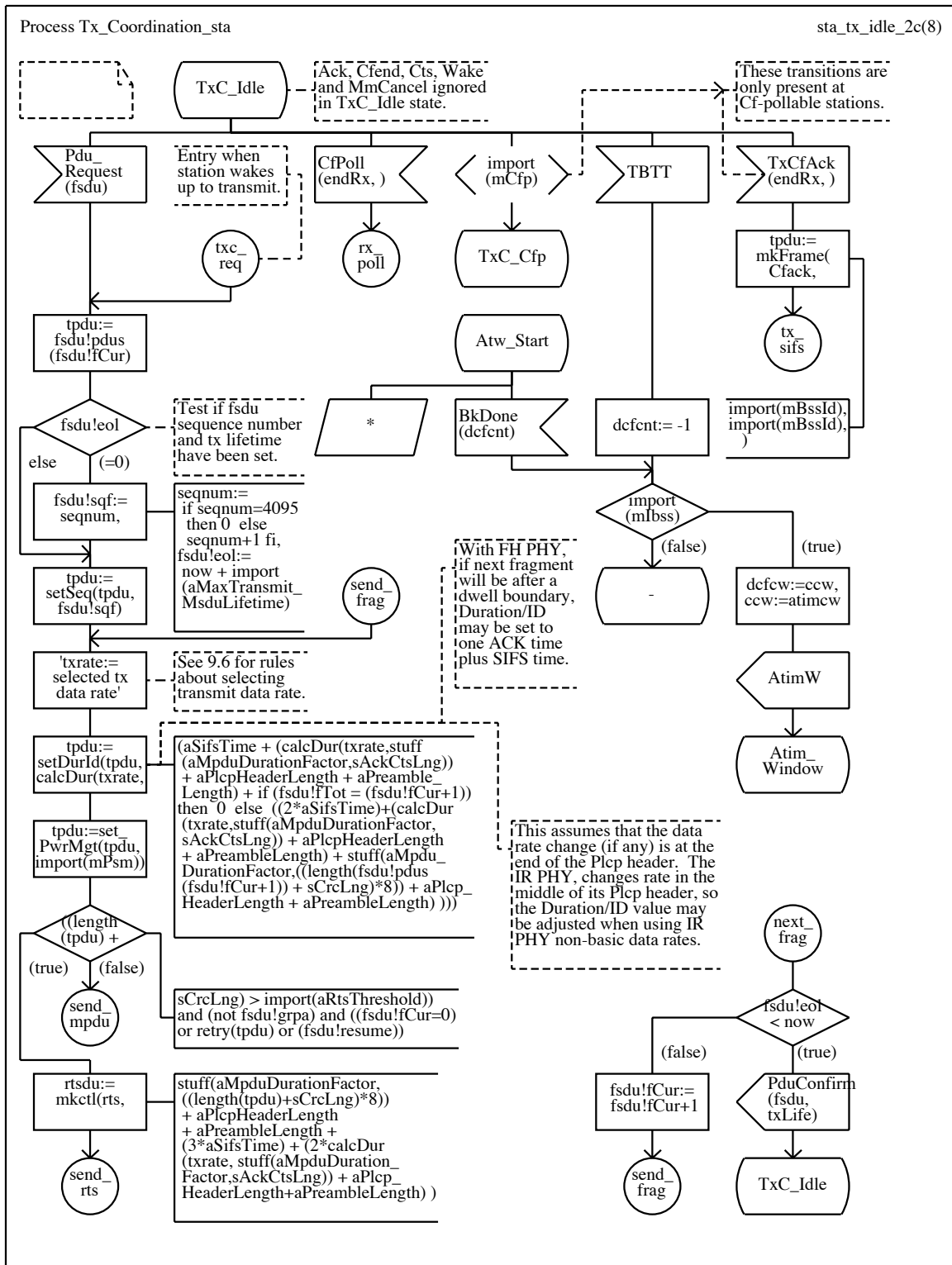


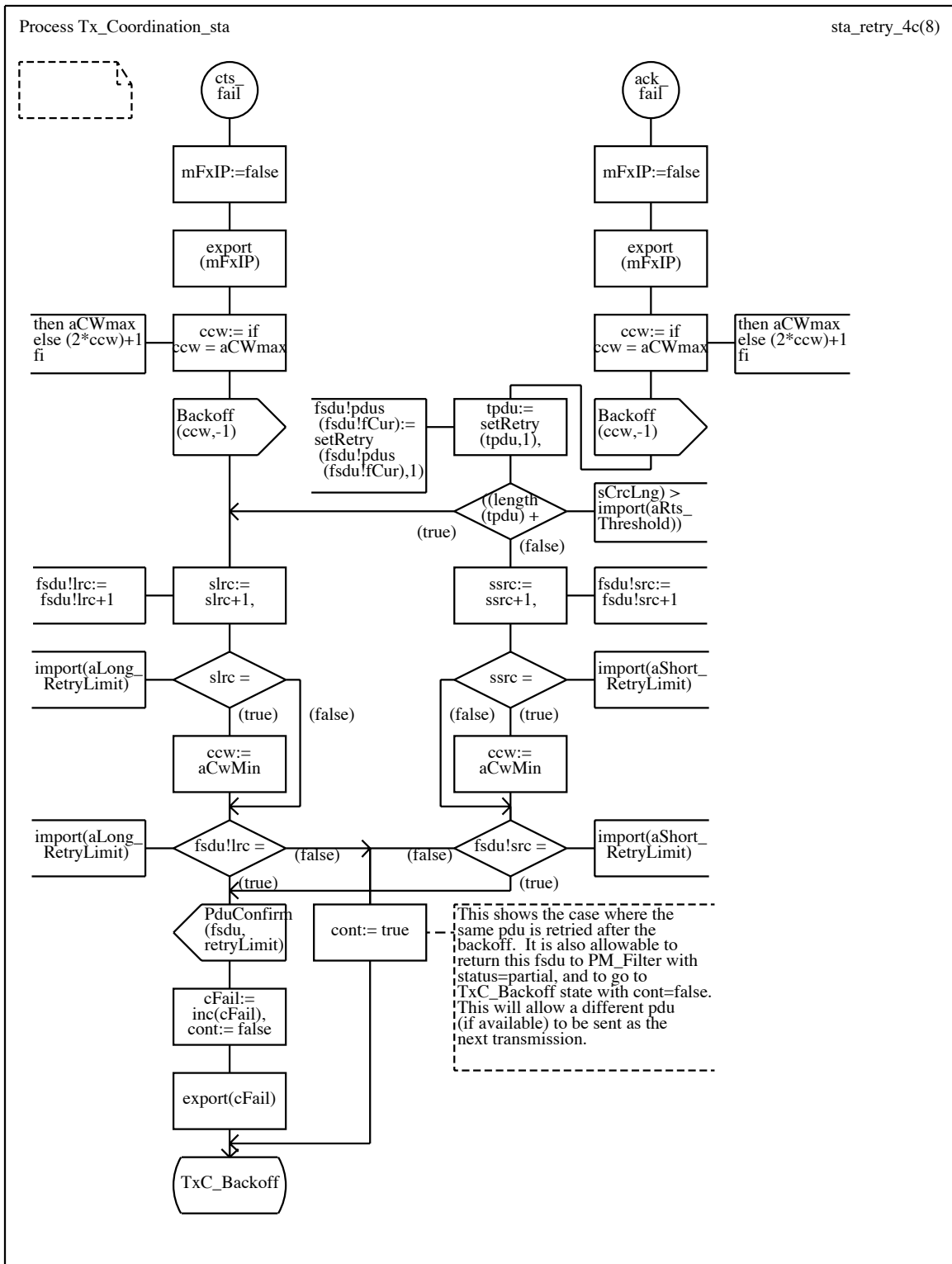


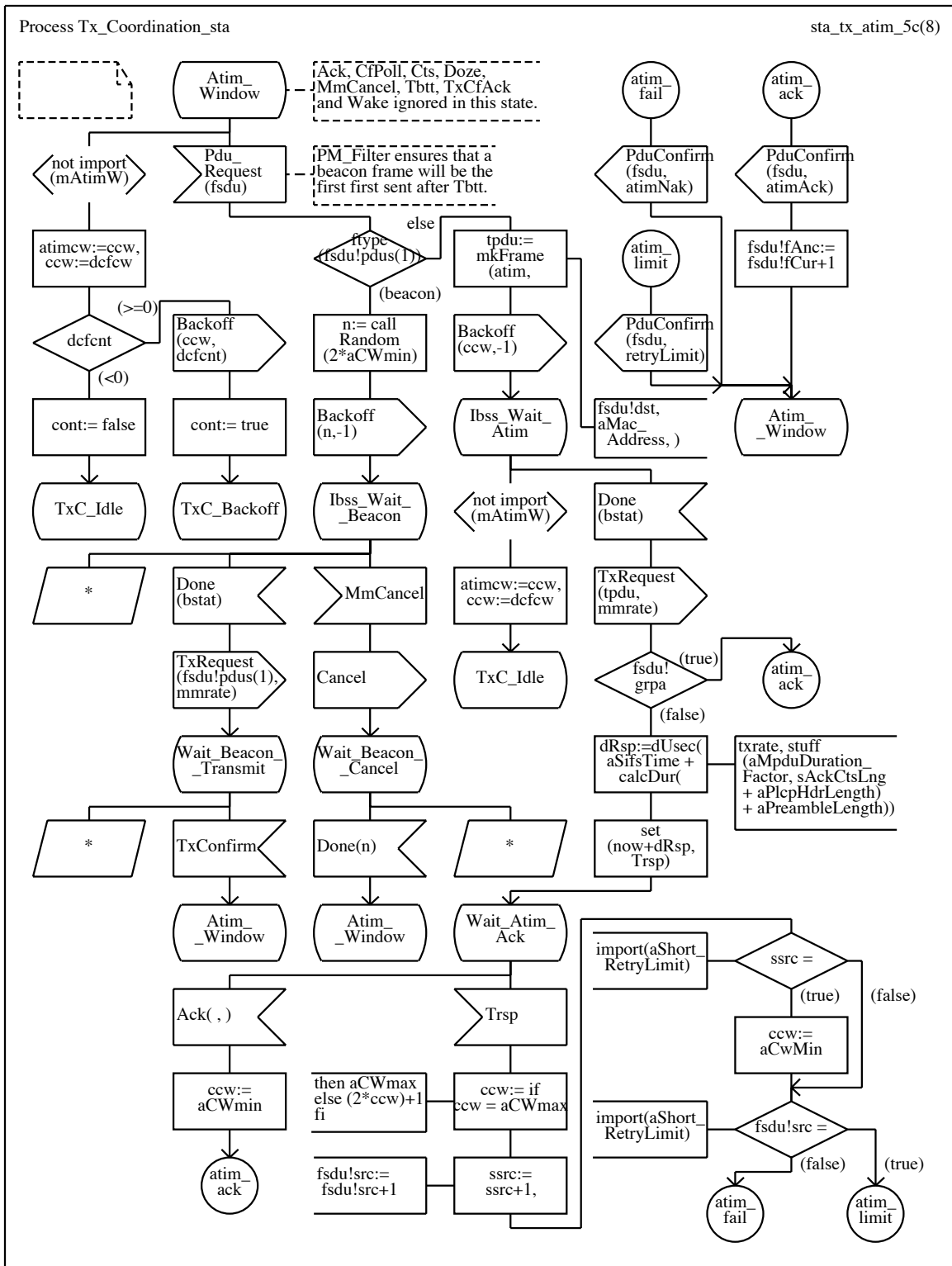


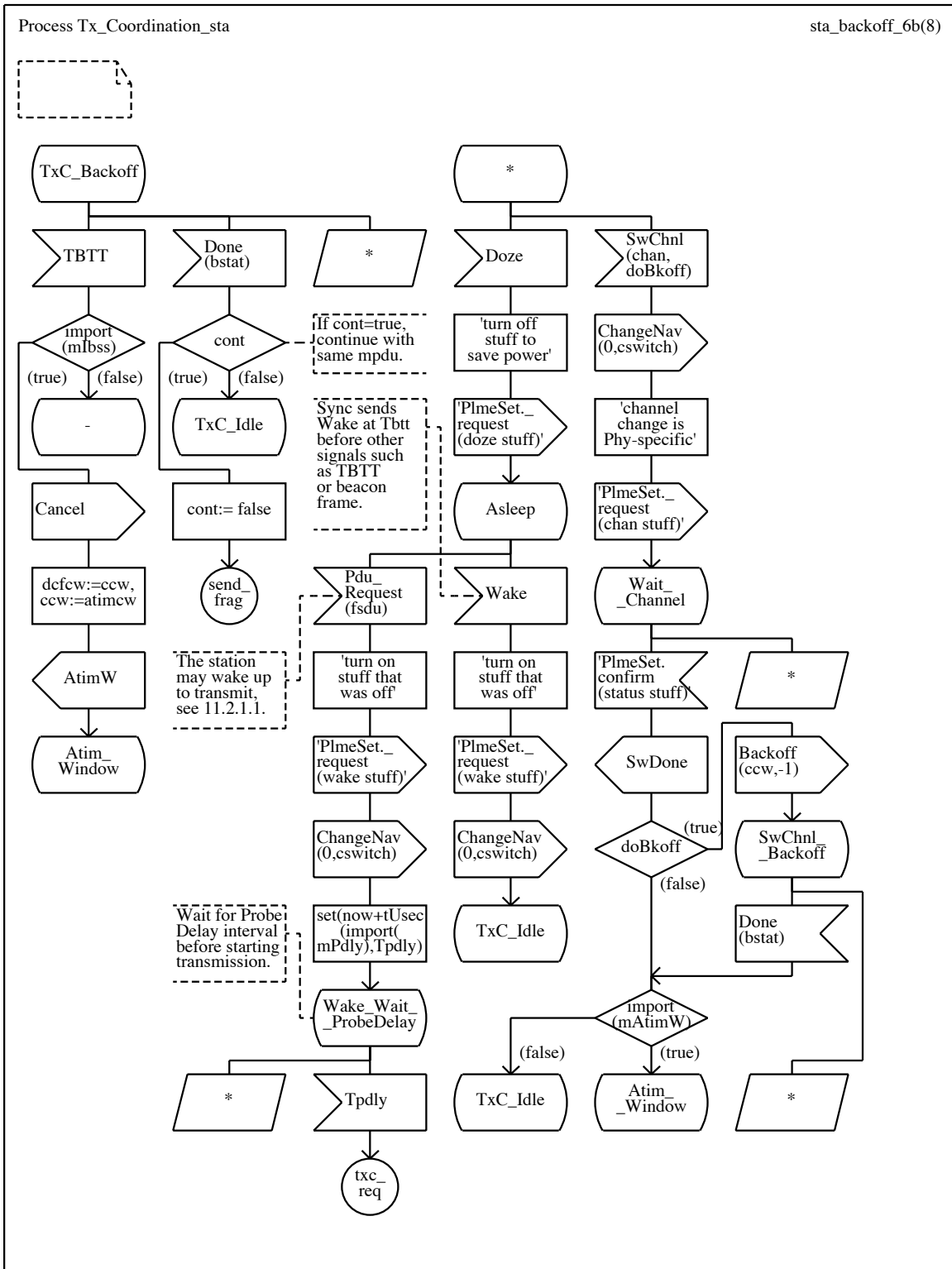


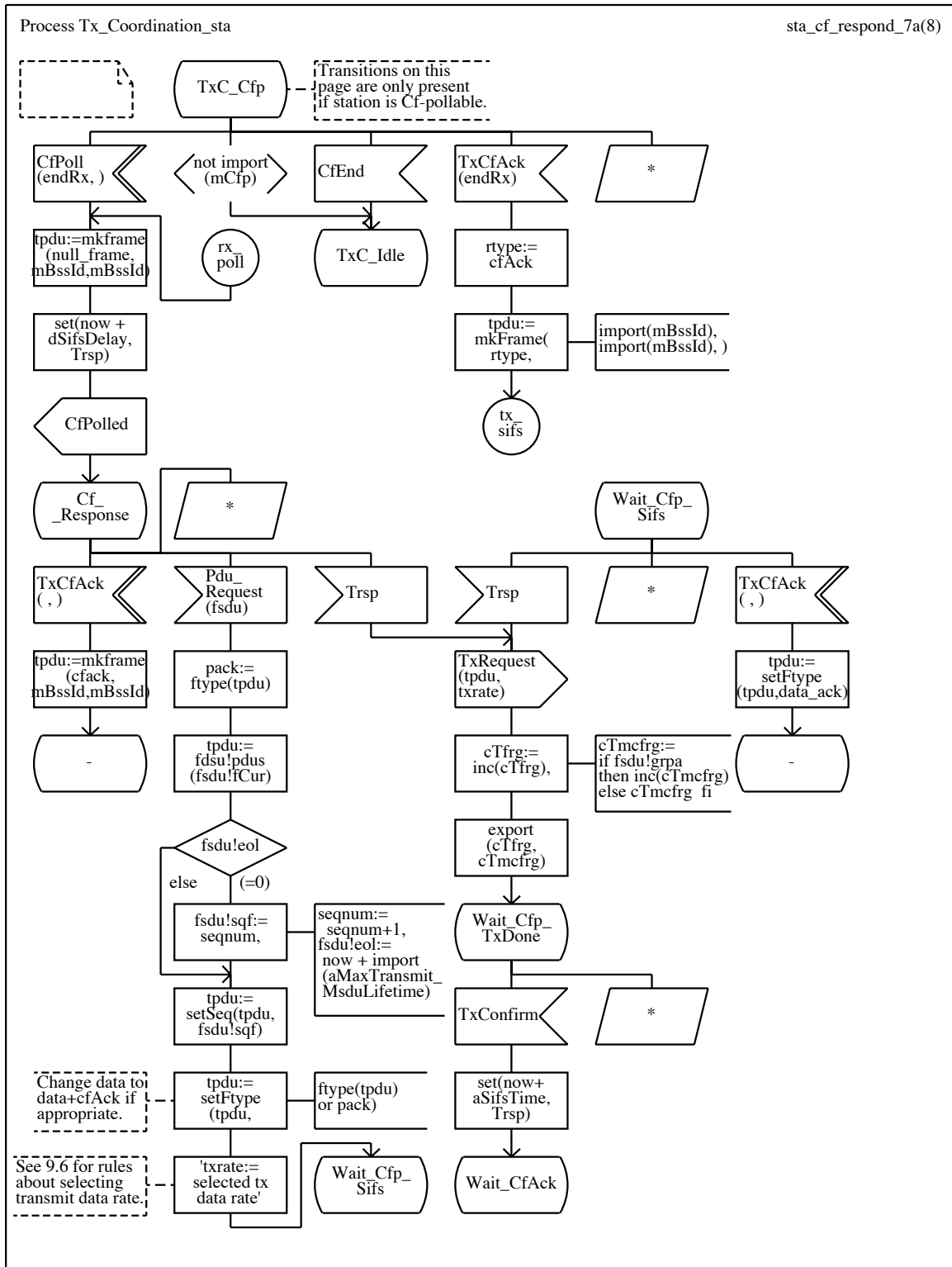


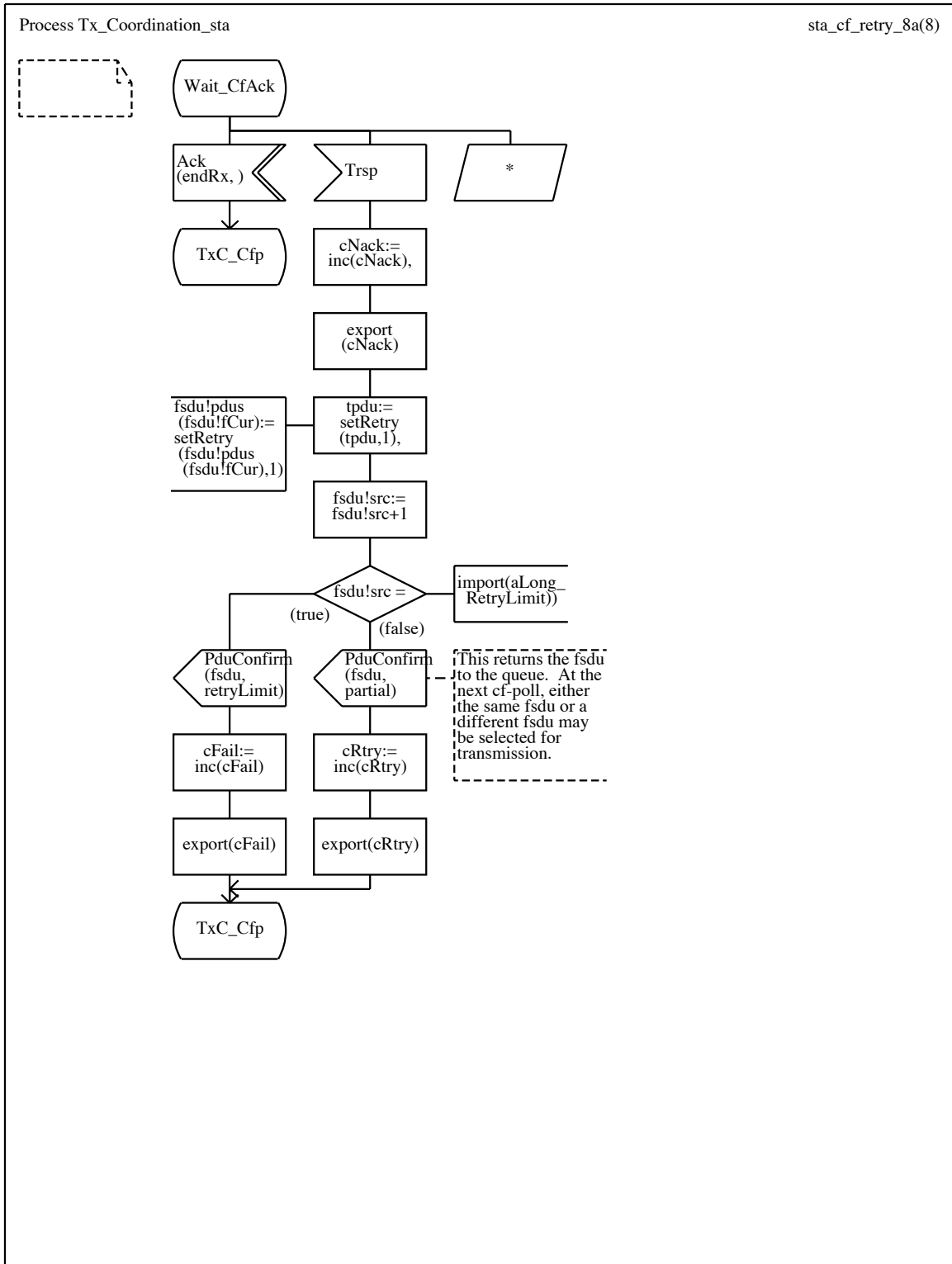


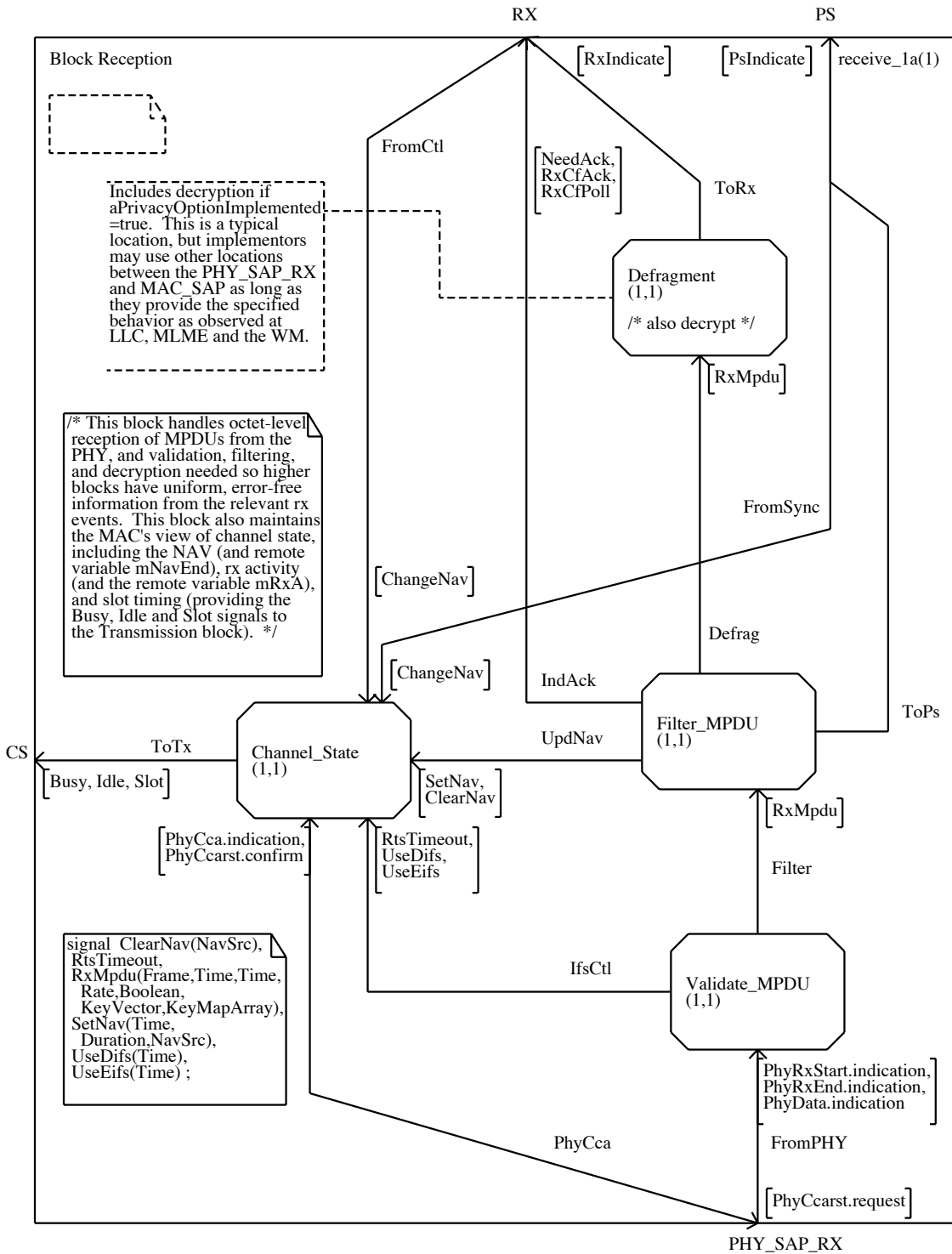


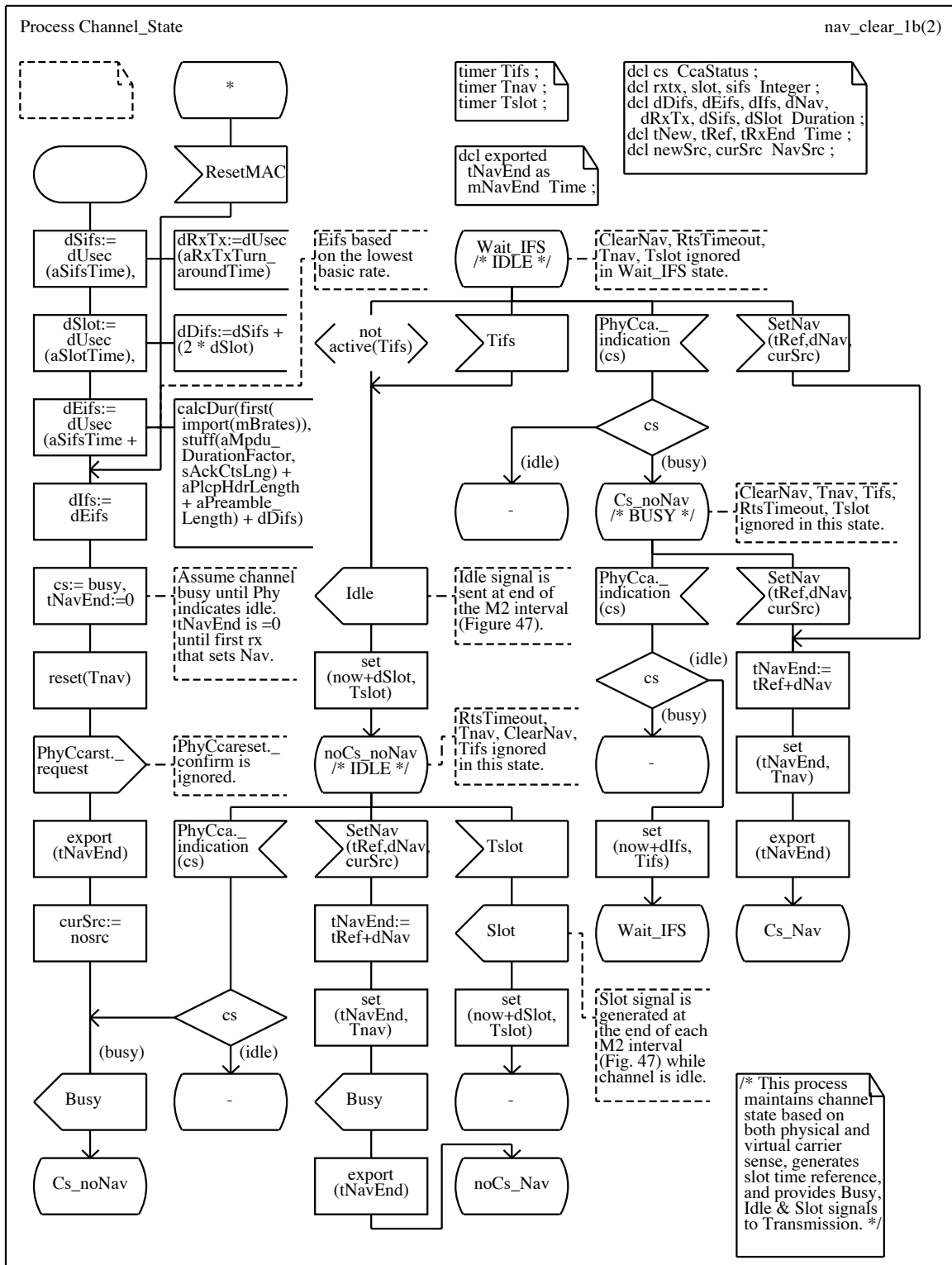


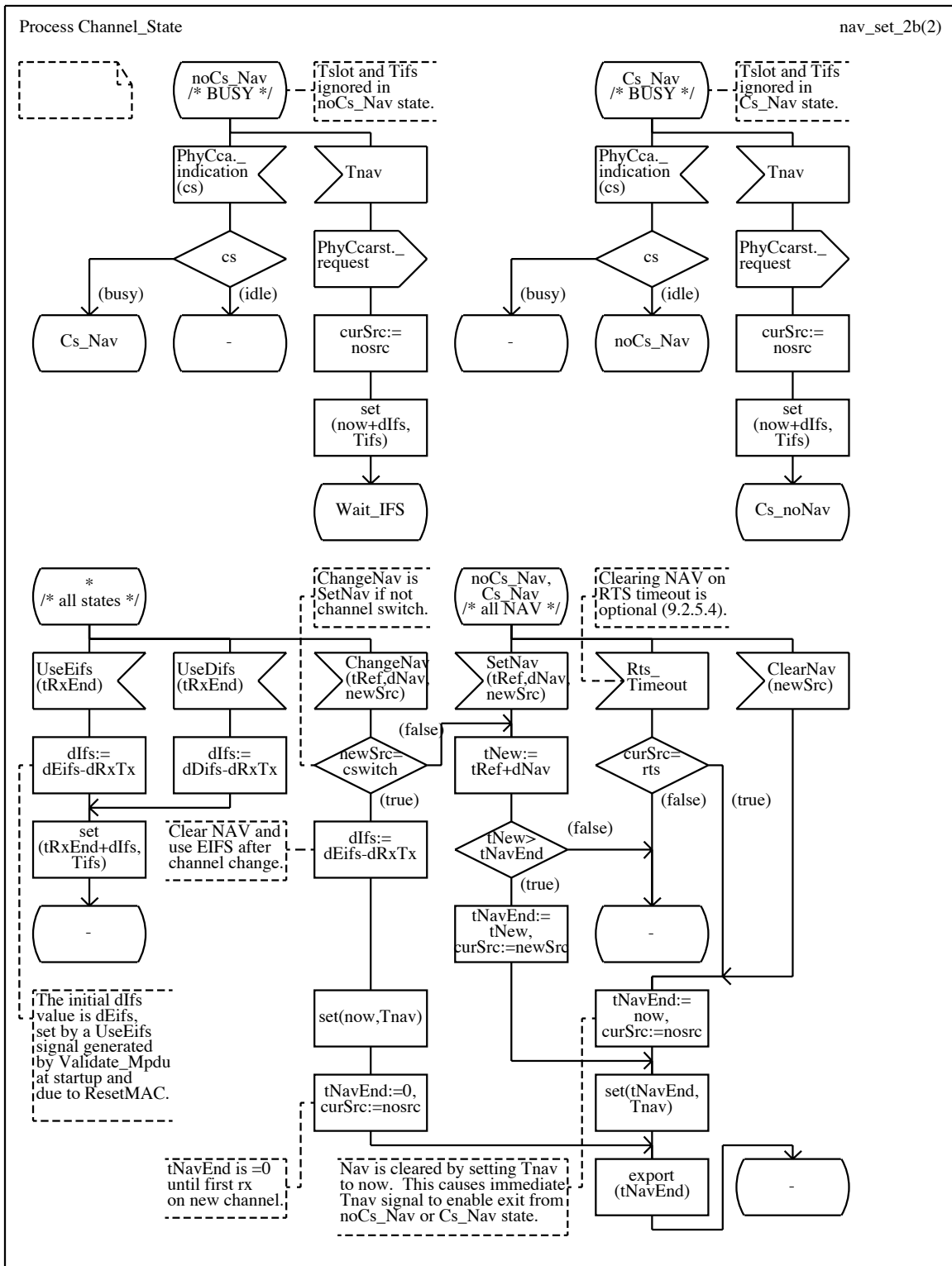


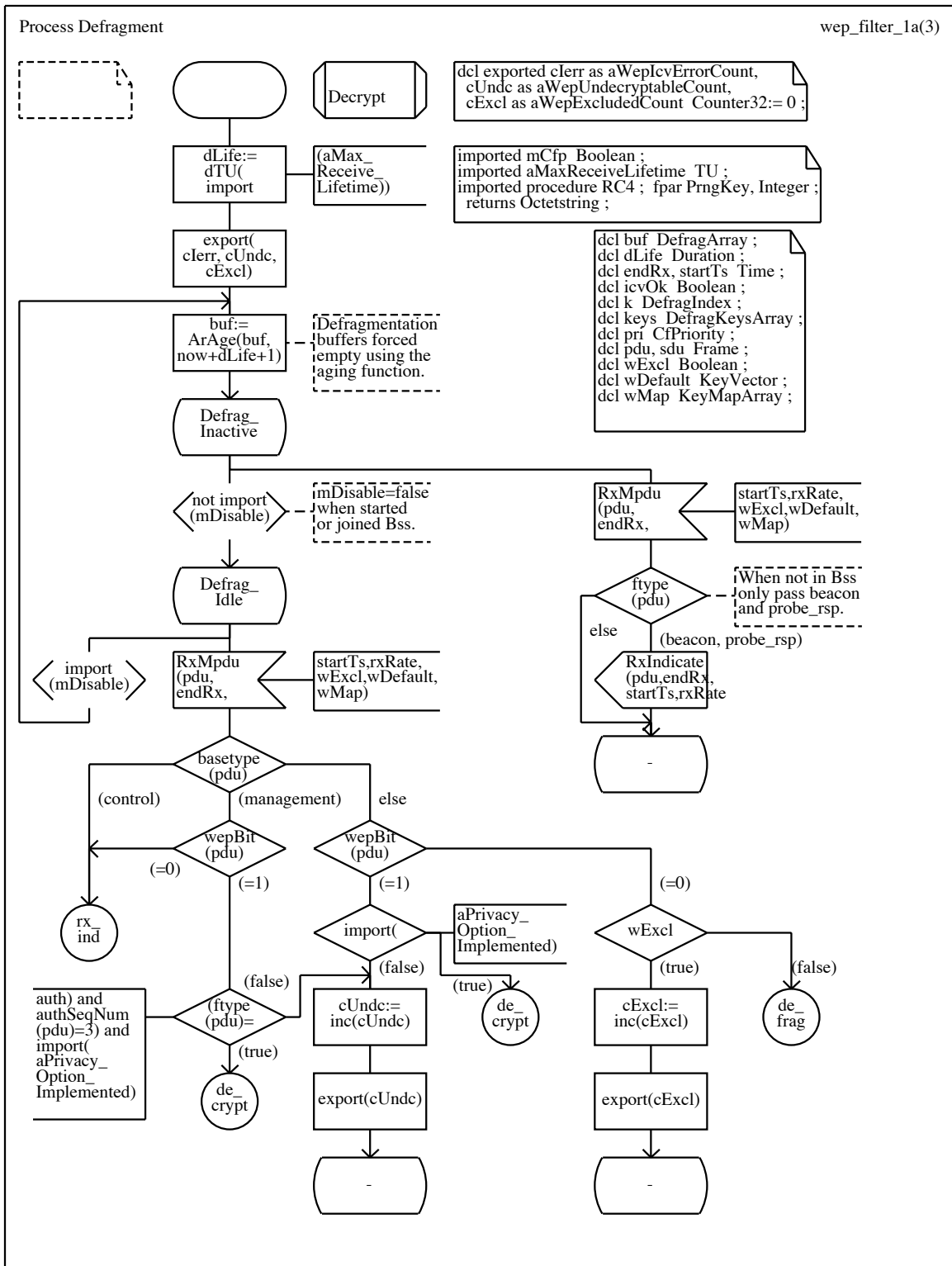


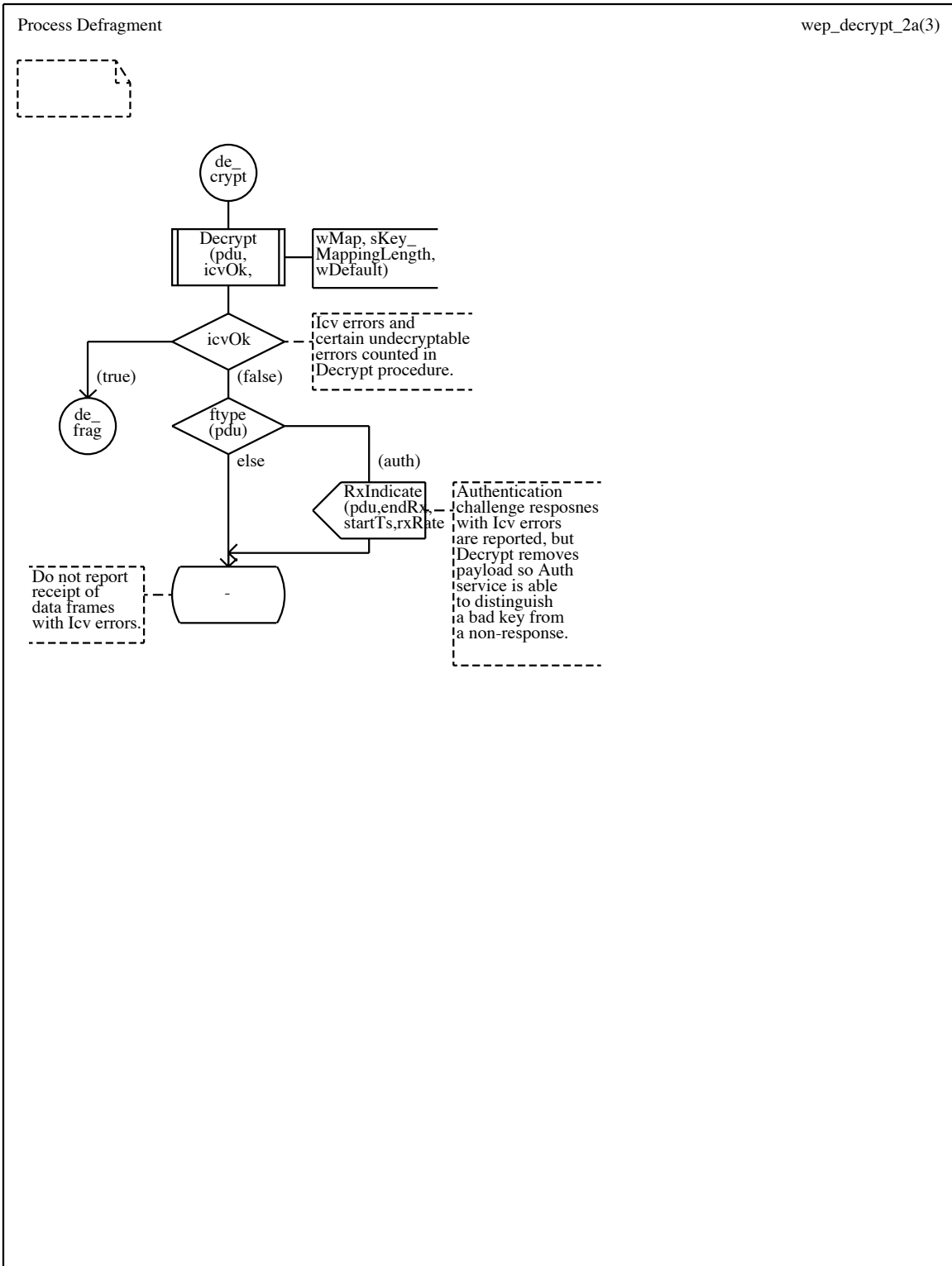


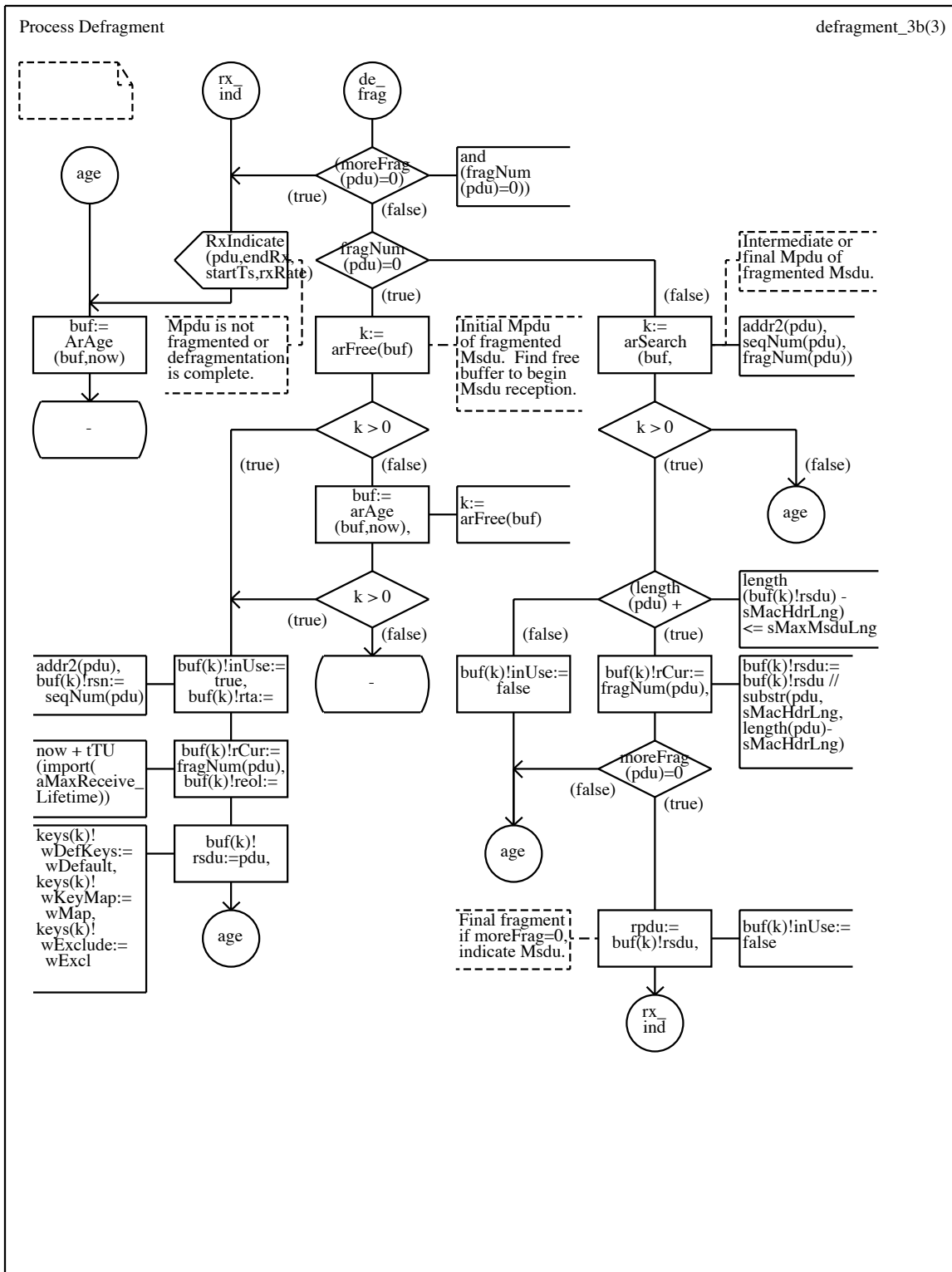


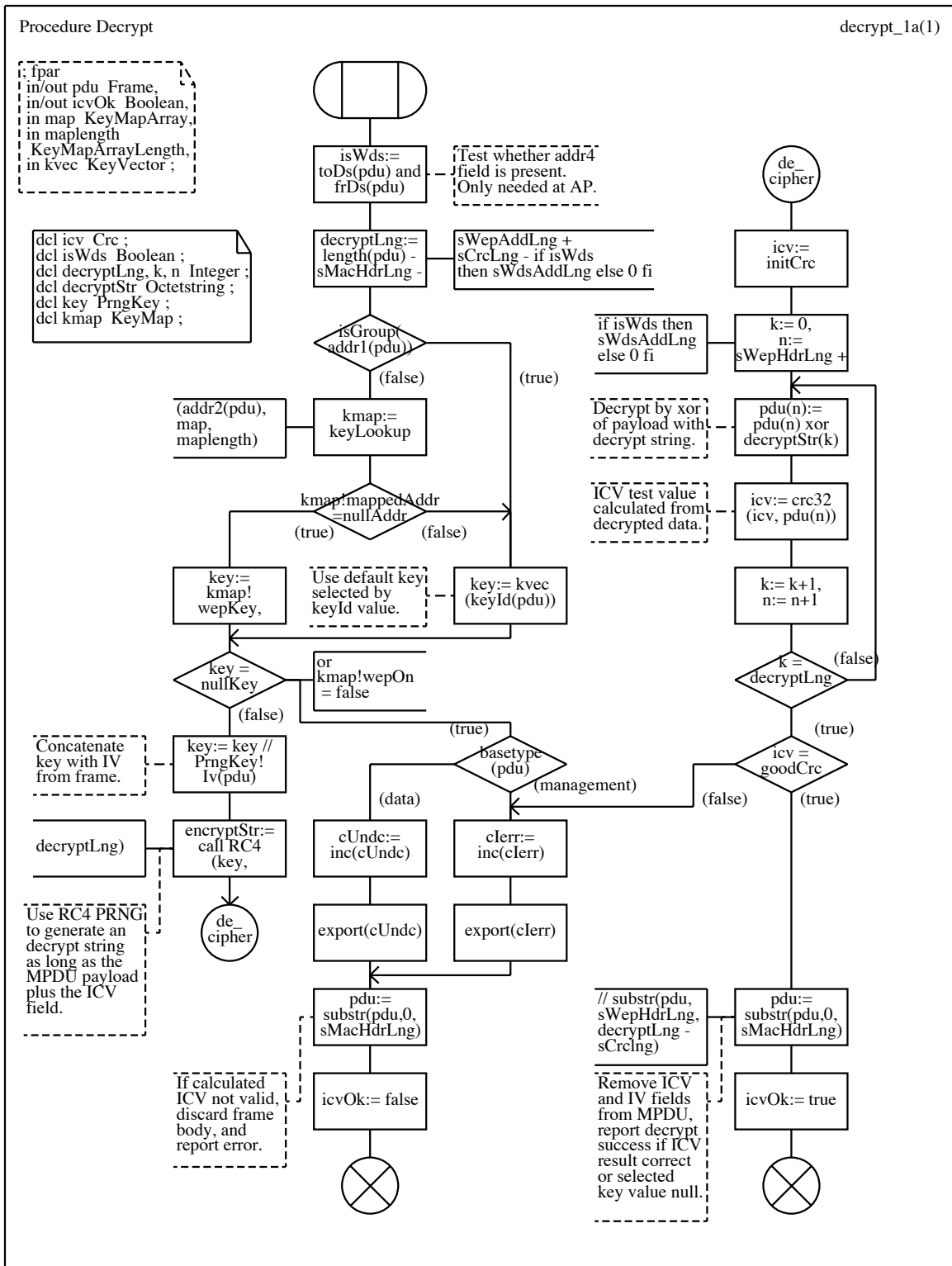


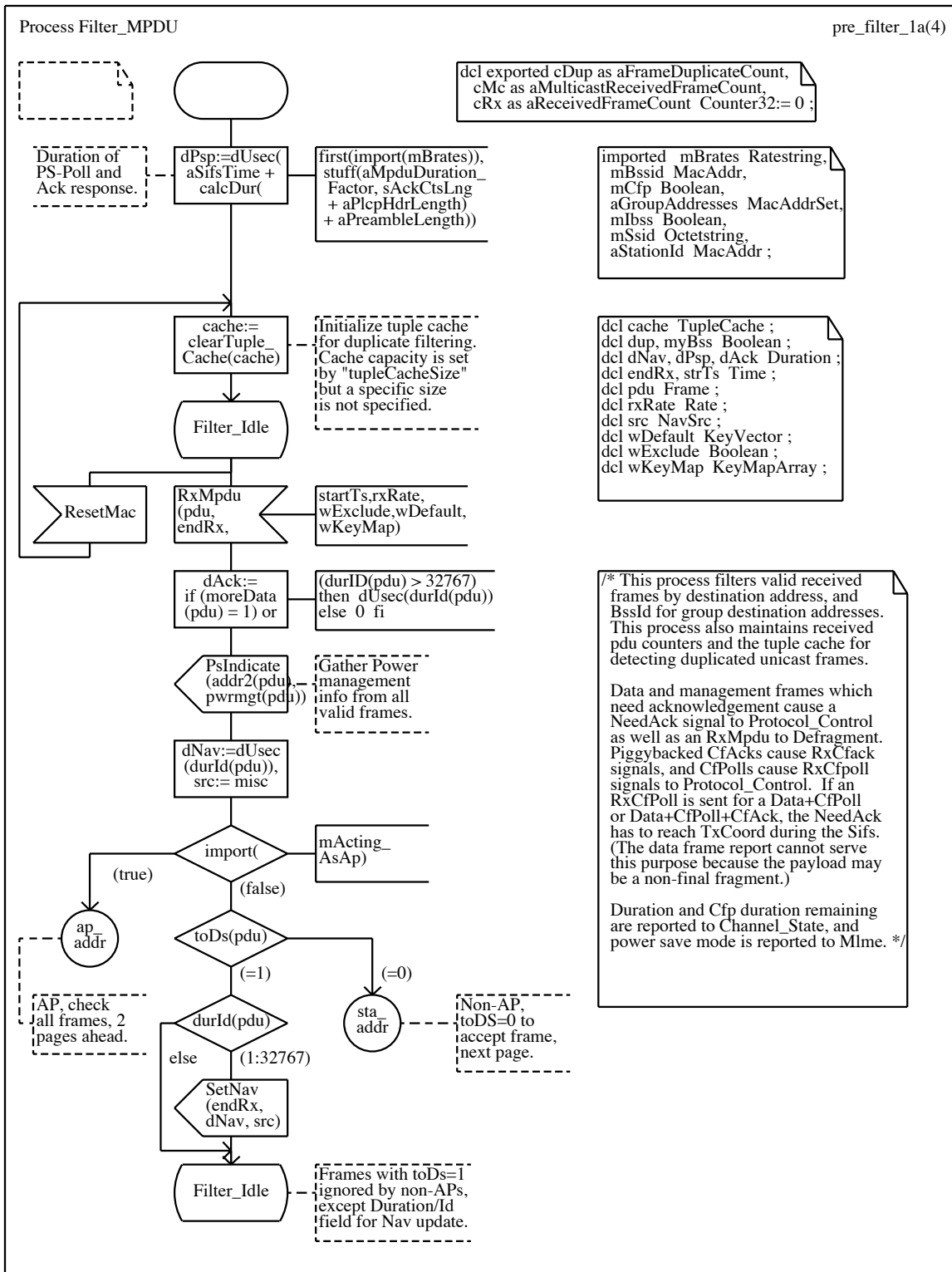


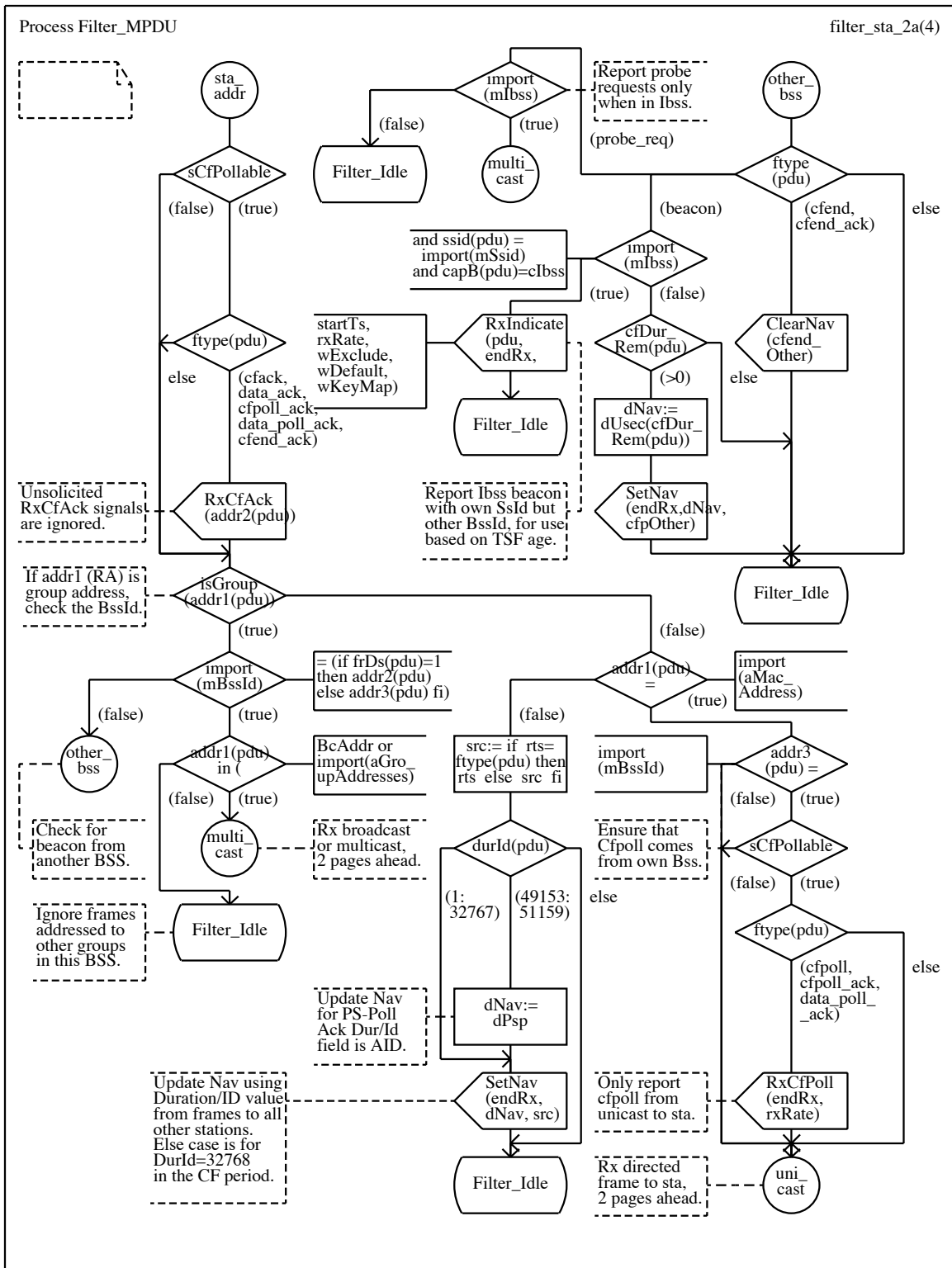


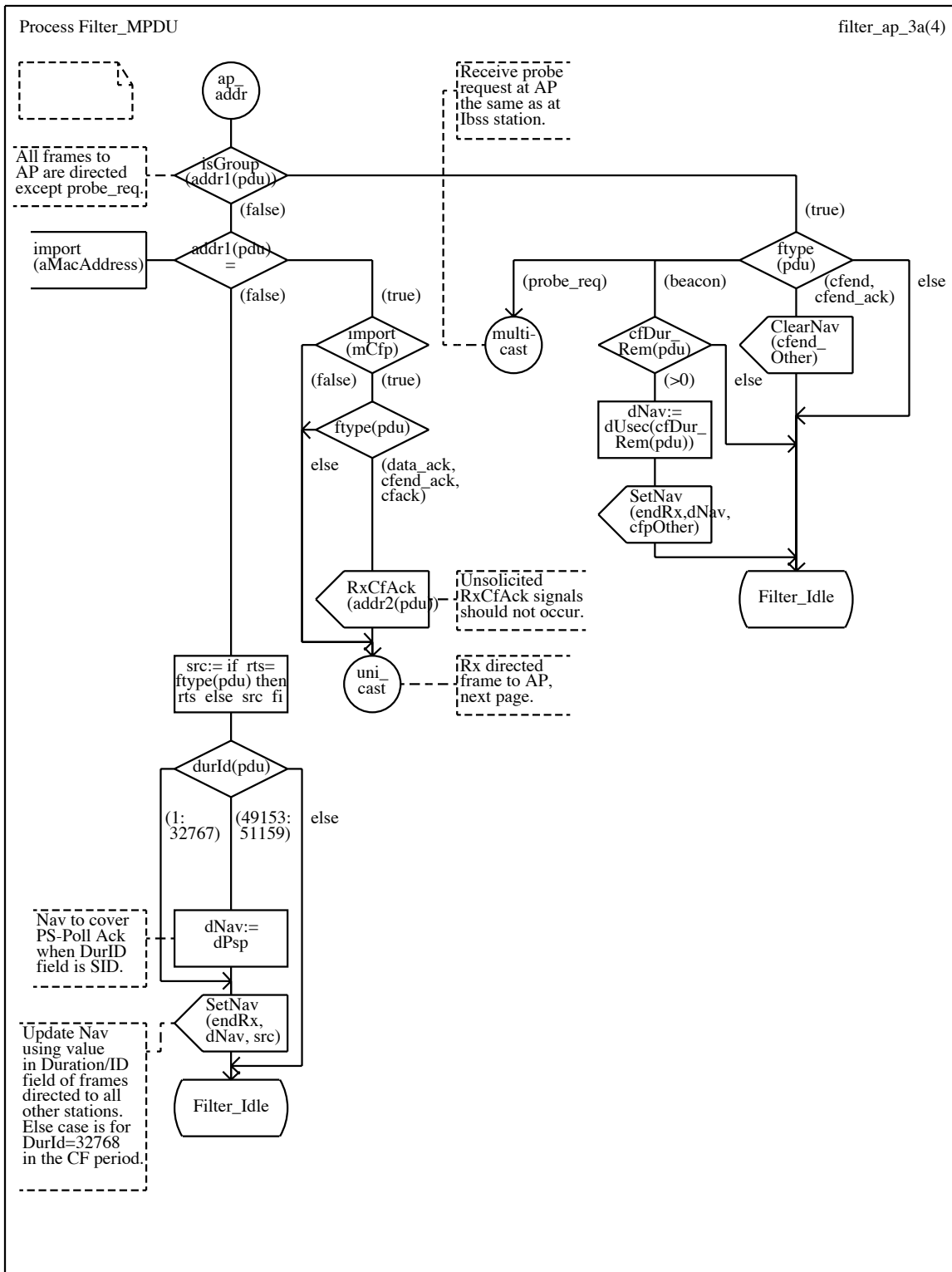


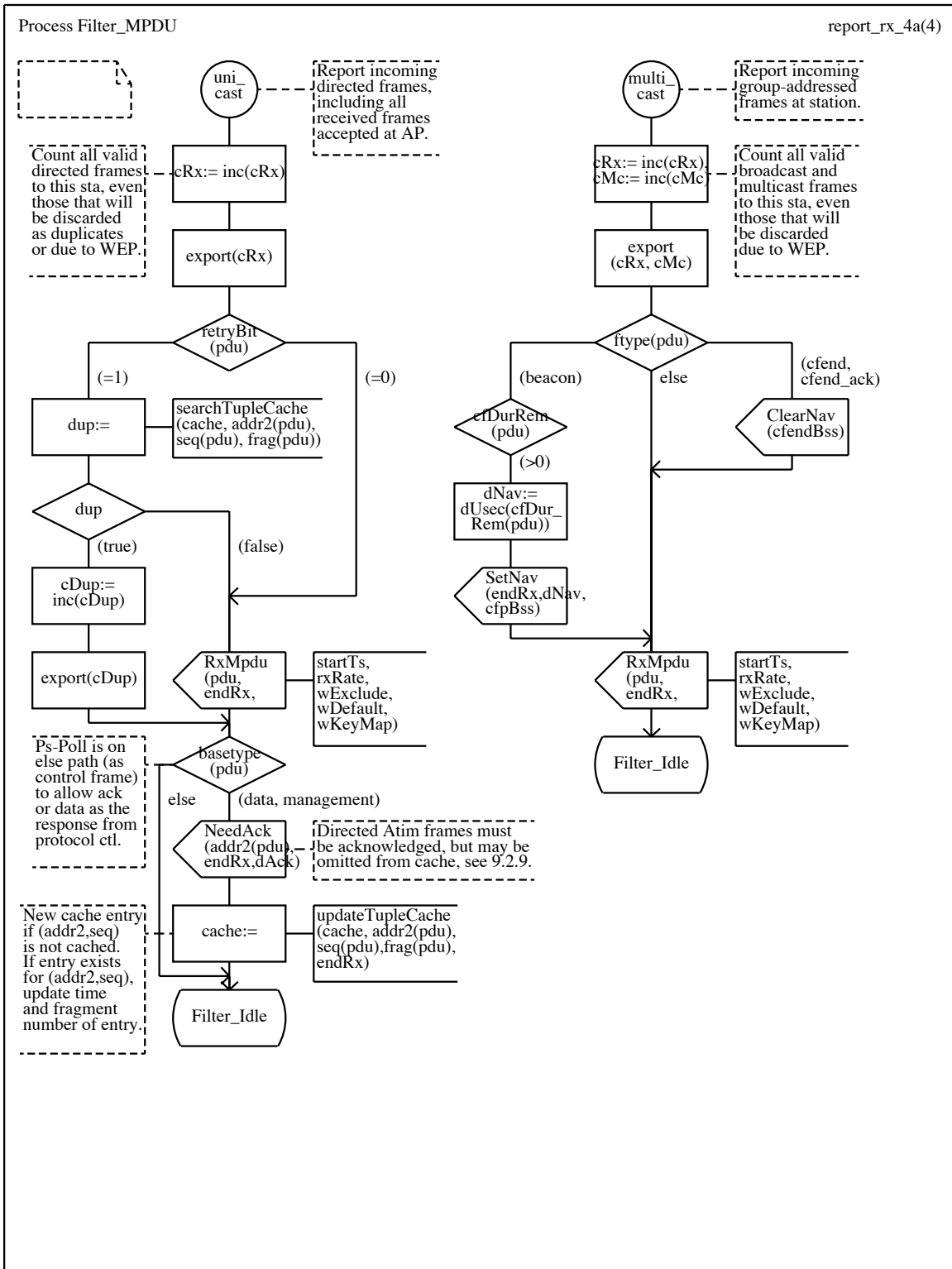


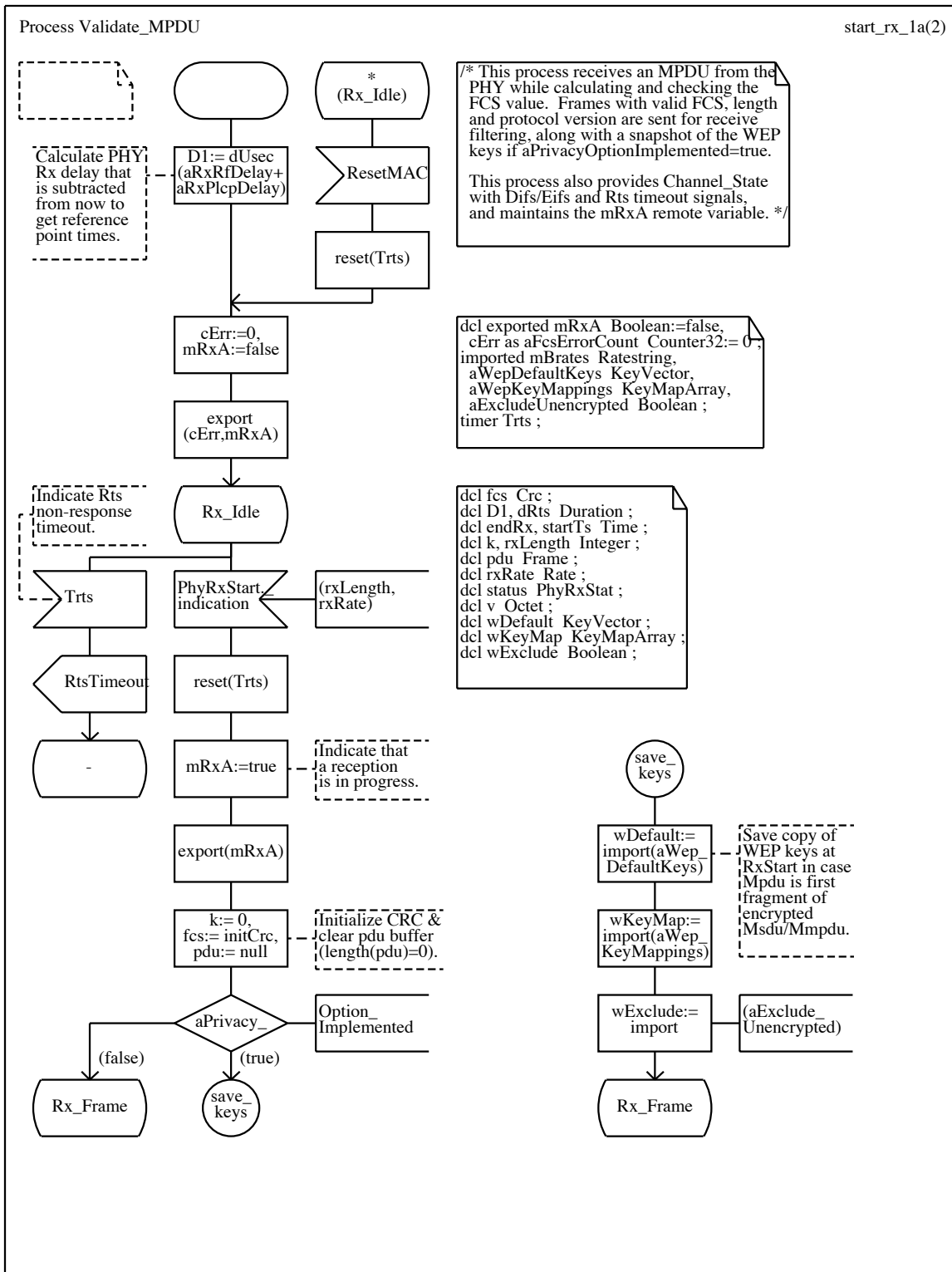


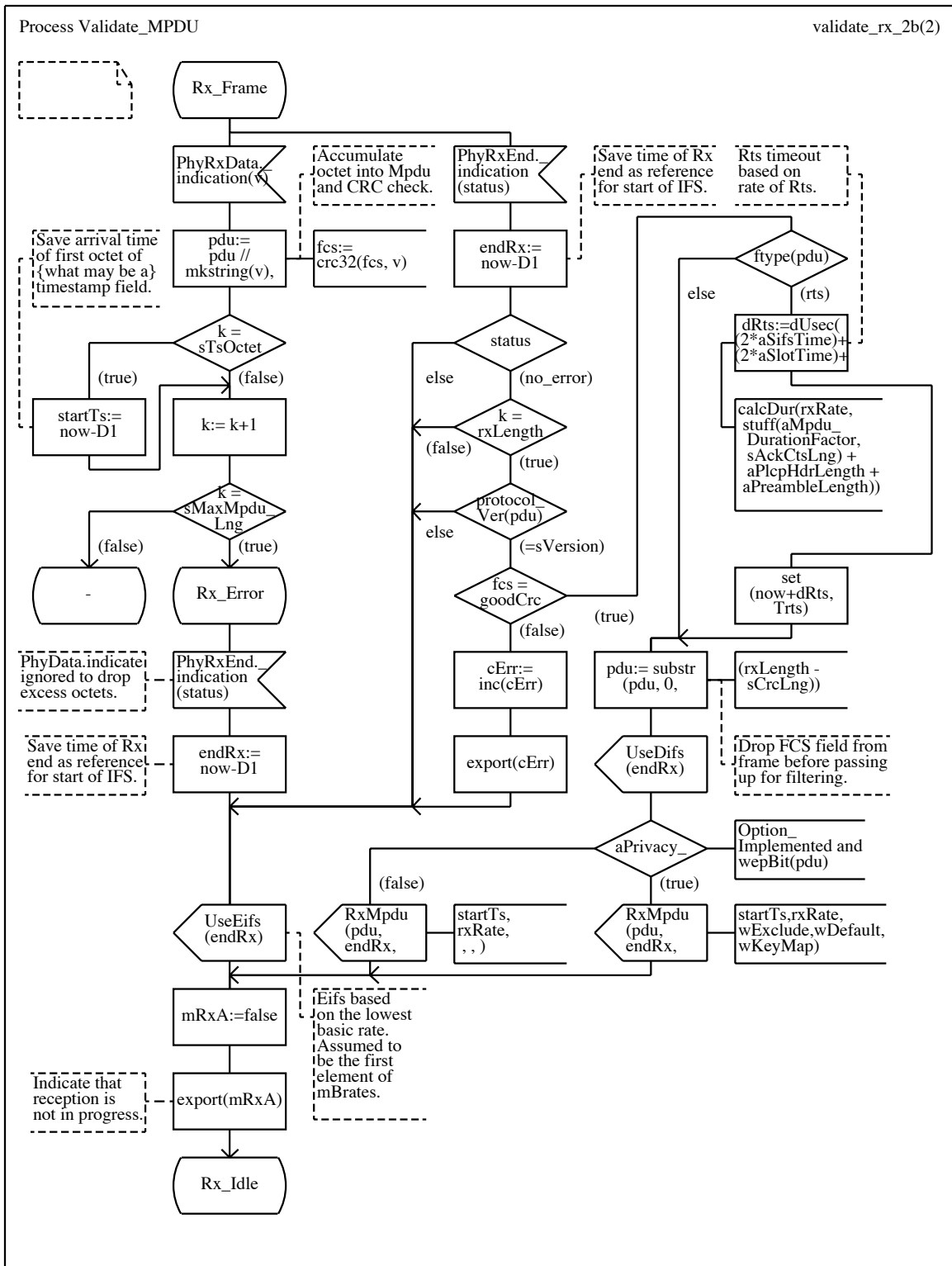


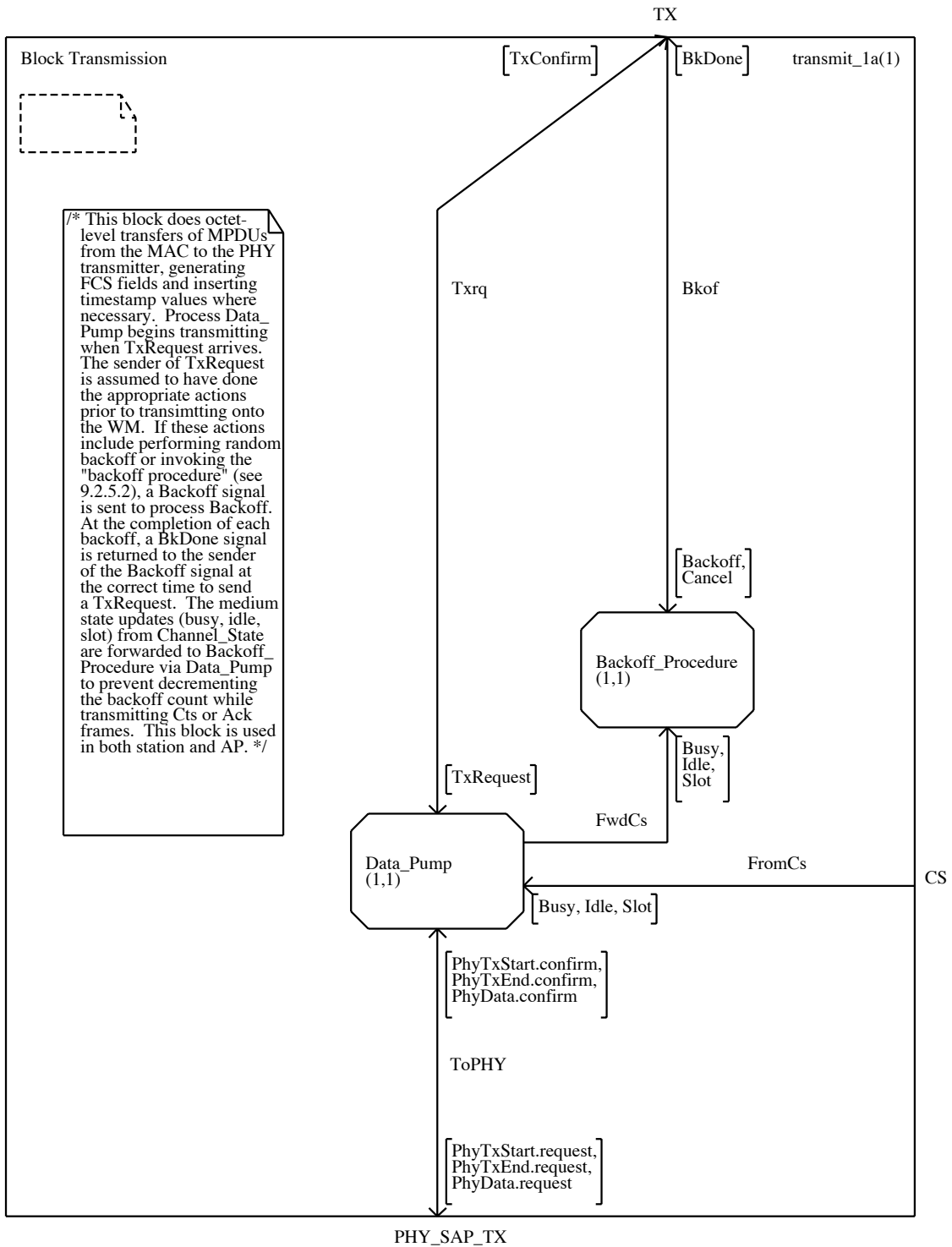


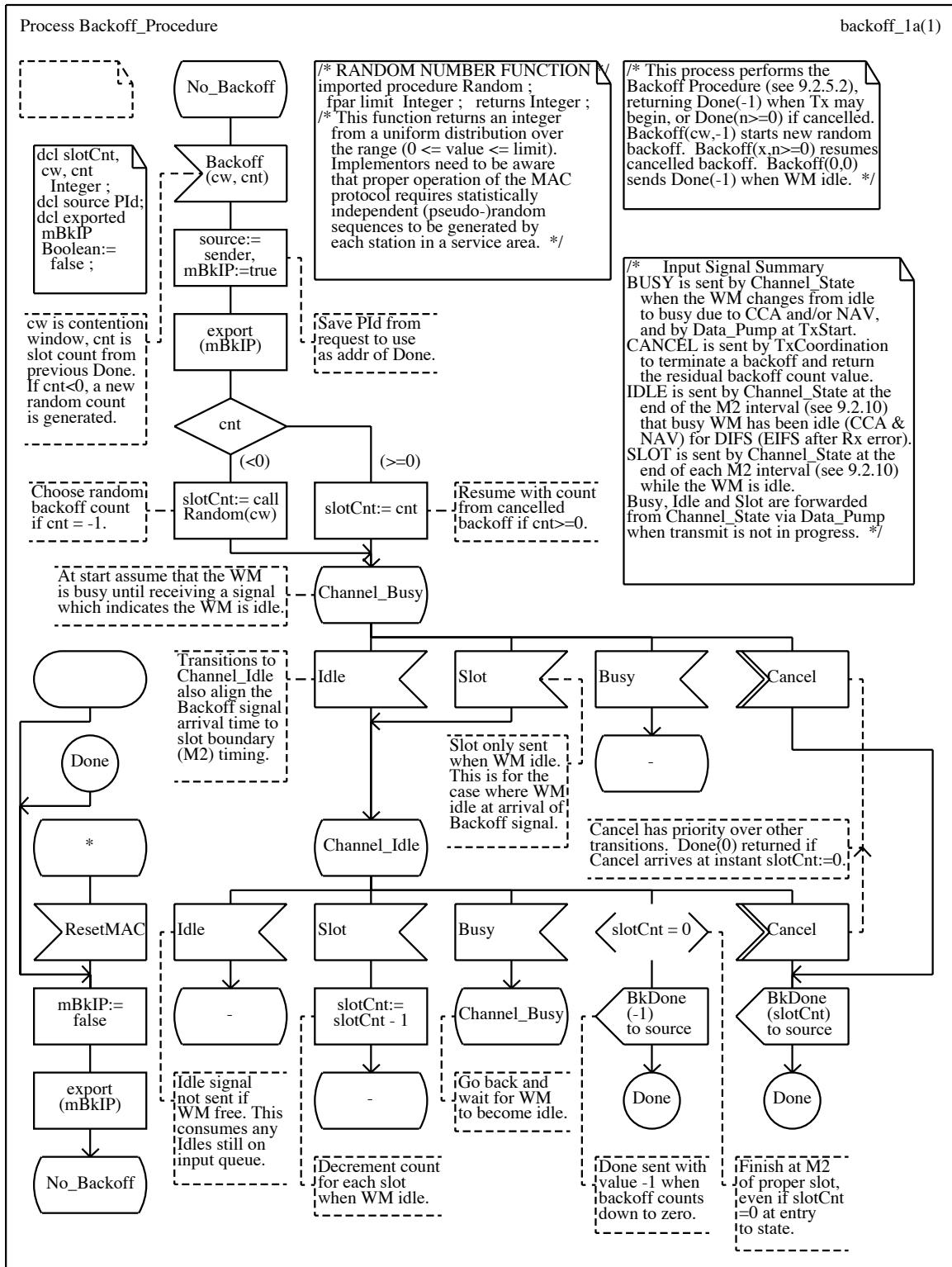


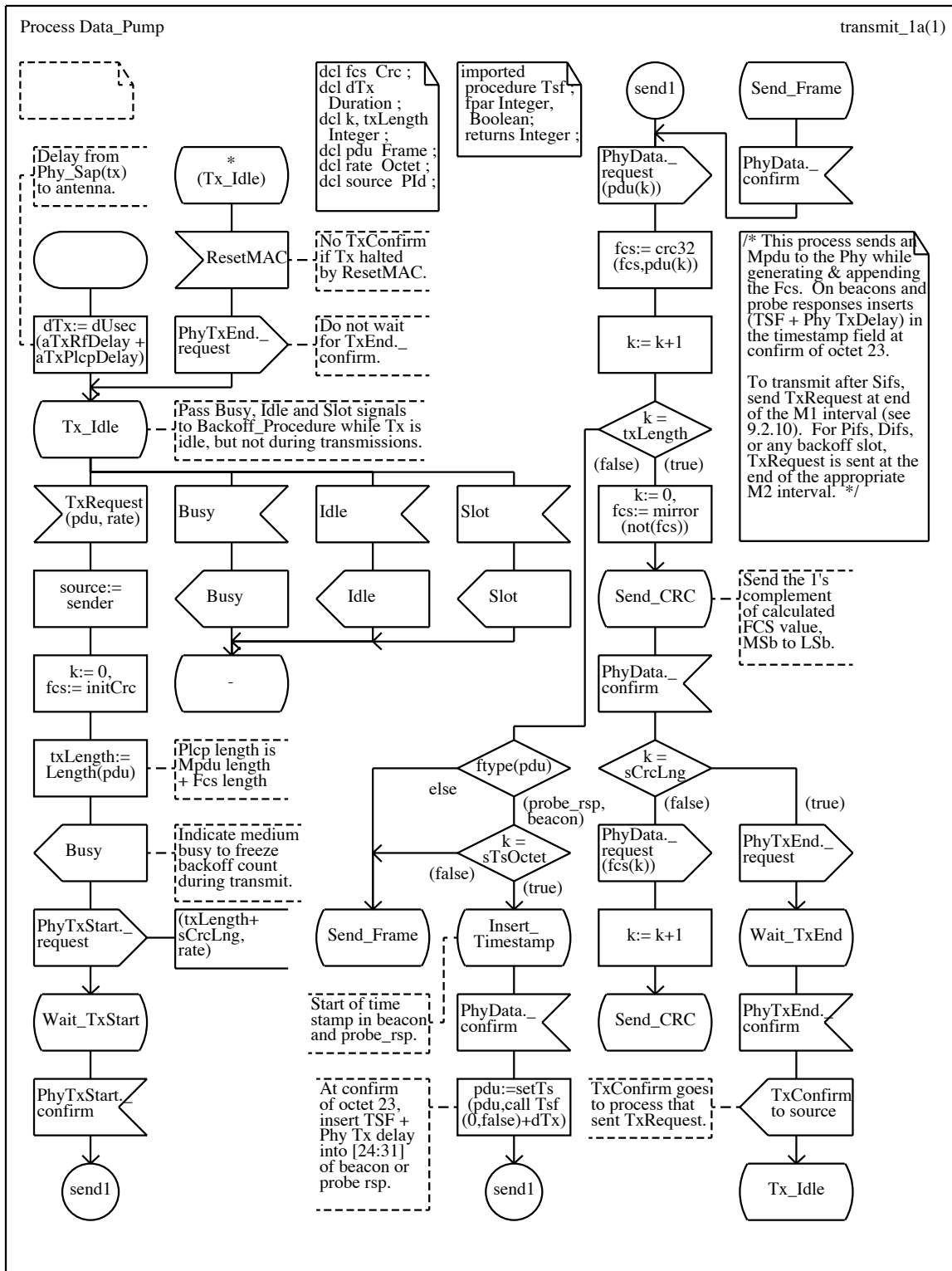








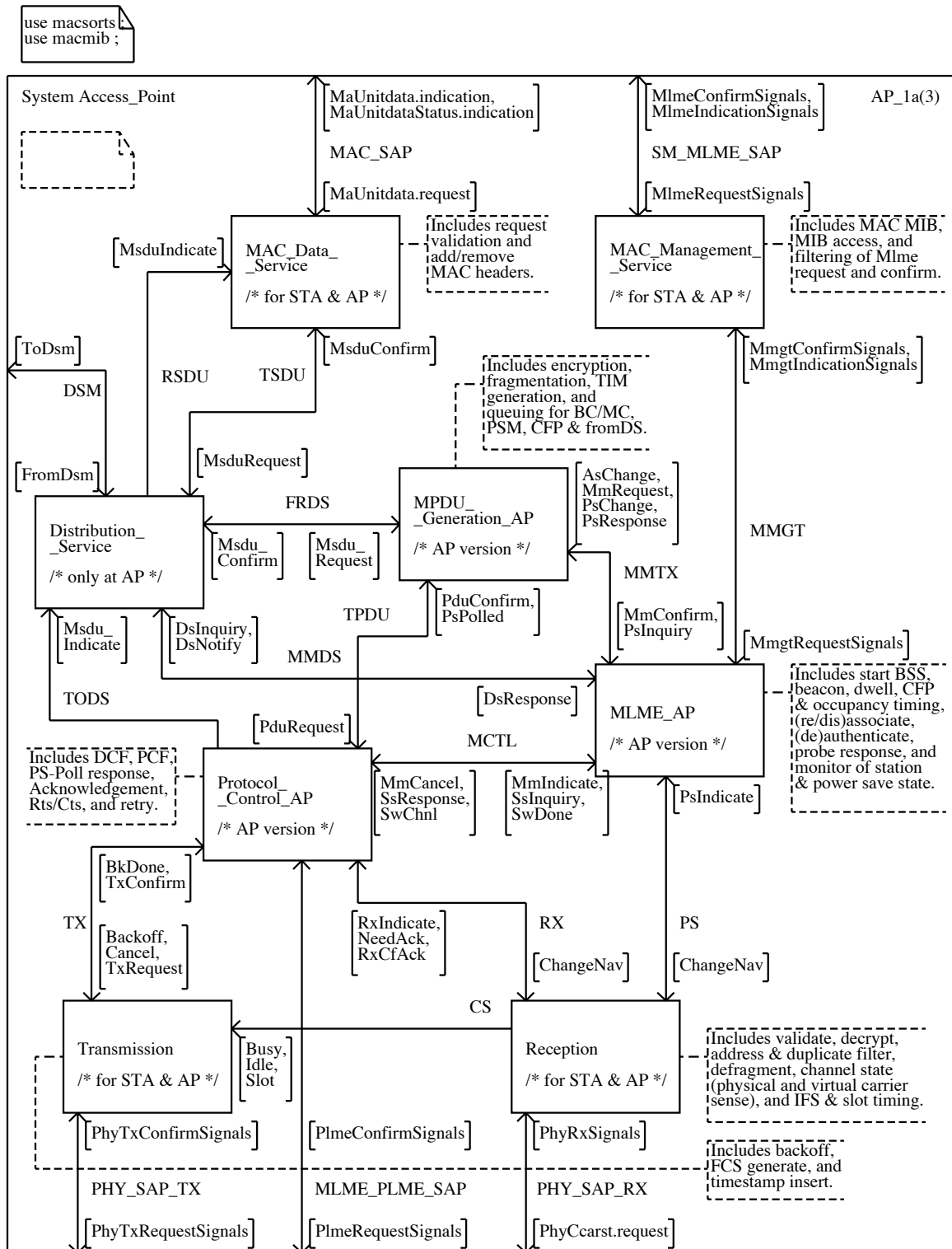




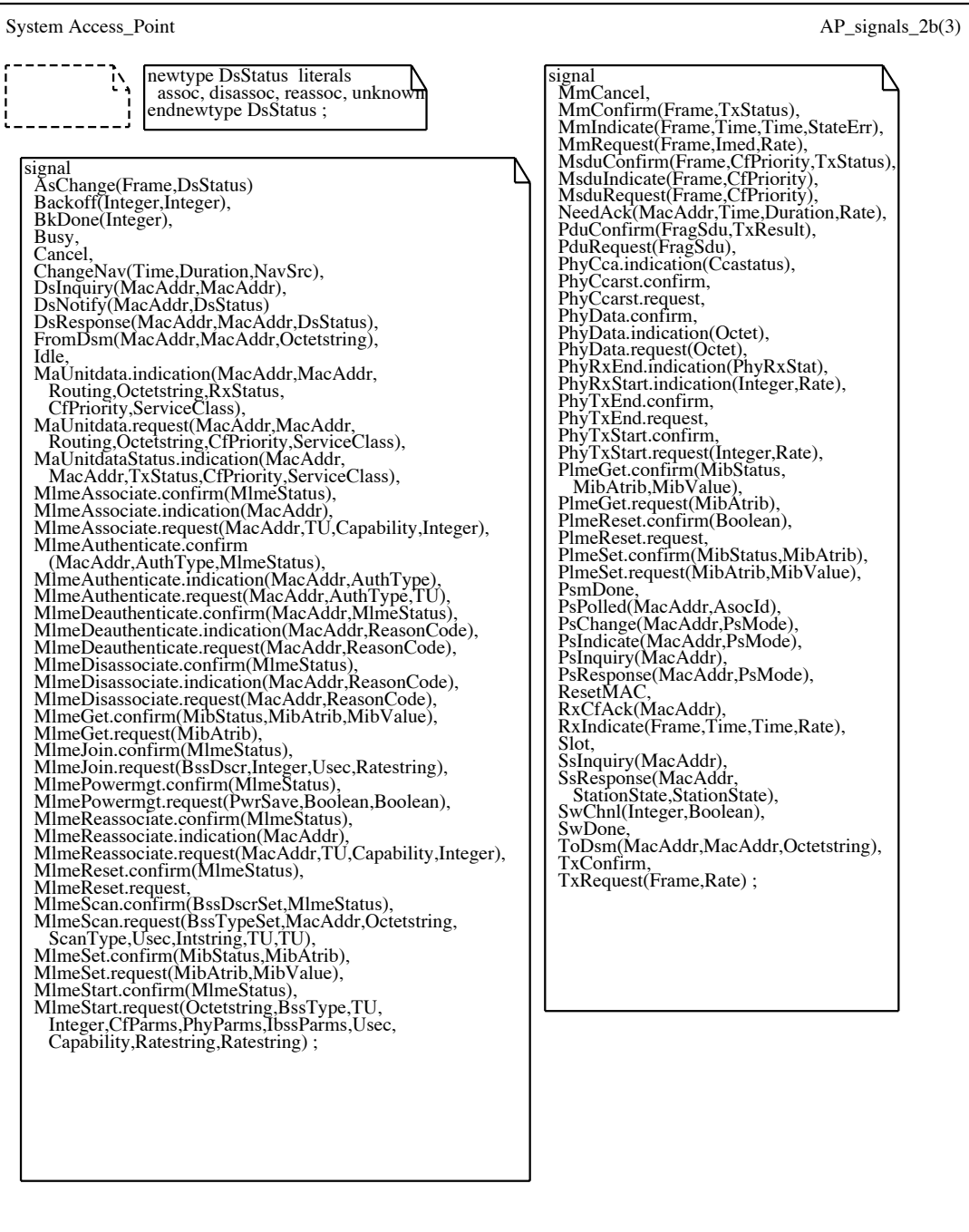
C.4 State machines for MAC access point

The following SDL-92 system specification defines operation of the MAC protocol at an IEEE 802.11 AP. Many aspects of AP operation are identical to STA operation. These are defined in blocks and processes referenced from both the STA and AP system specifications. Blocks and processes used in both STA and AP are identifiable by the SDL comment */* for STA & AP */* below the block or process name. The definitions of these blocks and processes appear in Clause C.3. Blocks and processes specific to AP operation are identifiable by the SDL comment */* AP version */* below the block or process name. The definitions of these blocks and processes appear in this subclause.

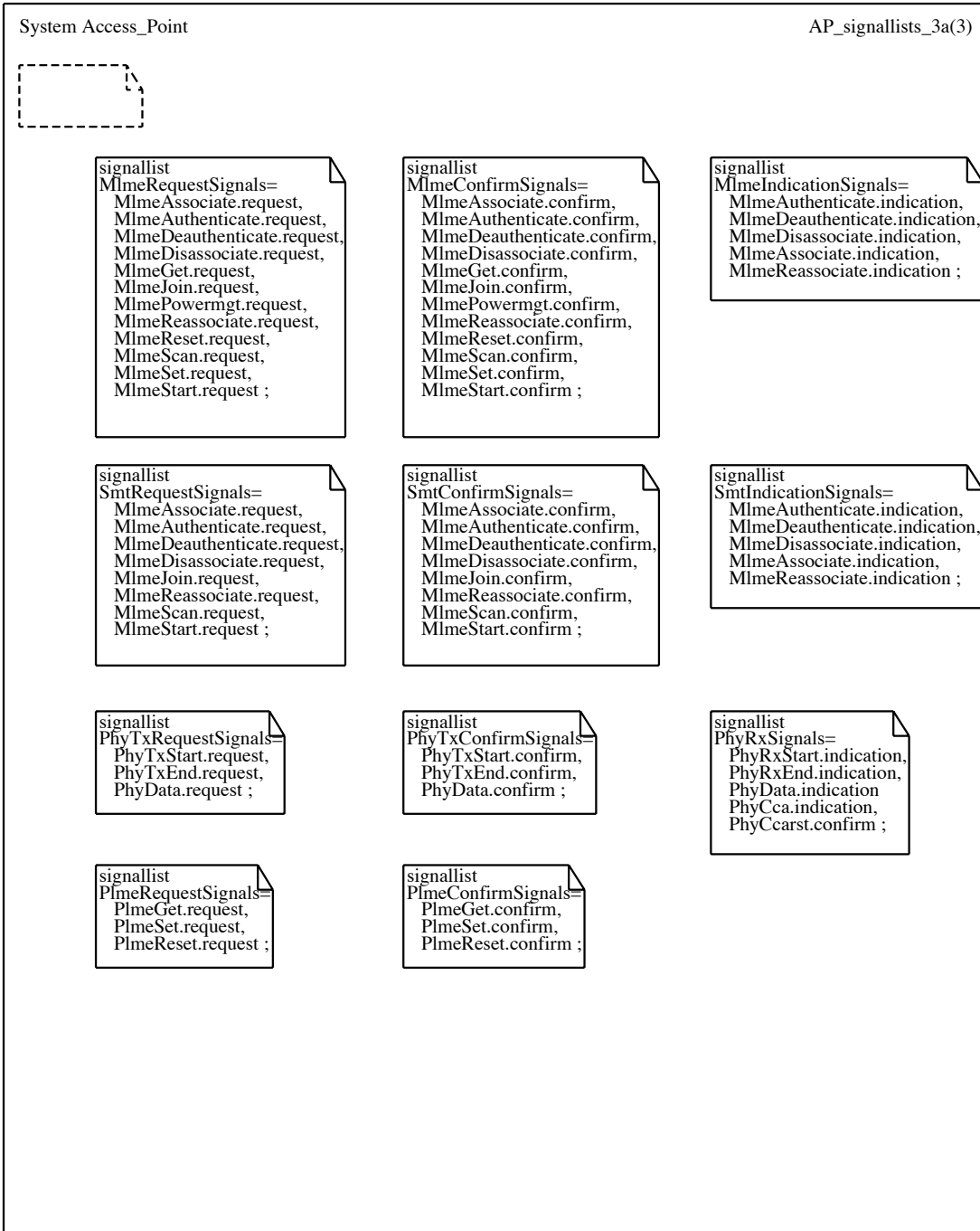
The remainder of Clause C.4 is the formal description, in SDL/GR, of an IEEE 802.11 AP.

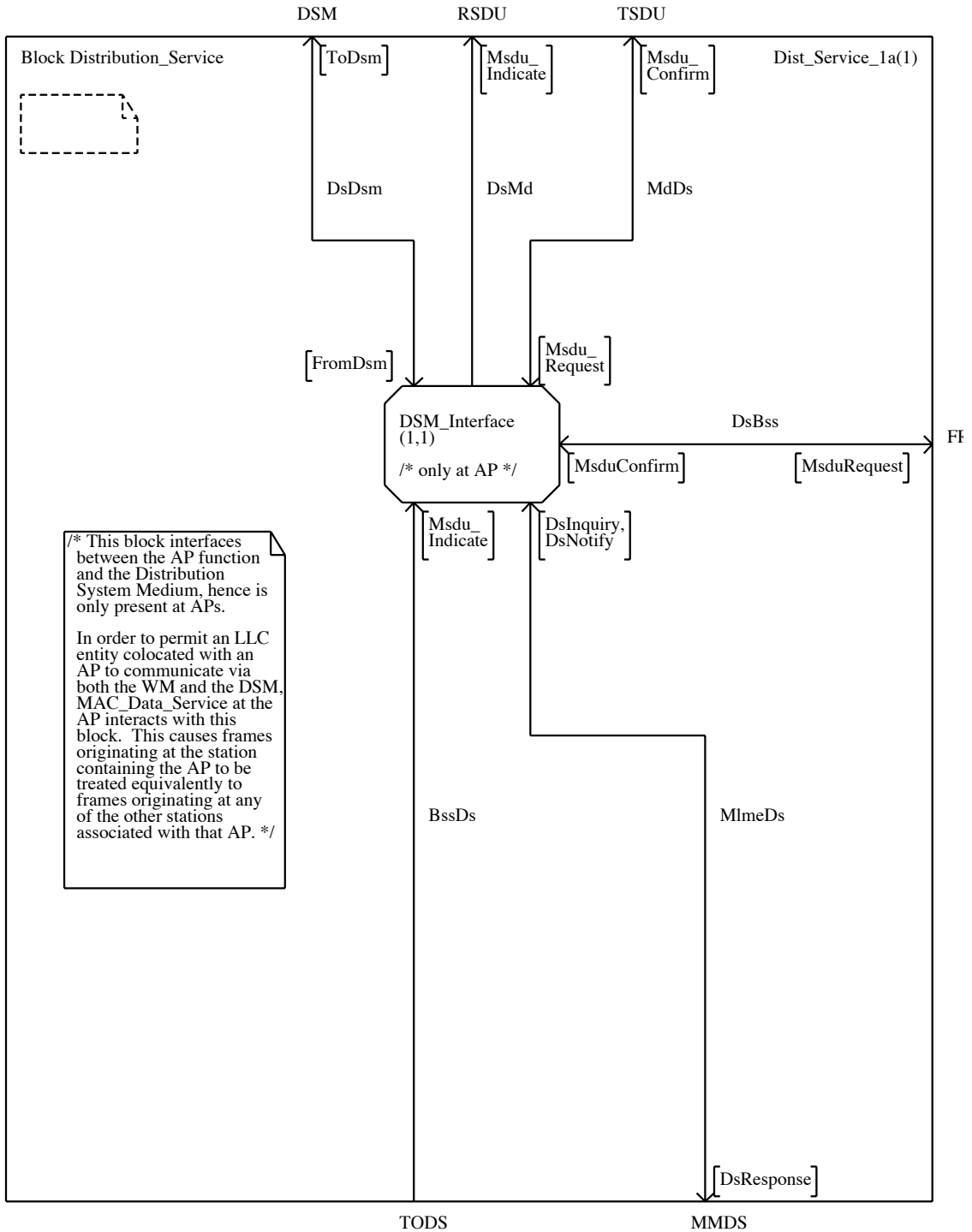


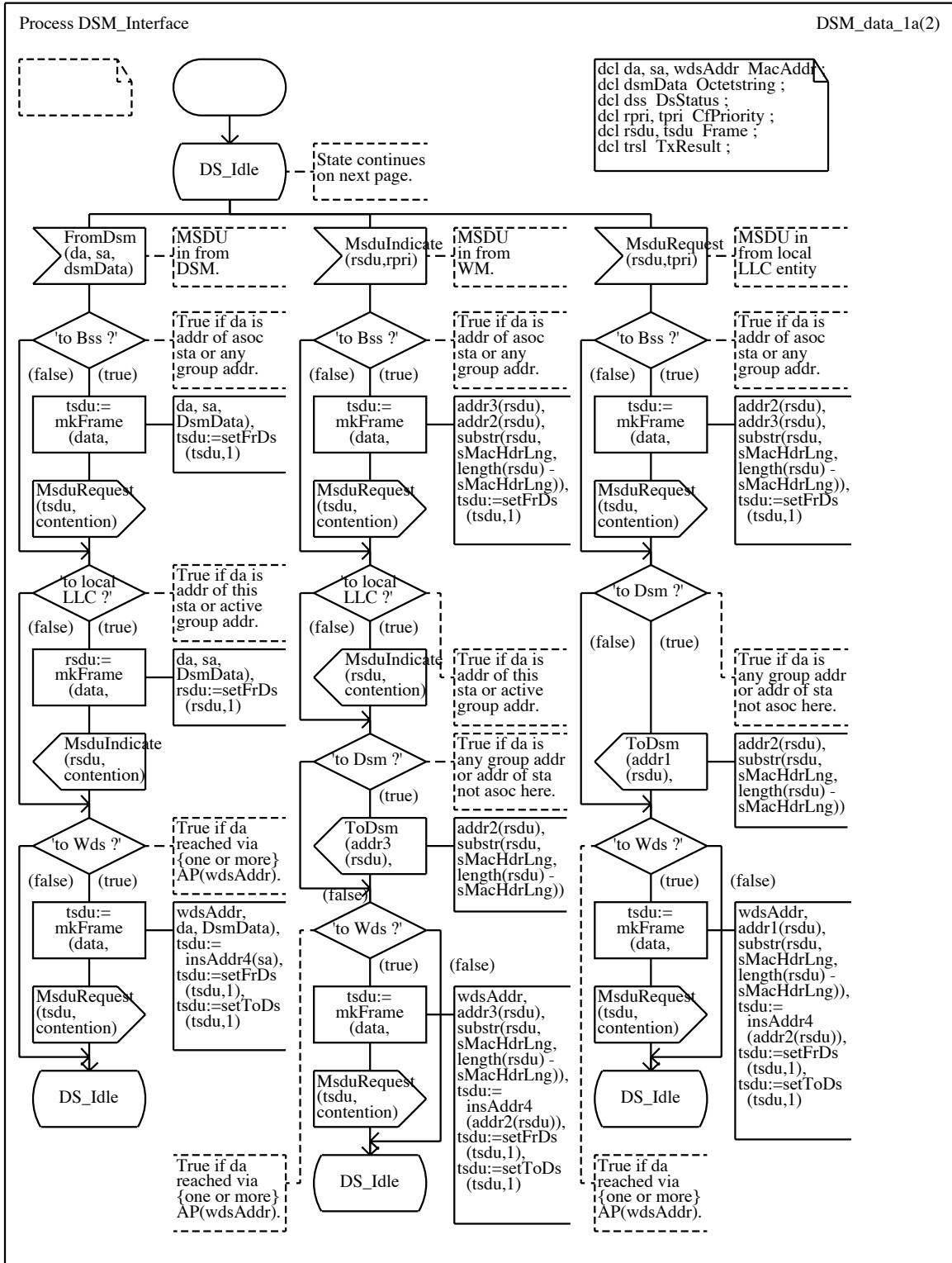
```
use macsorts  
use macmib ;
```

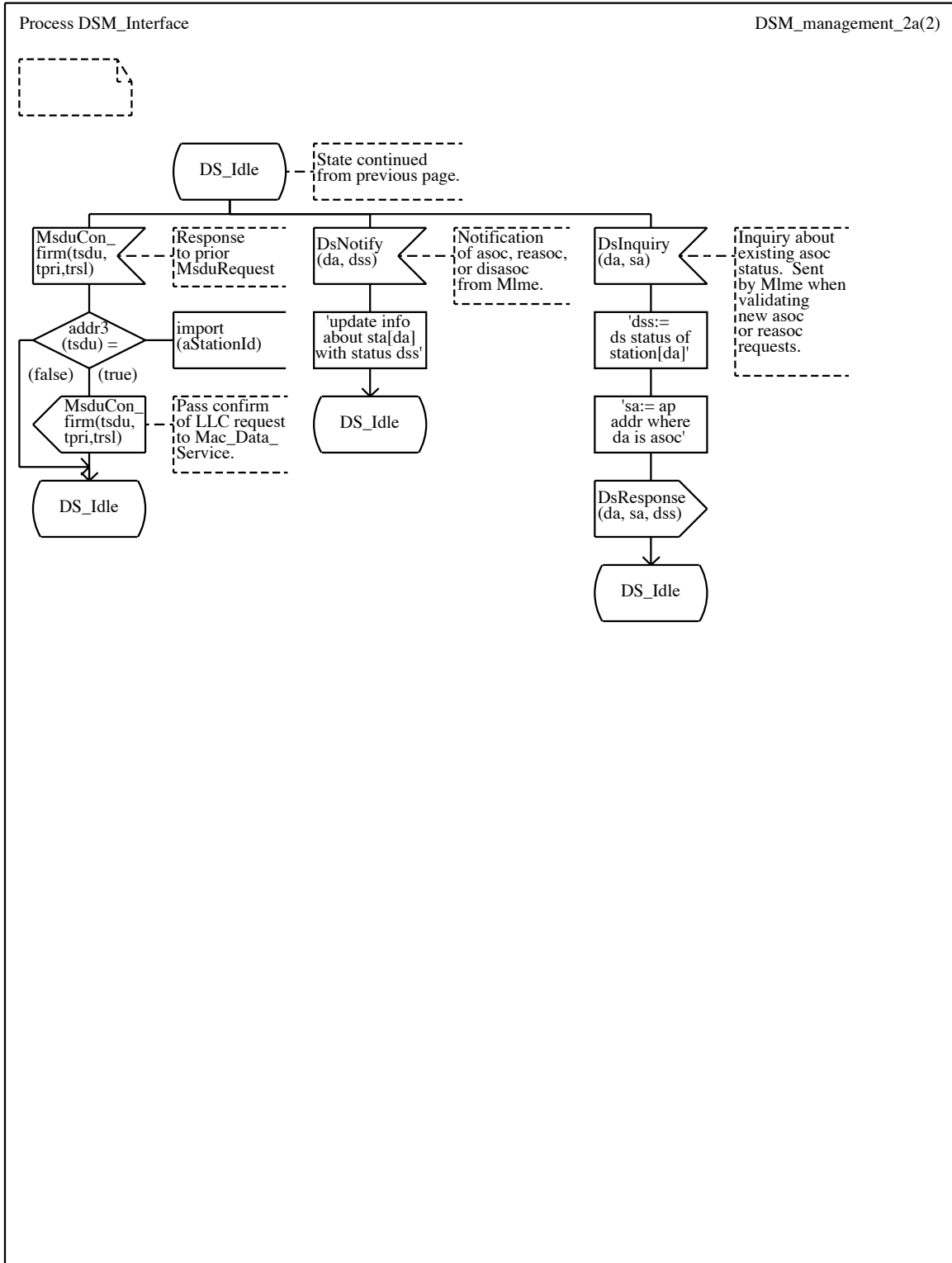


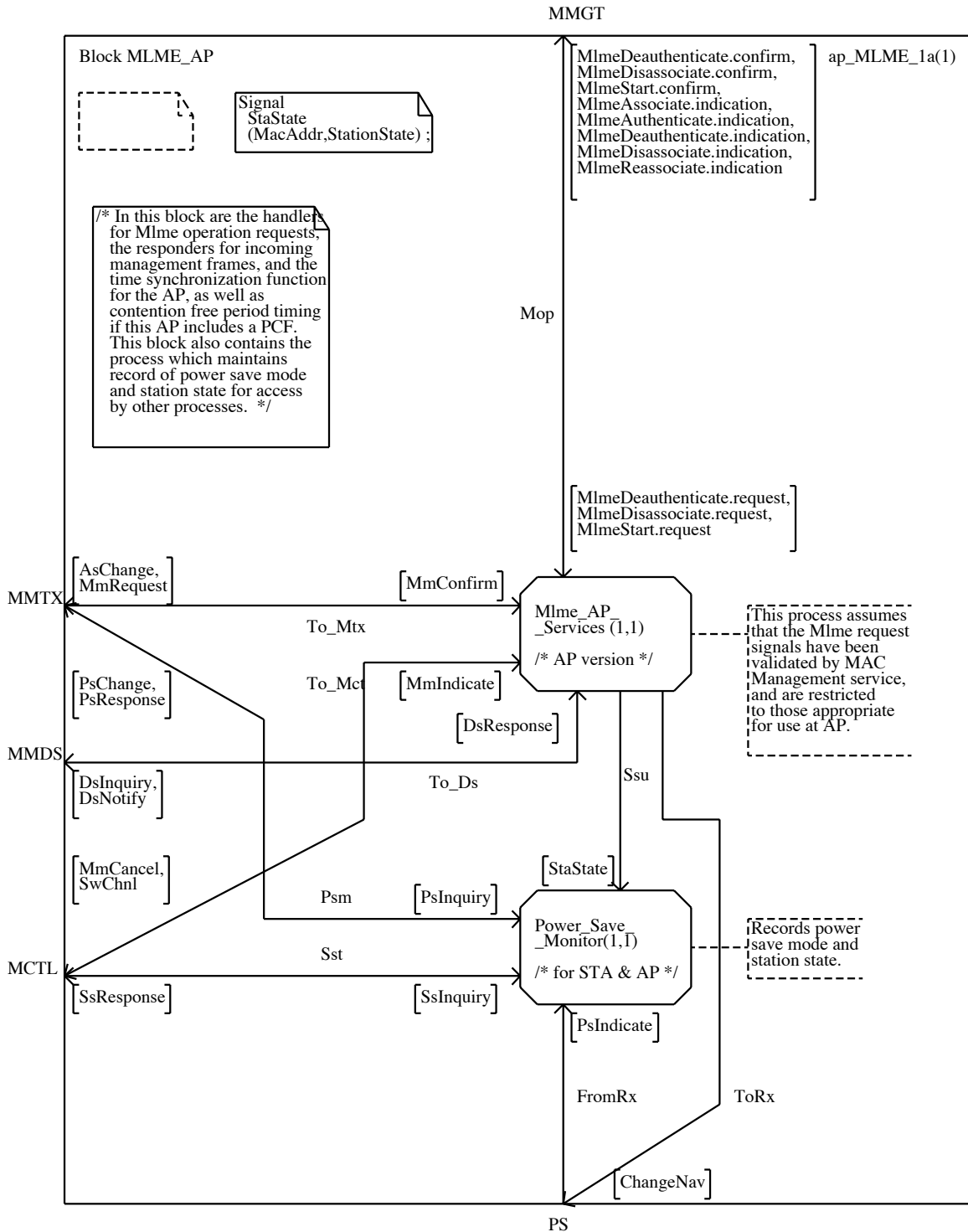
```
use macsorts
use macmib ;
```

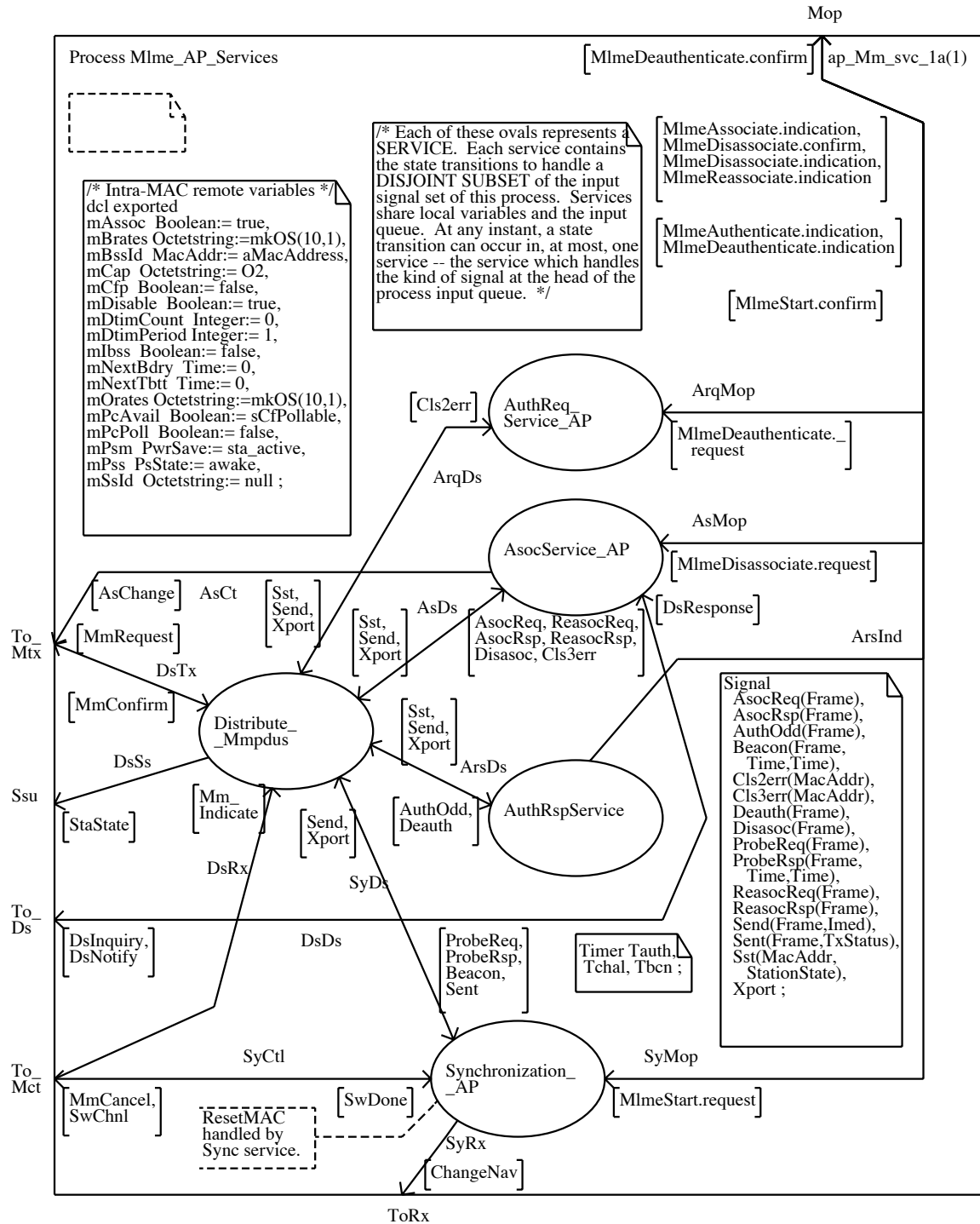


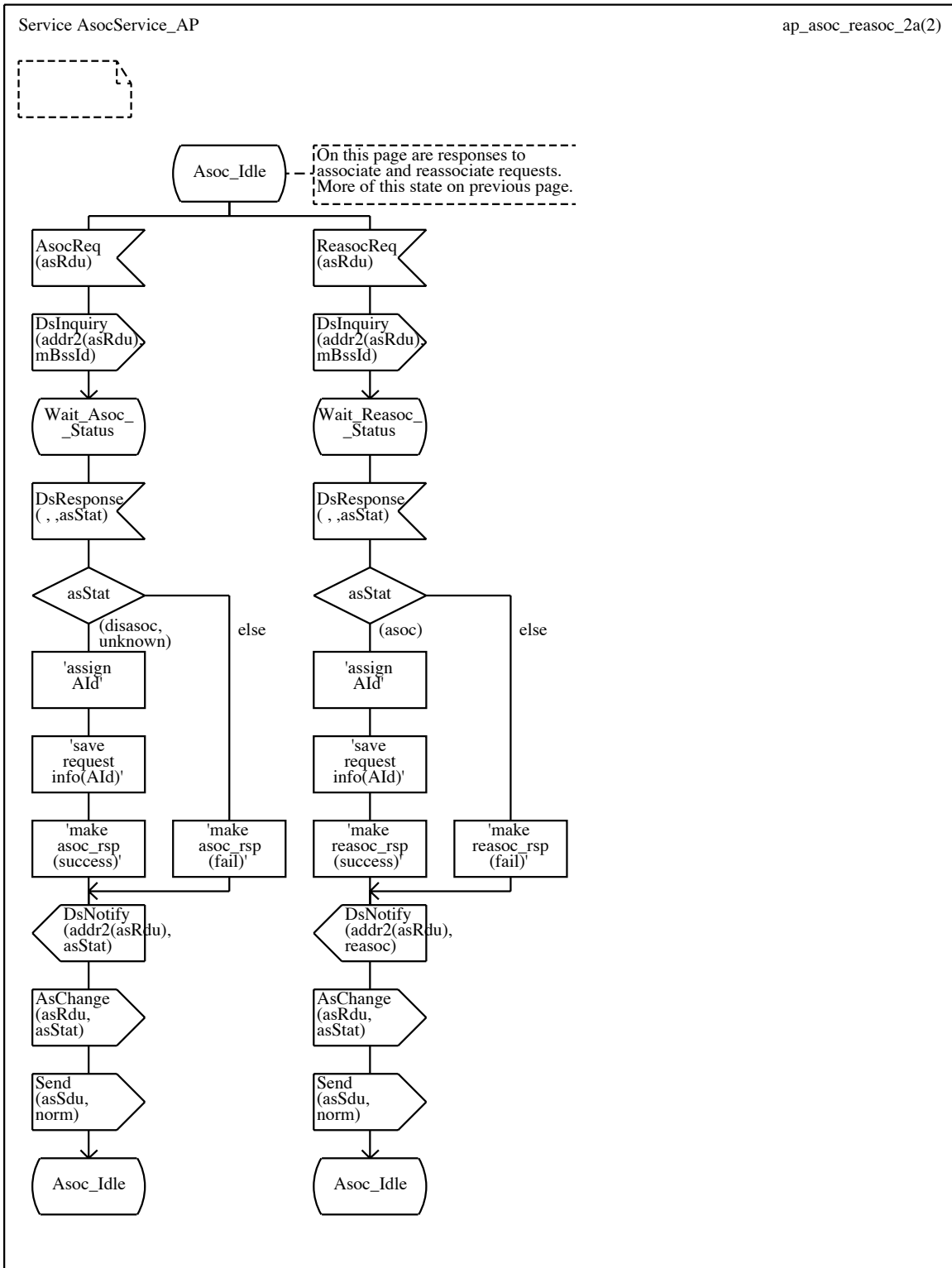


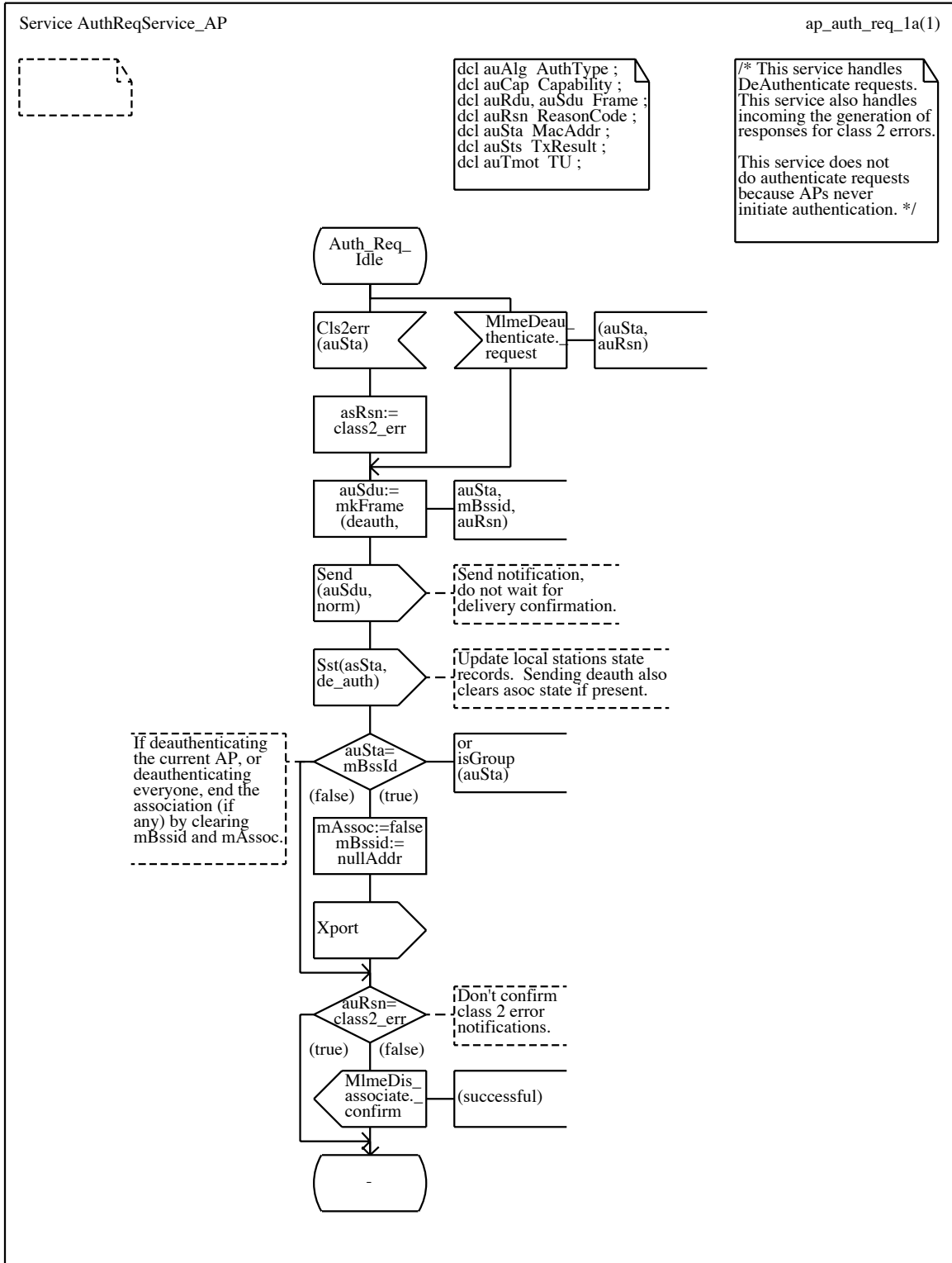


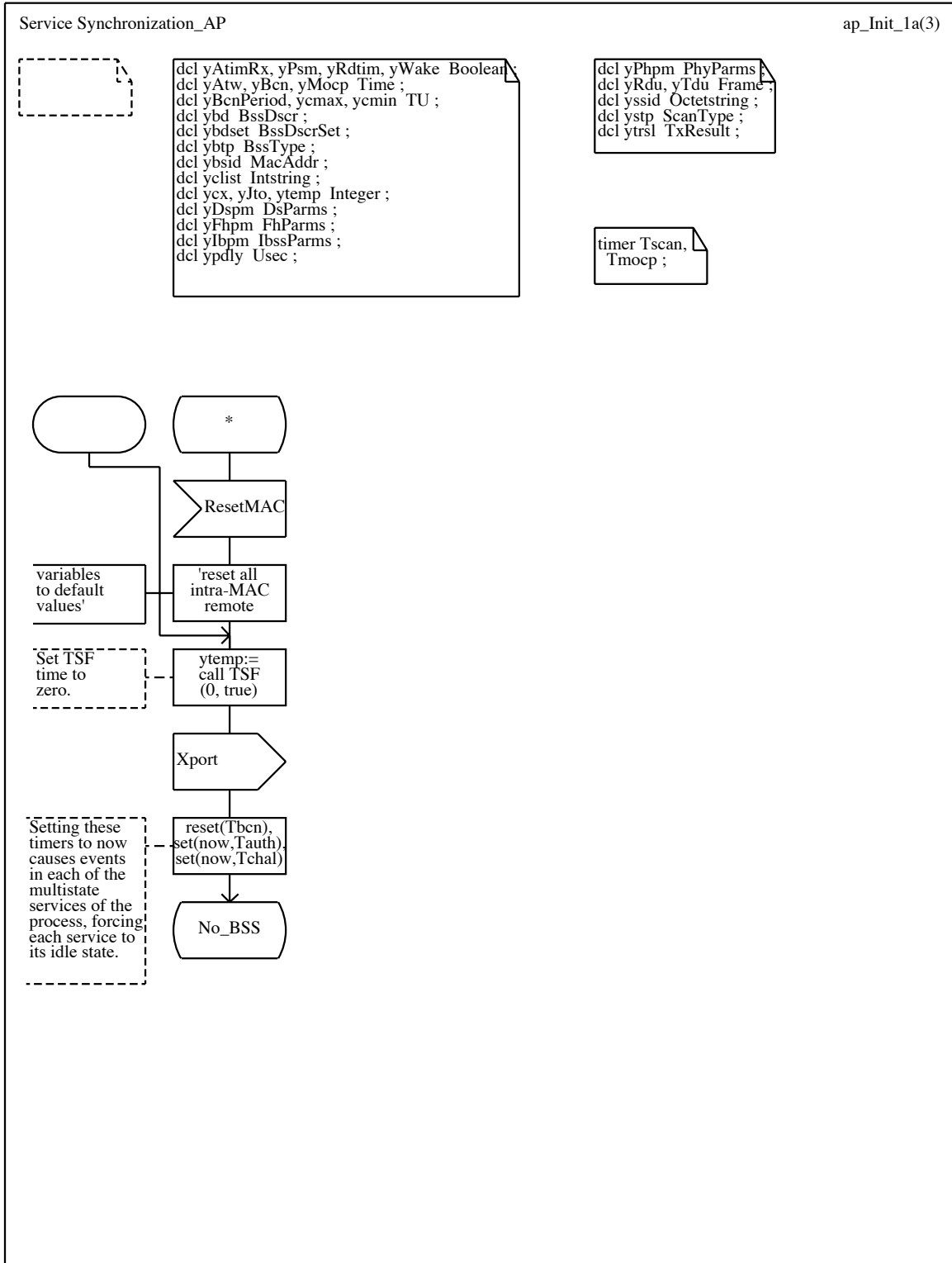


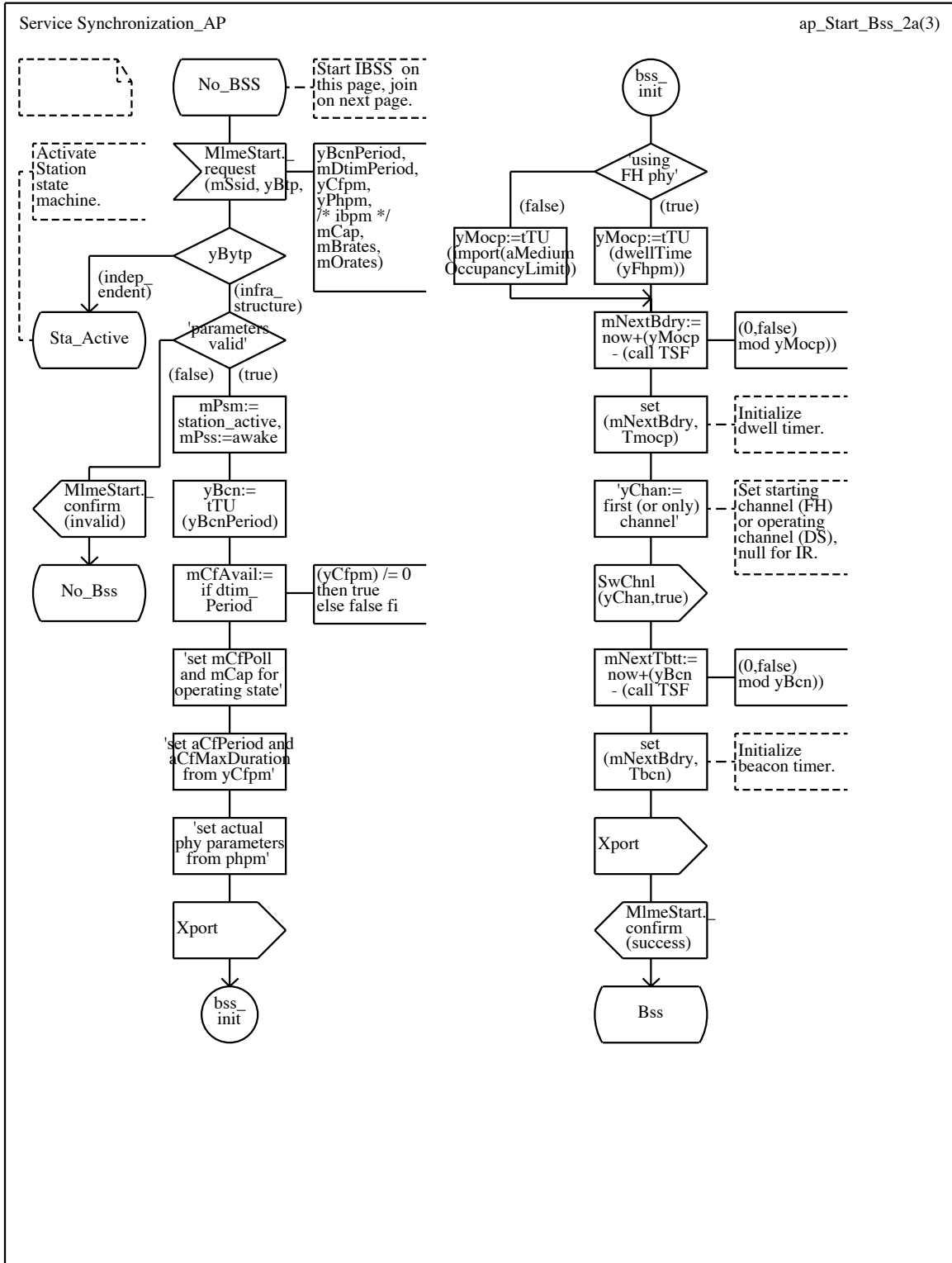


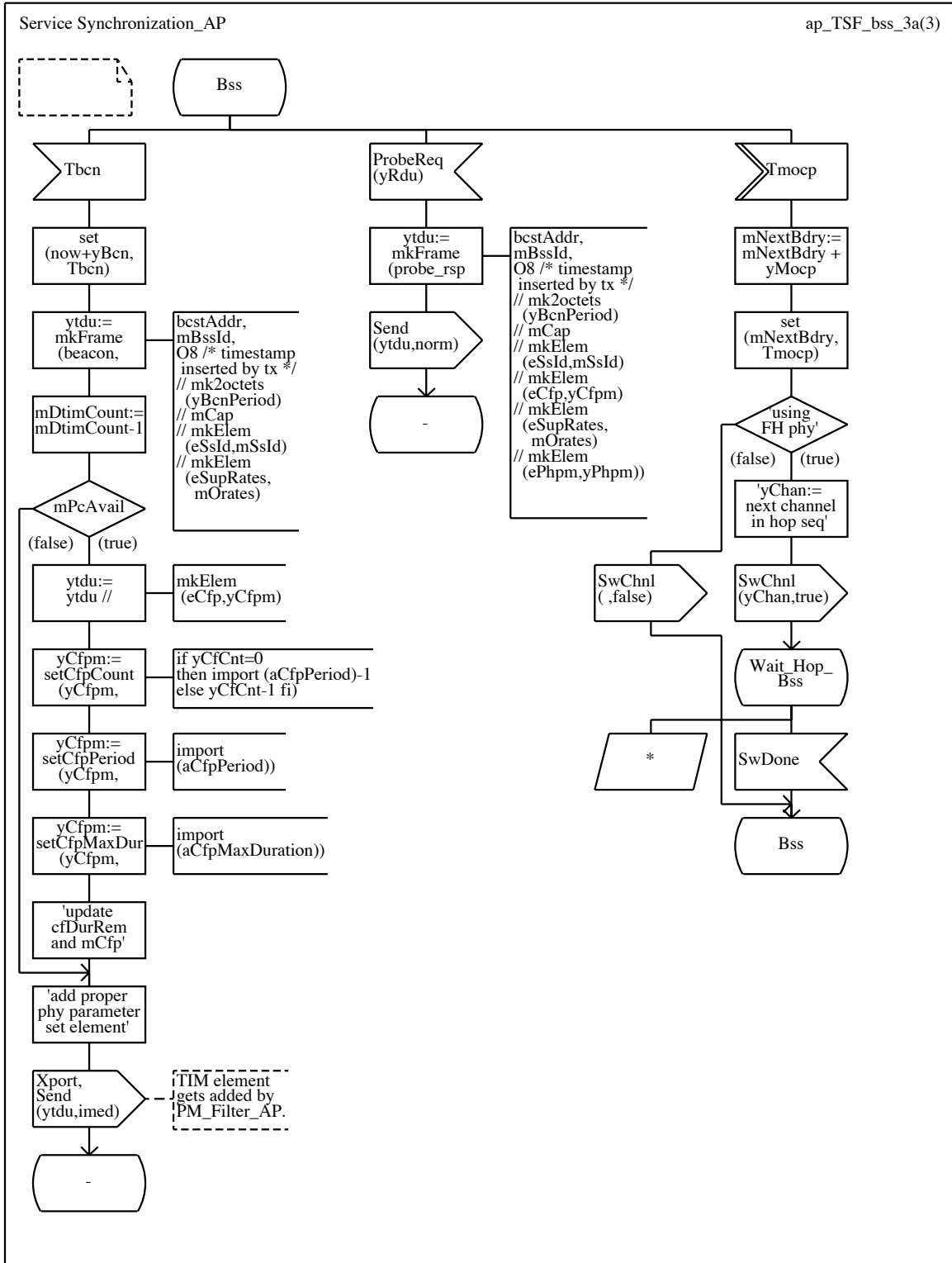


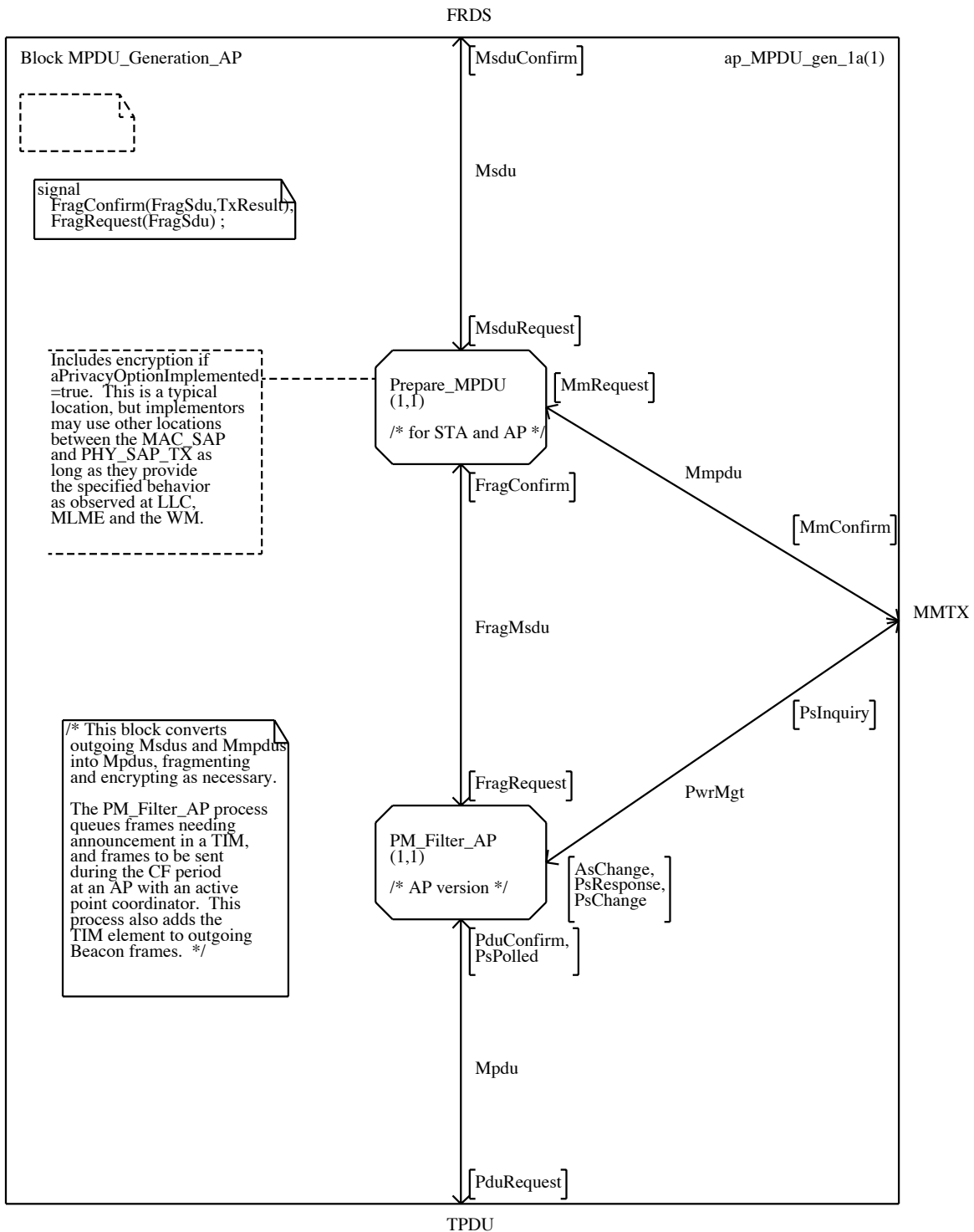


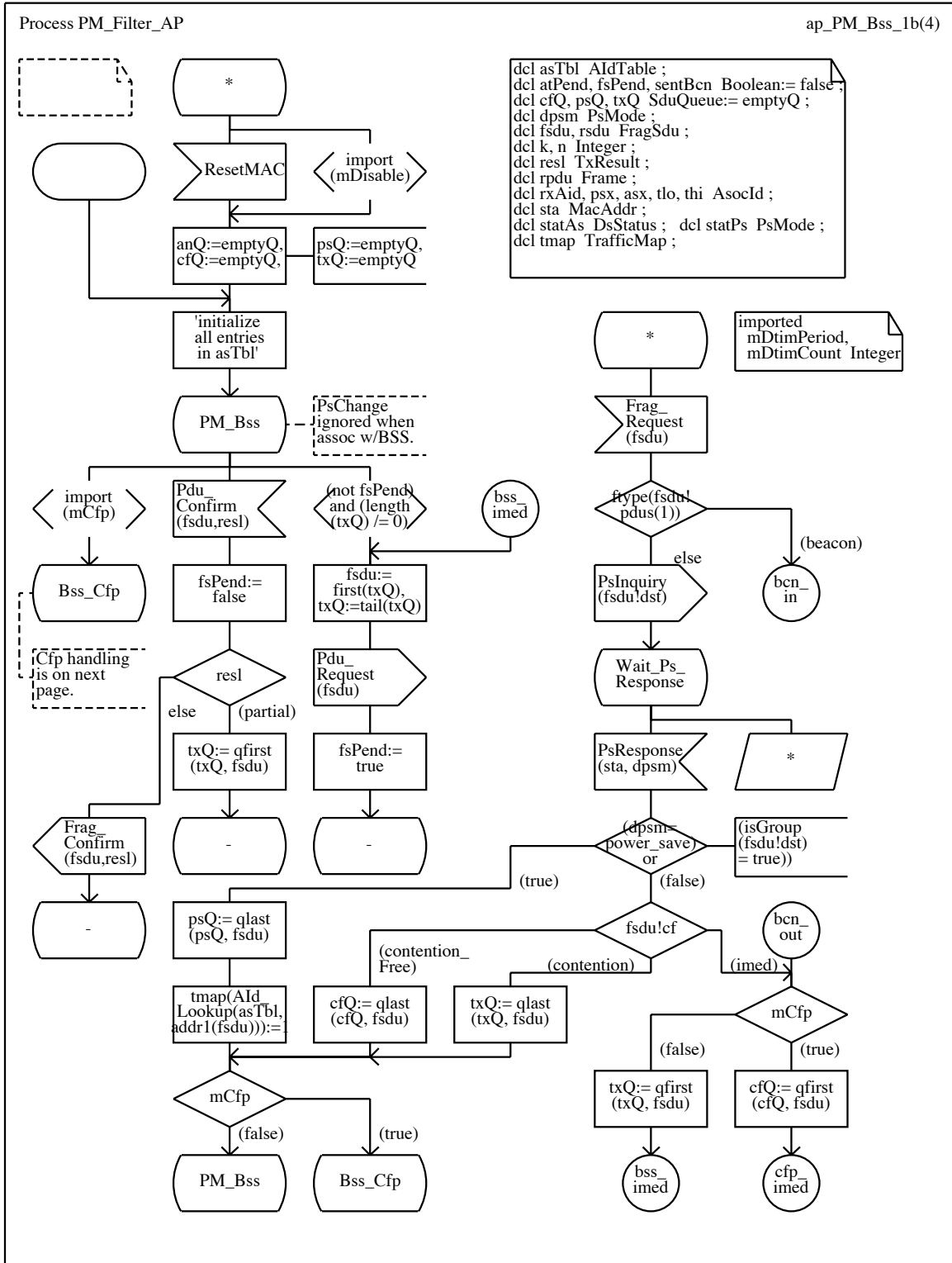


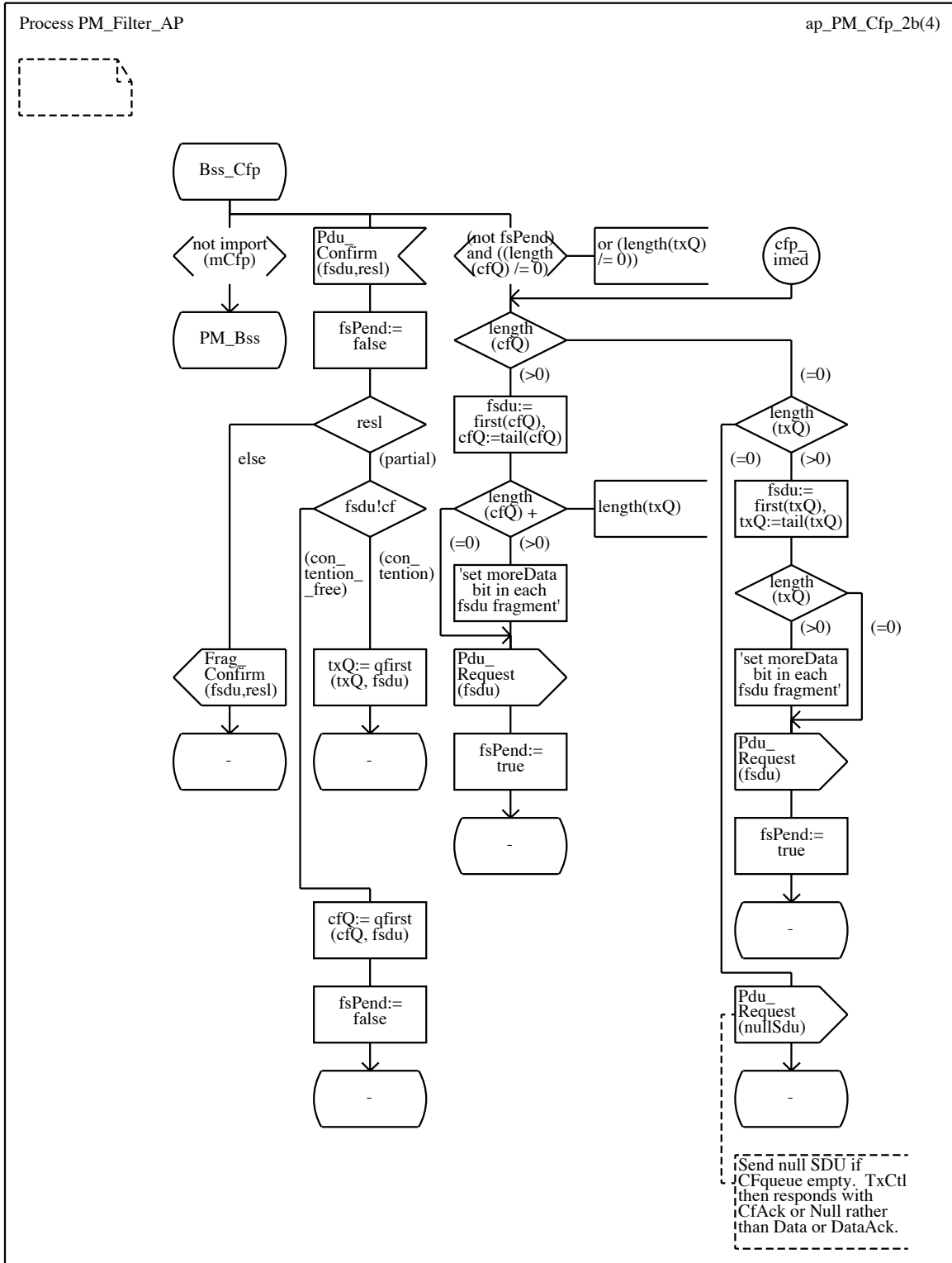


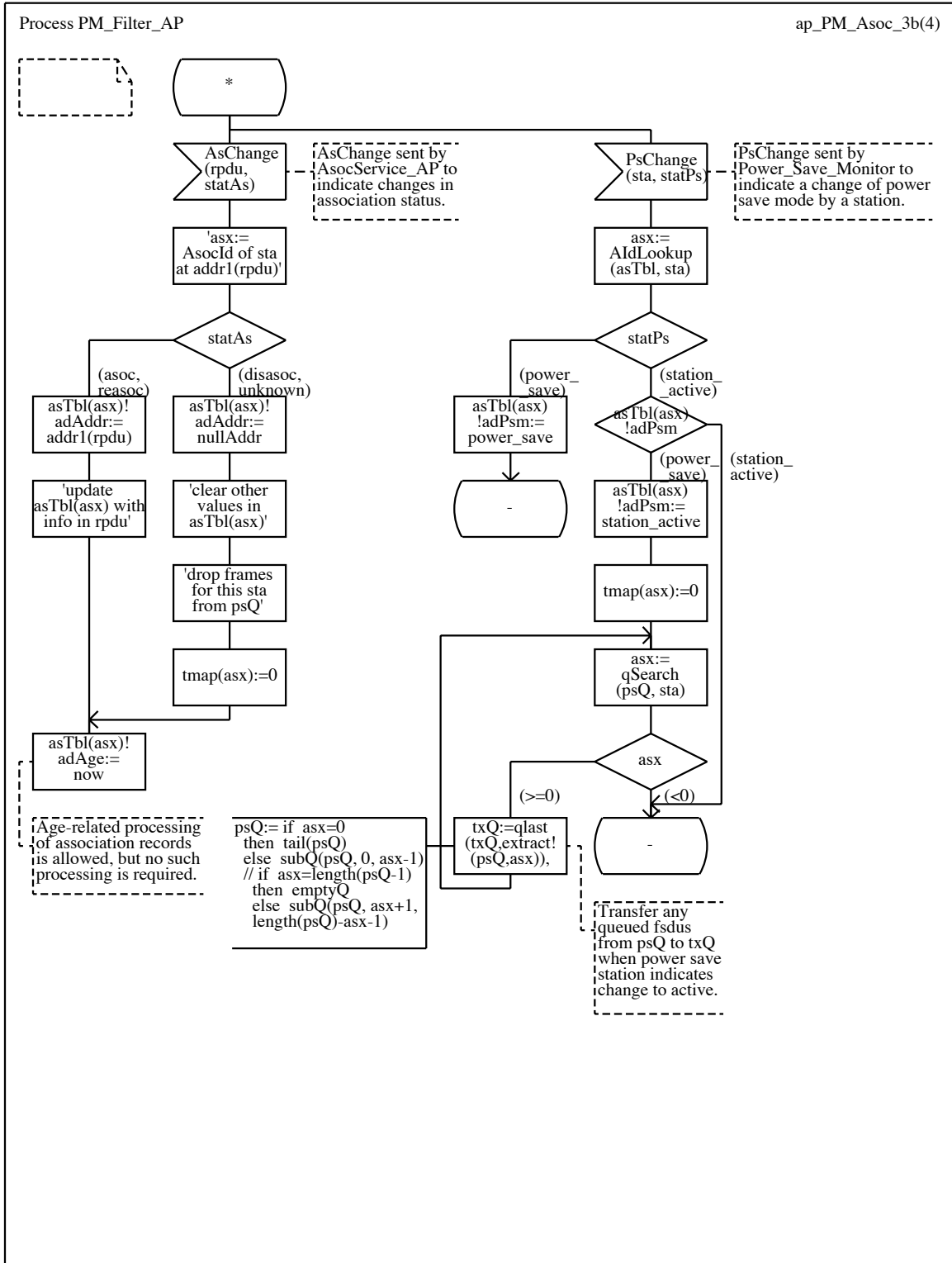


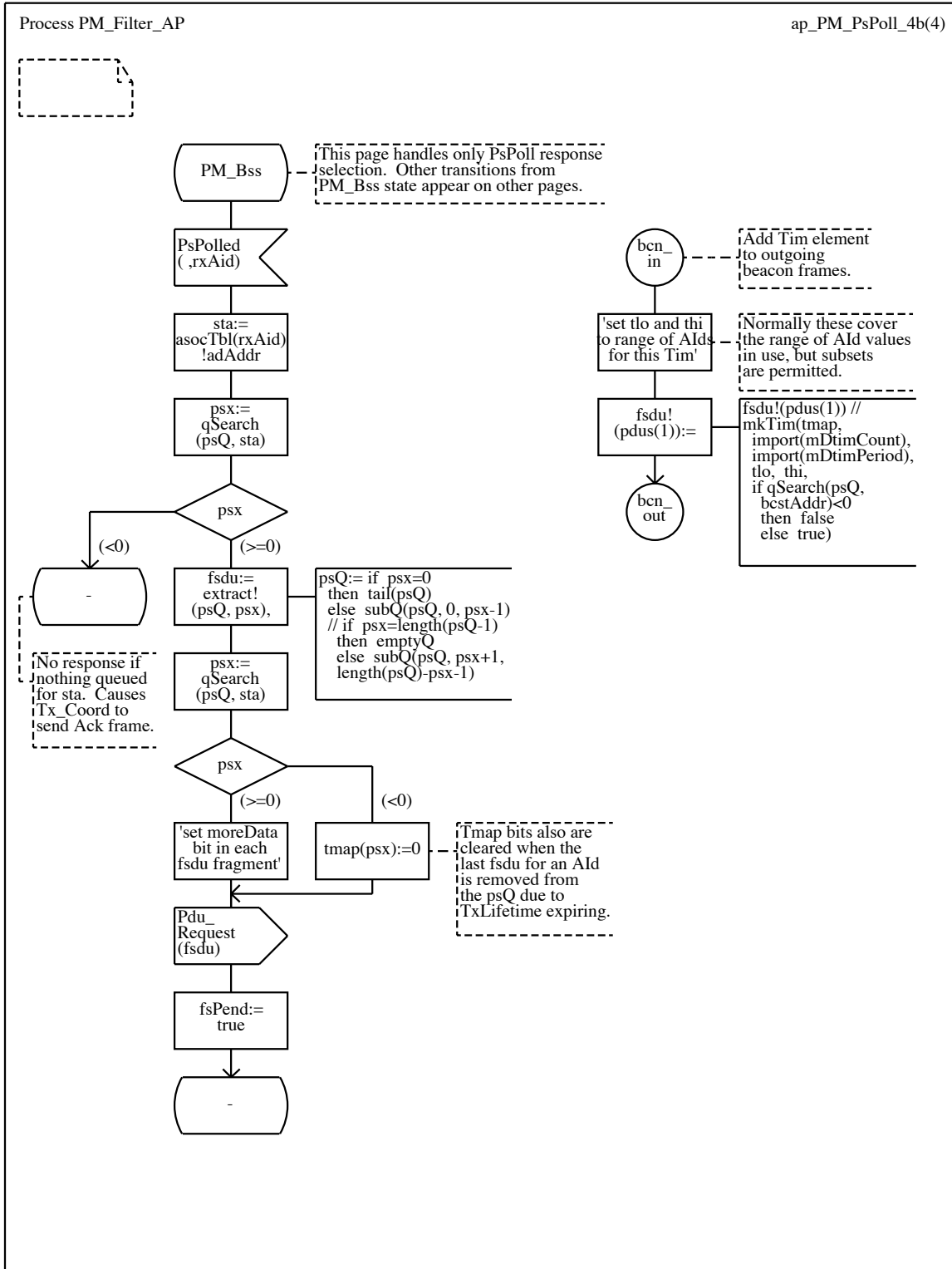


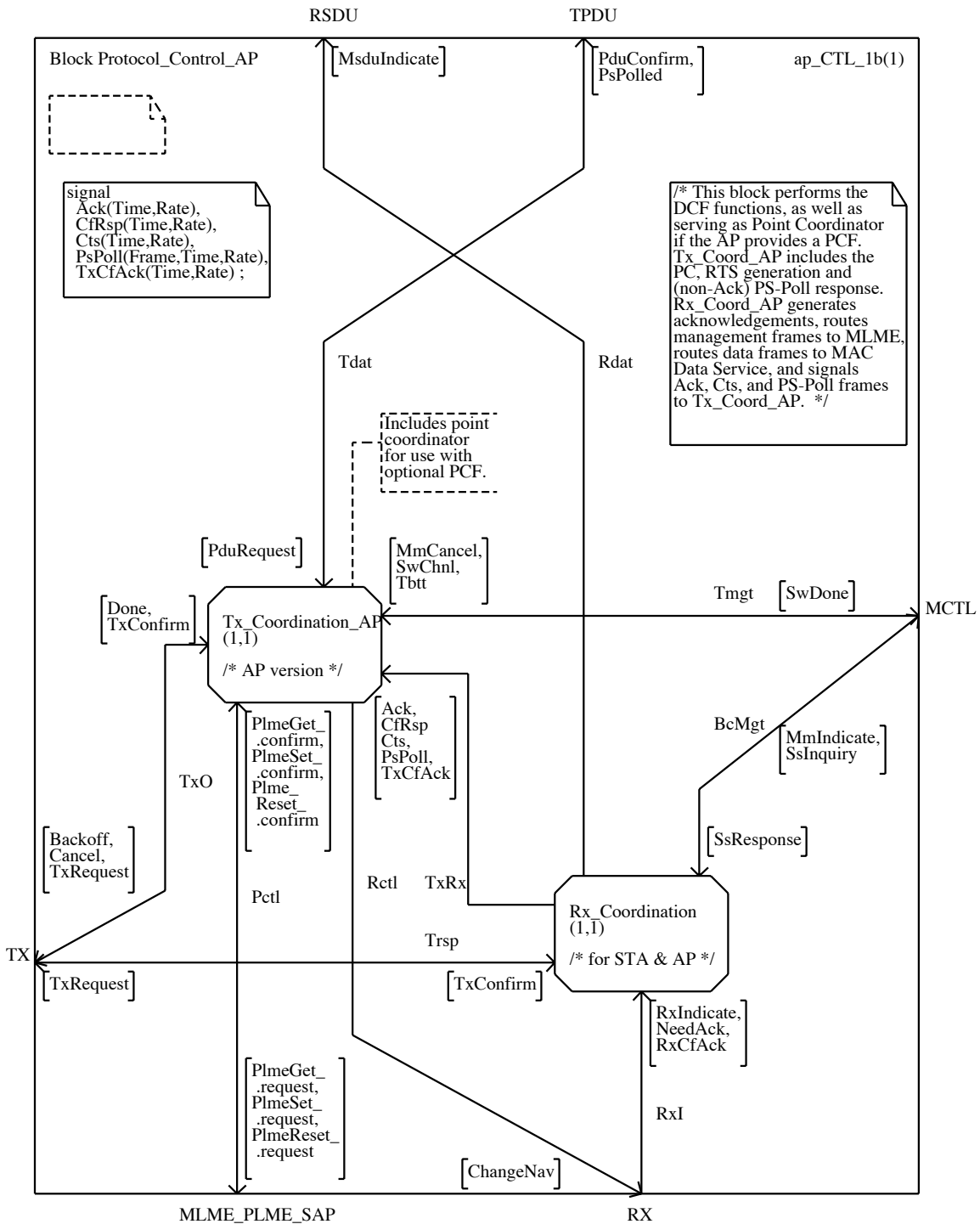


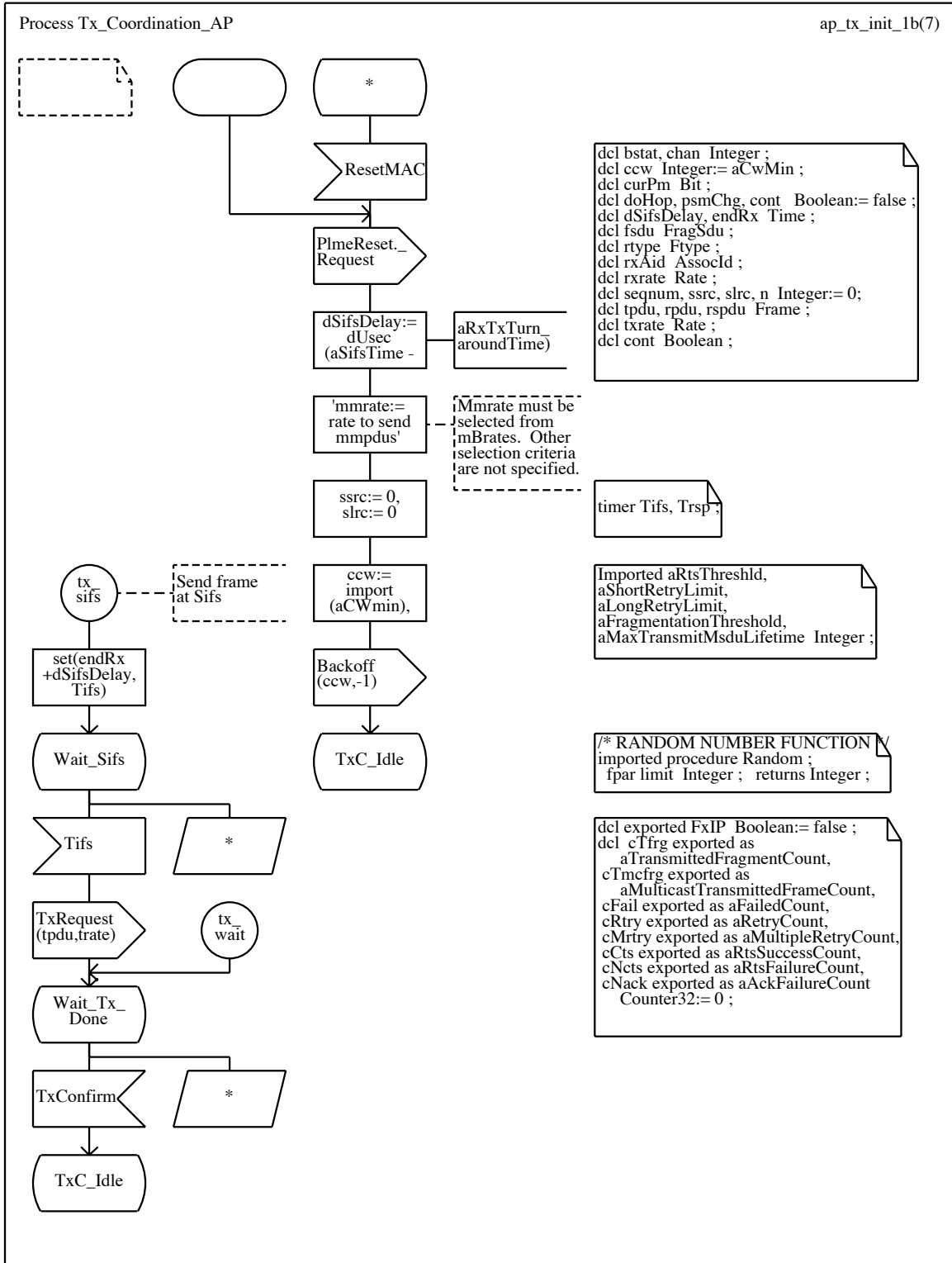


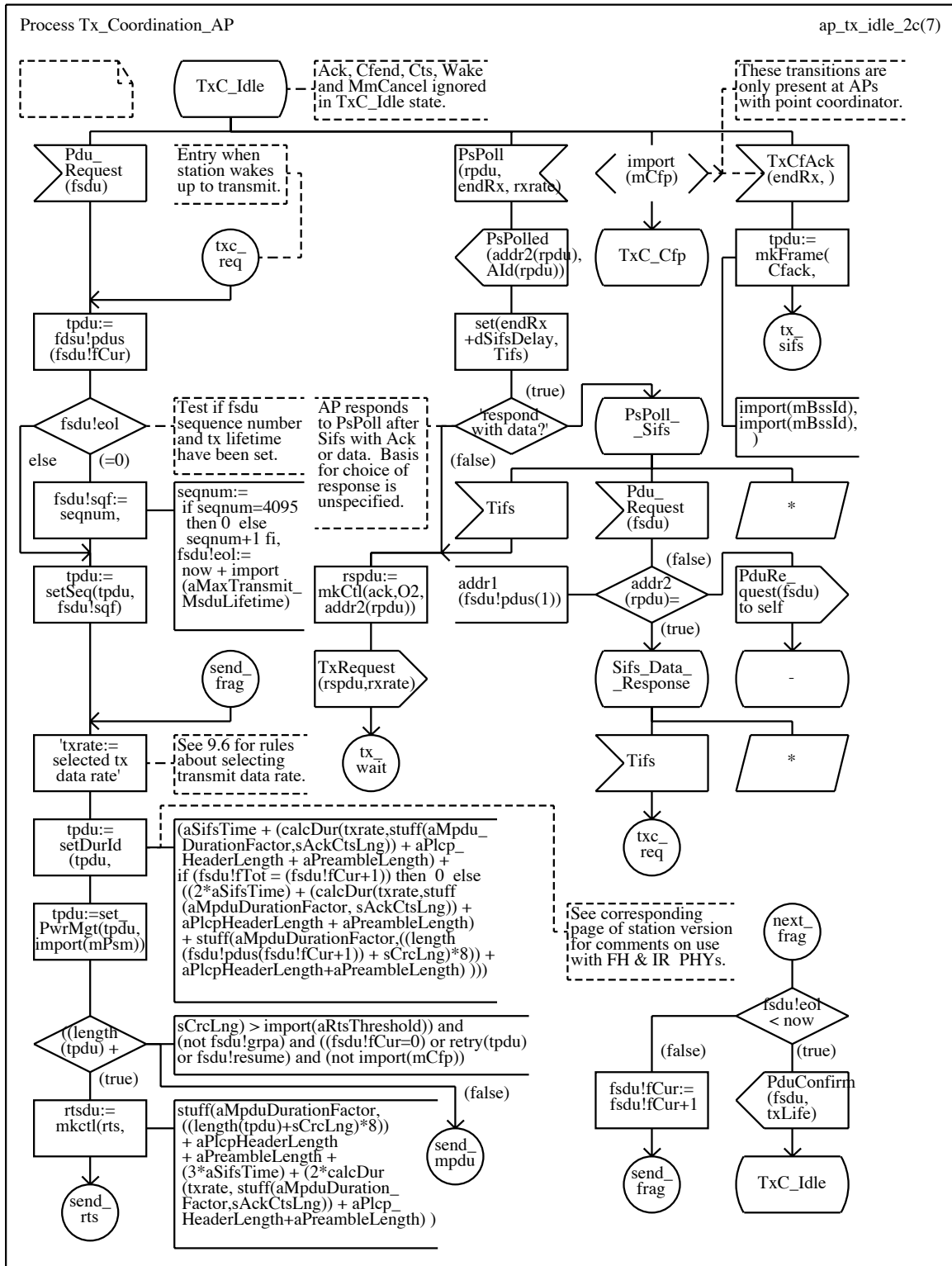


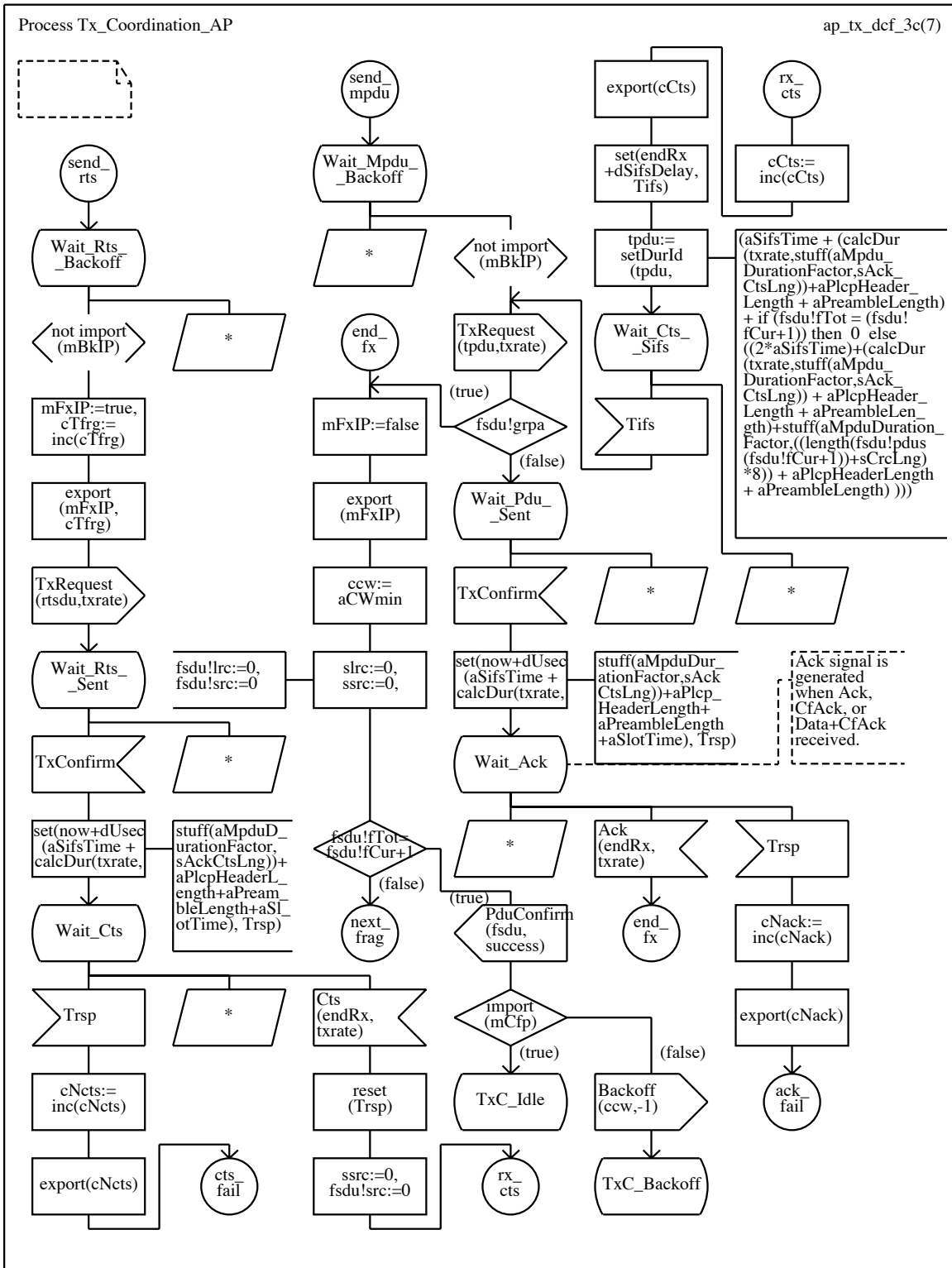


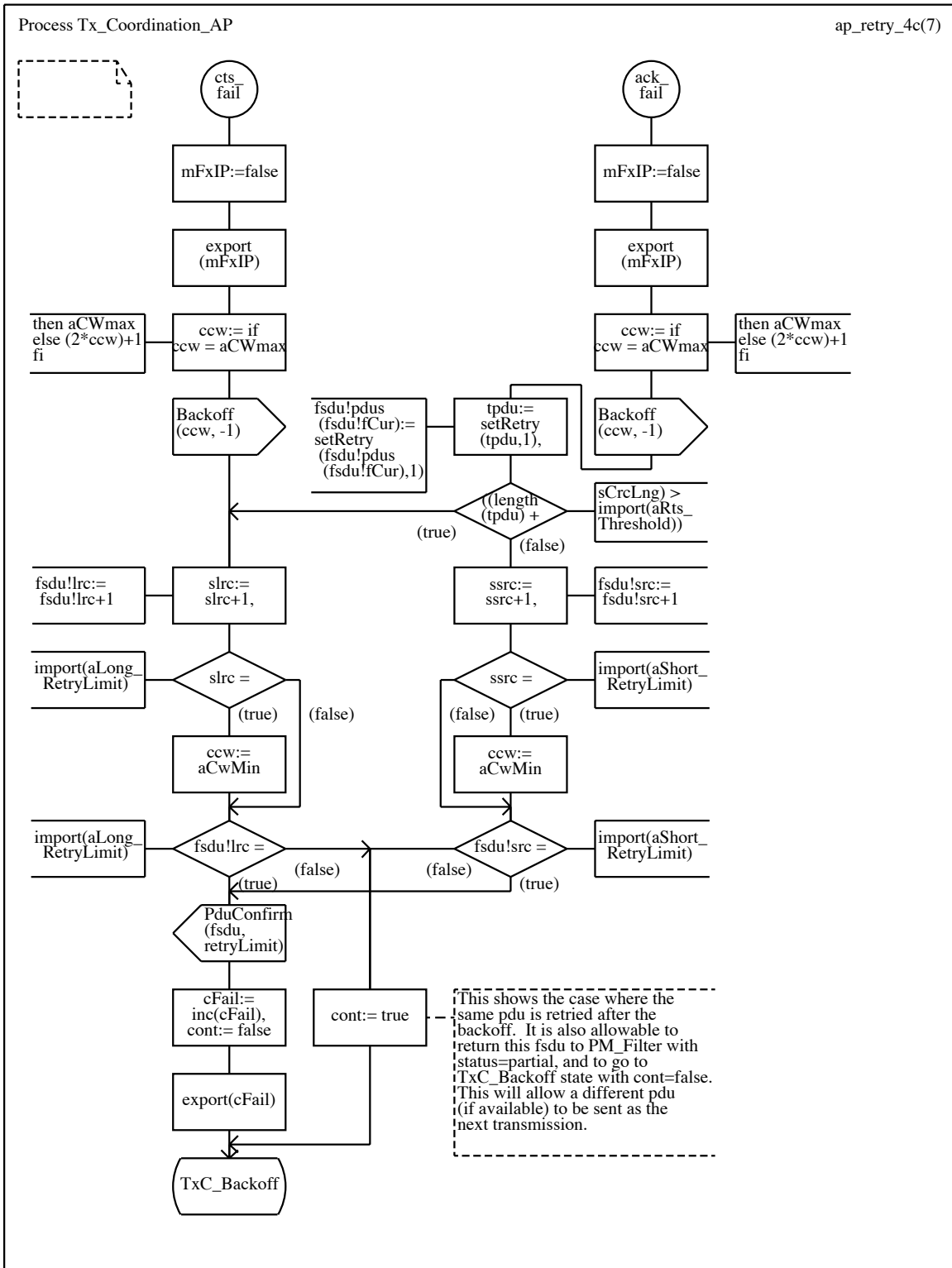


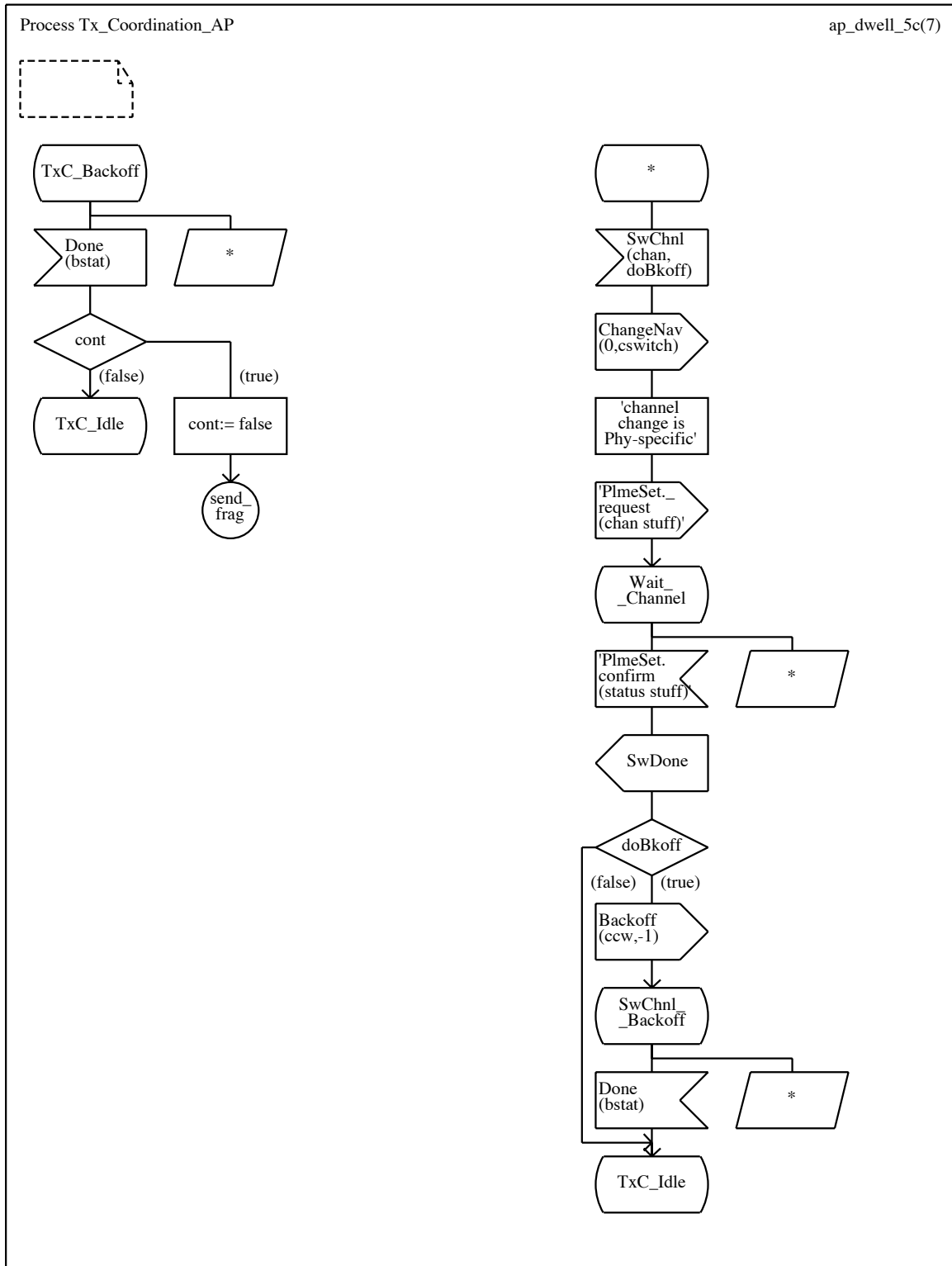


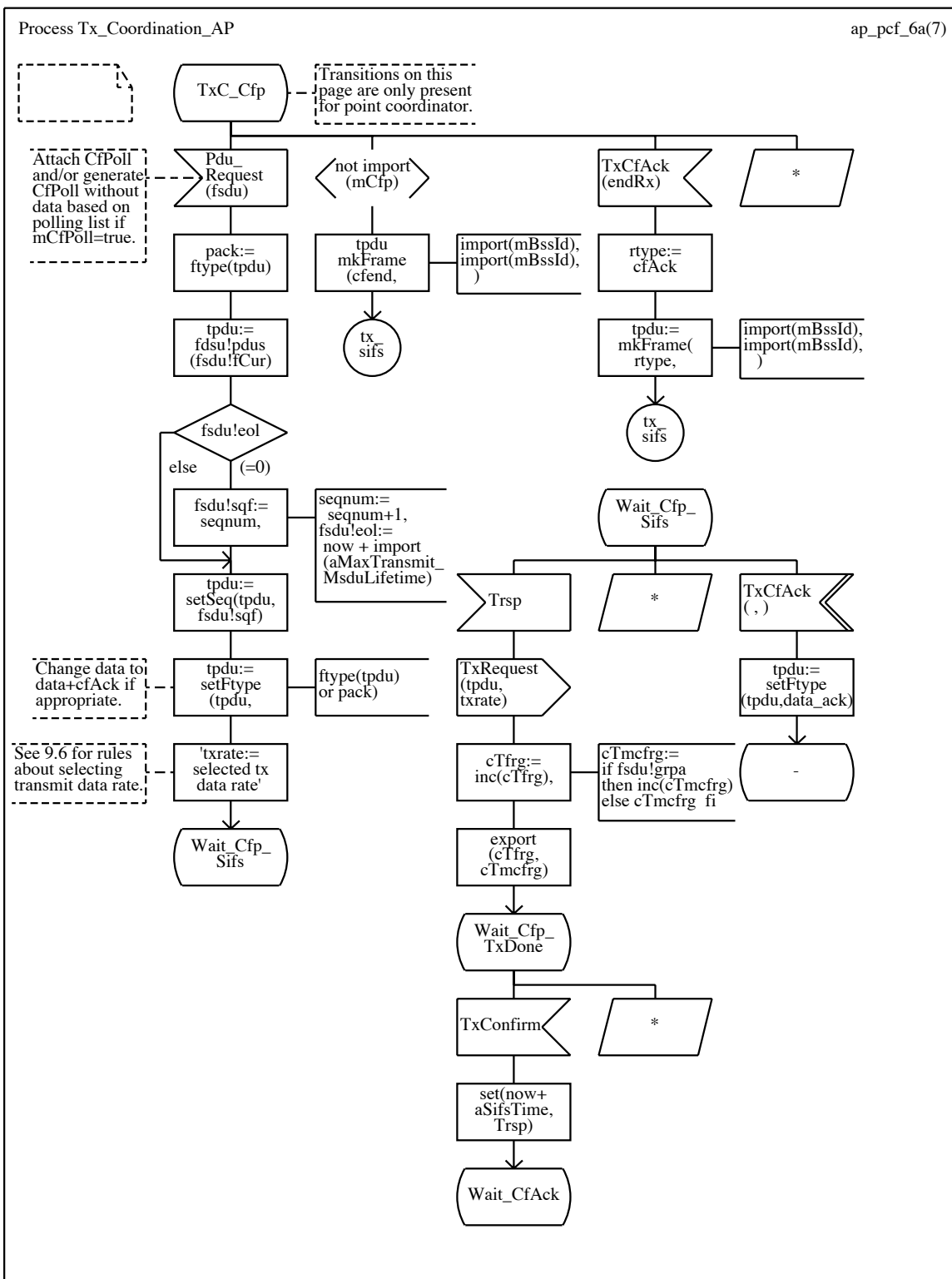


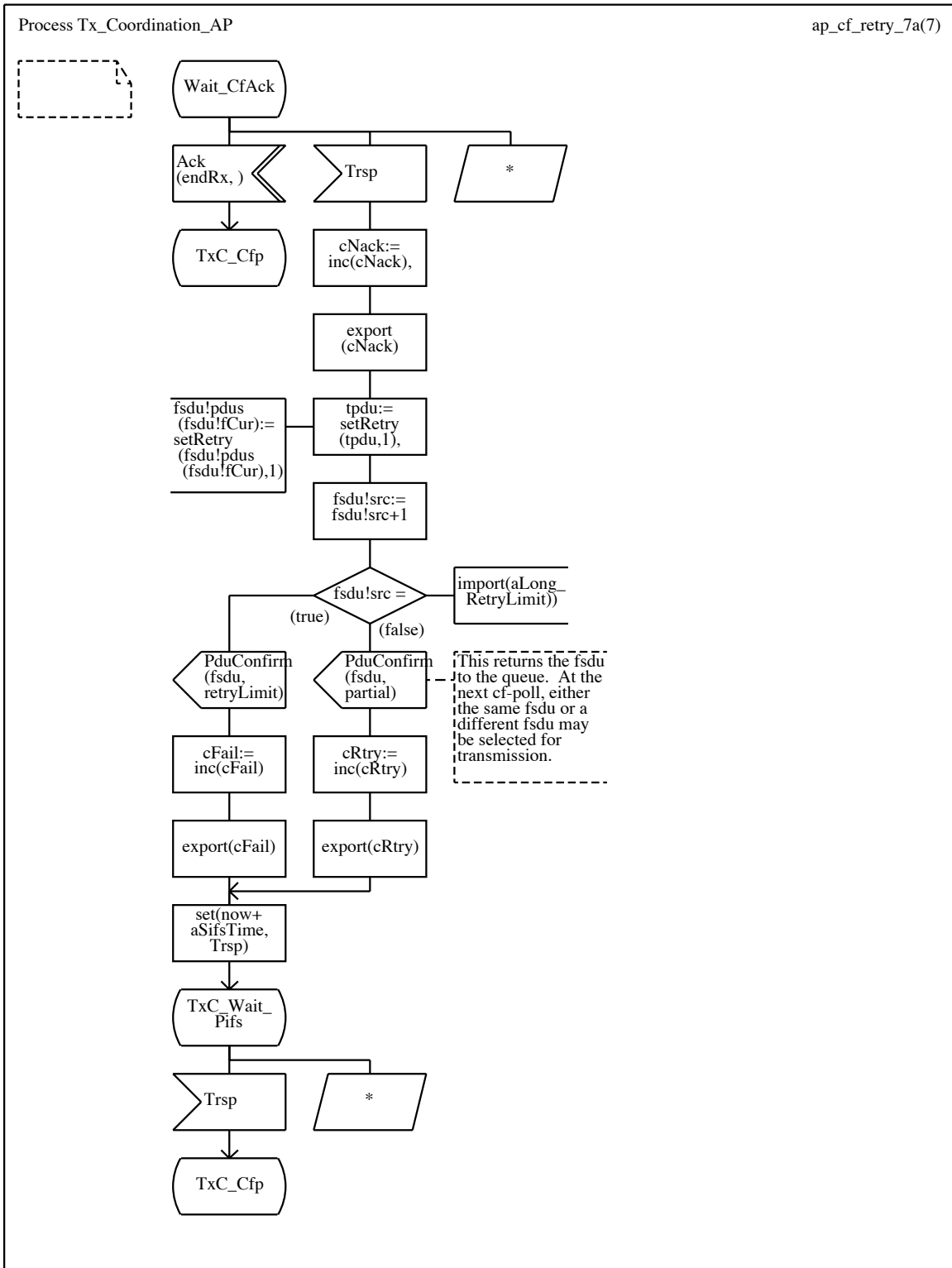












Annex D

(normative)

ASN.1 encoding of the MAC and PHY MIB

Publisher's note:

It has come to our attention that the definition of the Management Information Base (MIB) in the approved draft standard contains inconsistencies between the definitions in Clause 11, Clause 13, and Annex D. Because the definitions in Annex D are not correct, Annex D is not being published in this first edition.

The Working Group is planning to submit a PAR for the revision of the standard to make Annex D consistent with Clauses 11 and 13. They are also writing an interpretation of the Annex D material, which will be made available in December 1997 at no cost to all purchasers of the published standard. This information will also be posted on our web site at **standards.ieee.org/reading/index.html**.

Annex E

(informative)

Bibliography

E.1 General

[B1] ANSI Z136.1-1993, American National Standard for the Safe Use of Lasers.

[B2] IEC 60825-1 (1993), Safety of laser products—Part 1: Equipment classification, requirements and user's guide.

[B3] IEEE Std 802.10-1992, IEEE Standards for Local and Metropolitan Area Networks: Interoperable LAN/MAN Security (SILS) (ANSI).

[B4] Schneier, Bruce, "Applied Cryptography, Protocols, Algorithms and Source Code in C," New York: Wiley: 1994.

E.2 Specification and description language (SDL) documentation

[B5] Belina, Ferenc, Dieter Hogrefe, and Amardeo Sarma, *SDL with Applications from Protocol Specification*. Prentice Hall Europe, Hertfordshire, UK, 1991.

An introductory text on SDL, also useful as a language reference (for SDL-88).

[B6] Ellsberger, Jan, Dieter Hogrefe, and Amardeo Sarma, *SDL, Formal Object-Oriented Language for Communicating Systems*; (Prentice Hall Europe, Hertfordshire, UK, 1997).

A recently published book, which appears to be the most comprehensive single-volume introduction and reference for SDL-92, including its object-oriented extensions.

[B7] Faergemand, Ove and Anders Olsen, *New Features in SDL-92*; *SDL Newsletter* (ISSN 1023-7151), no. 16 (May, 1993), pp. 10–29. Also available online at <http://www.tdr.dk/public/SDL/SDL.html>.

This provides a summary of the changes from SDL-88 to SDL-92.

[B8] Olsen, Anders, Ove Faergemand, Birger Moller-Pedersen, Rick Reed, and T. R. W. Smith, *Systems Engineering Using SDL-92*. Elsevier Science B.V., Amsterdam, the Netherlands, 1994.

A detailed guide to using SDL-92, including a thorough explanation of abstract data type mechanism and SDL combined with ASN.1 (Z.105).

