

EXHIBIT B

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

**REQUEST FOR CONTINUED EXAMINATION(RCE)TRANSMITTAL
(Submitted Only via EFS-Web)**

Application Number	11/213,215	Filing Date	2005-08-25	Docket Number (if applicable)	APL-P2766-C2	Art Unit	2116
First Named Inventor	Lynn R. Youngs			Examiner Name	Stefan Stoynov		

This is a Request for Continued Examination (RCE) under 37 CFR 1.114 of the above-identified application.
Request for Continued Examination (RCE) practice under 37 CFR 1.114 does not apply to any utility or plant application filed prior to June 8, 1995, or to any design application. The Instruction Sheet for this form is located at WWW.USPTO.GOV

SUBMISSION REQUIRED UNDER 37 CFR 1.114

Note: If the RCE is proper, any previously filed unentered amendments and amendments enclosed with the RCE will be entered in the order in which they were filed unless applicant instructs otherwise. If applicant does not wish to have any previously filed unentered amendment(s) entered, applicant must request non-entry of such amendment(s).

- Previously submitted. If a final Office action is outstanding, any amendments filed after the final Office action may be considered as a submission even if this box is not checked.
- Consider the arguments in the Appeal Brief or Reply Brief previously filed on _____
- Other _____
- Enclosed
- Amendment/Reply
- Information Disclosure Statement (IDS)
- Affidavit(s)/ Declaration(s)
- Other _____

MISCELLANEOUS

- Suspension of action on the above-identified application is requested under 37 CFR 1.103(c) for a period of months _____
(Period of suspension shall not exceed 3 months; Fee under 37 CFR 1.17(i) required)
- Other _____

FEES

- The RCE fee under 37 CFR 1.17(e) is required by 37 CFR 1.114 when the RCE is filed.**
The Director is hereby authorized to charge any underpayment of fees, or credit any overpayments, to
Deposit Account No 501003

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT REQUIRED

- Patent Practitioner Signature
- Applicant Signature

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Signature of Registered U.S. Patent Practitioner			
Signature	/Shun Yao/	Date (YYYY-MM-DD)	2008-02-26
Name	Shun Yao	Registration Number	59242

This collection of information is required by 37 CFR 1.114. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

Privacy Act Statement

The Privacy Act of 1974 (P.L. 93-579) requires that you be given certain information in connection with your submission of the attached form related to a patent application or patent. Accordingly, pursuant to the requirements of the Act, please be advised that: (1) the general authority for the collection of this information is 35 U.S.C. 2(b)(2); (2) furnishing of the information solicited is voluntary; and (3) the principal purpose for which the information is used by the U.S. Patent and Trademark Office is to process and/or examine your submission related to a patent application or patent. If you do not furnish the requested information, the U.S. Patent and Trademark Office may not be able to process and/or examine your submission, which may result in termination of proceedings or abandonment of the application or expiration of the patent.

The information provided by you in this form will be subject to the following routine uses:

1. The information on this form will be treated confidentially to the extent allowed under the Freedom of Information Act (5 U.S.C. 552) and the Privacy Act (5 U.S.C. 552a). Records from this system of records may be disclosed to the Department of Justice to determine whether the Freedom of Information Act requires disclosure of these records.
2. A record from this system of records may be disclosed, as a routine use, in the course of presenting evidence to a court, magistrate, or administrative tribunal, including disclosures to opposing counsel in the course of settlement negotiations.
3. A record in this system of records may be disclosed, as a routine use, to a Member of Congress submitting a request involving an individual, to whom the record pertains, when the individual has requested assistance from the Member with respect to the subject matter of the record.
4. A record in this system of records may be disclosed, as a routine use, to a contractor of the Agency having need for the information in order to perform a contract. Recipients of information shall be required to comply with the requirements of the Privacy Act of 1974, as amended, pursuant to 5 U.S.C. 552a(m).
5. A record related to an International Application filed under the Patent Cooperation Treaty in this system of records may be disclosed, as a routine use, to the International Bureau of the World Intellectual Property Organization, pursuant to the Patent Cooperation Treaty.
6. A record in this system of records may be disclosed, as a routine use, to another federal agency for purposes of National Security review (35 U.S.C. 181) and for review pursuant to the Atomic Energy Act (42 U.S.C. 218(c)).
7. A record from this system of records may be disclosed, as a routine use, to the Administrator, General Services, or his/her designee, during an inspection of records conducted by GSA as part of that agency's responsibility to recommend improvements in records management practices and programs, under authority of 44 U.S.C. 2904 and 2906. Such disclosure shall be made in accordance with the GSA regulations governing inspection of records for this purpose, and any other relevant (i.e., GSA or Commerce) directive. Such disclosure shall not be used to make determinations about individuals.
8. A record from this system of records may be disclosed, as a routine use, to the public after either publication of the application pursuant to 35 U.S.C. 122(b) or issuance of a patent pursuant to 35 U.S.C. 151. Further, a record may be disclosed, subject to the limitations of 37 CFR 1.14, as a routine use, to the public if the record was filed in an application which became abandoned or in which the proceedings were terminated and which application is referenced by either a published application, an application open to public inspections or an issued patent.
9. A record from this system of records may be disclosed, as a routine use, to a Federal, State, or local law enforcement agency, if the USPTO becomes aware of a violation or potential violation of law or regulation.

Application Number : 11/213,215 Confirmation Number: 9248
Applicant : Lynn R. Youngs
Filed : 25 August 2005
TC/A.U. : 2116
Examiner : Stoynov, Stefan

Docket Number : APL-P2766-C2
Customer No. : 62,096

Amendment After Notice of Allowance
VIA Electronic Filing

AMENDMENT

Sir:

In response to the notice of allowance of **19 December 2008**, applicant wishes to re-open prosecution. Please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 8 of this paper.

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims
in the application:

Listing of Claims:

- 1 1-42 (Cancelled)

- 1 43. (Cancelled)

- 1 44. (Cancelled)

- 1 45. (Cancelled)

- 1 46. (Cancelled)

- 1 47. (Cancelled)

- 1 49. (Cancelled)

- 1 50. (Cancelled)

- 1 51. (Cancelled)

- 1 52. (Cancelled)

- 1 53. (Cancelled)

- 1 54. ((Cancelled)
- 1 55. (Cancelled)
- 1 56. (Cancelled)
- 1 57. (Cancelled)
- 1 58. (Cancelled)
- 1 59. (Cancelled)
- 1 60. (Cancelled)
- 1 61. (Cancelled)
- 1 62. (Cancelled)
- 1 63. (Cancelled)
- 1 64. (New) An instruction-processing system with minimal static power
2 leakage, the instruction-processing system comprising:
3 a core with instruction-processing circuitry;
4 an area coupled to the core;
5 a core voltage provided to the core; and
6 an area voltage provided to the area;
7 wherein in a normal operation mode:
8 a clock signal to the core is active;

9 the core voltage is a first value;
10 the core is active;
11 the area voltage is a second value; and
12 the area is active;
13 wherein in a first power-saving mode that is exited upon receipt of an
14 interrupt signal:
15 the clock signal to the core is inactive;
16 the core voltage is equal to or greater than the first value; and
17 the area voltage is equal to or greater than the second value;
18 wherein in a second power-saving mode that can be exited upon receipt of a
19 signal that is not an interrupt signal:
20 the clock signal to the core is inactive;
21 the core voltage is less than the first value; and
22 the area voltage is equal to or greater than the second value.

1 65. (New) The instruction-processing system of claim 64, wherein the
2 first power-saving mode can be exited upon receipt of a signal that is not an
3 interrupt signal.

1 66. (New) The instruction-processing system of claim 64, wherein the
2 area comprises a cache.

1 67. (New) The instruction-processing system of claim 66, wherein the
2 area further comprises cache tags.

1 68. (New) The instruction-processing system of claim 64, wherein prior
2 to entering the second power-saving mode, the state of the core is saved to a
3 memory.

1 69. (New) The instruction-processing system of claim 64, wherein upon
2 exiting the second power-saving mode, the state of the core is restored.

1 70. (New) The instruction-processing system of claim 64, wherein in the
2 second power-saving mode, the core voltage is at zero.

1 71. (New) A method for minimizing static power leakage in an
2 instruction-processing system, wherein the instruction-processing system comprises
3 a core with instruction-processing circuitry, an area coupled to the core, a core
4 voltage provided to the core, and an area voltage provided to the area, the method
5 comprising:

6 entering a normal operation mode by:
7 providing a clock signal to the core;
8 providing the core with a core voltage that is equal to a first value;
9 providing the area with an area voltage that is equal to a second
10 value;
11 entering a first power-saving mode by:
12 disabling the clock signal to the core;
13 providing the core with a core voltage that is equal to or greater than
14 the first value; and
15 providing the area with an area voltage that is equal to or greater than
16 the second value;
17 exiting the first power-saving mode upon receipt of an interrupt signal;
18 entering a second power-saving mode by:
19 disabling the clock signal to the core;
20 setting the core voltage to a value less than the first value; and
21 providing the area with an area voltage that is equal to or greater than
22 the second value; and
23 exiting the second power-saving mode upon receipt of a signal that is not an
24 interrupt signal.

1 72. (New) The method of claim 71, further comprising exiting the first
2 power-saving mode upon receipt of a signal that is not an interrupt signal.

1 73. (New) The instruction-processing system of claim 71, wherein the
2 area comprises a cache.

1 74. (New) The method of claim 73, wherein the area further comprises
2 cache tags.

1 75. (New) The method of claim 71, further comprising saving the state
2 of the core to a memory prior to entering the second power-saving mode.

1 76. (New) The method of claim 71, further comprising restoring the
2 state of the core upon exiting the second power-saving mode.

1 77. (New) The method of claim 71, wherein in the second power-saving
2 mode, setting the core voltage to the value less than the first value comprises setting
3 the core voltage to zero.

1 78. (New) A computer-readable medium containing data representing an
2 instruction-processing system with minimal static power leakage, the instruction-
3 processing system comprising:
4 a core with instruction-processing circuitry;
5 an area coupled to the core;
6 a core voltage provided to the core; and
7 an area voltage provided to the area;
8 wherein in a normal operation mode:
9 a clock signal to the core is active;
10 the core voltage is a first value;

11 the core is active;
12 the area voltage is a second value; and
13 the area is active;
14 wherein in a first power-saving mode that is exited upon receipt of an
15 interrupt signal:
16 the clock signal to the core is inactive;
17 the core voltage is equal to or greater than the first value; and
18 the area voltage is equal to or greater than the second value;
19 wherein in a second power-saving mode that can be exited upon receipt of a
20 signal that is not an interrupt signal:
21 the clock signal to the core is inactive;
22 the core voltage is less than the first value; and
23 the area voltage is equal to or greater than the second value.

1 79. (New) The computer-readable medium of claim 78, wherein the first
2 power-saving mode can be exited upon receipt of a signal that is not an interrupt
3 signal.

1 80. (New) The computer-readable medium of claim 78, wherein the area
2 comprises a cache.

1 81. (New) The computer-readable medium of claim 80, wherein the area
2 further comprises cache tags.

1 82. (New) The computer-readable medium of claim 78, wherein prior to
2 entering the second power-saving mode, the state of the core is saved to a memory.

1 83. (New) The computer-readable medium of claim 78, wherein upon
2 exiting the second power-saving mode, the state of the core is restored.

1 84. (New) The computer-readable medium stem of claim 78, wherein in
2 the second power-saving mode, the core voltage is at zero.

REMARKS

In response to the Notice of Allowance mailed **19 December 2007**, applicant wishes to re-open prosecution. Applicant has cancelled claims 43-63, and added new claims 64-84. These new claims find support at least in paragraphs [0019], [0021], [0022], [0024]-[0026], [0029]-[0031], and FIGs. 1A, 1B, and 2 of the instant application. No new matter has been added.

CONCLUSION

It is submitted that the present application is presently in form for allowance. Such action is respectfully requested.

Respectfully submitted,

By /Shun Yao/
Shun Yao
Registration No. 59,242

Date: 26 February 2008

Shun Yao
Park, Vaughan & Fleming LLP
2820 Fifth Street
Davis, CA 95618-7759
Tel: (530) 759-1667
Fax: (530) 759-1665
Email: shun@parklegal.com

Electronic Patent Application Fee Transmittal

Application Number:	11213215			
Filing Date:	25-Aug-2005			
Title of Invention:	CONSERVING POWER BY REDUCING VOLTAGE SUPPLIED TO AN INSTRUCTION-PROCESSING PORTION OF A PROCESSOR			
First Named Inventor/Applicant Name:	Lynn R. Youngs			
Filer:	A.Richard Park			
Attorney Docket Number:	APL-P2766-C2			
Filed as Large Entity				
Utility Filing Fees				
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Basic Filing:				
Pages:				
Claims:				
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				
Extension-of-Time:				

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Request for continued examination	1801	1	810	810
Total in USD (\$)				810

Electronic Acknowledgement Receipt

EFS ID:	2909288
Application Number:	11213215
International Application Number:	
Confirmation Number:	9248
Title of Invention:	CONSERVING POWER BY REDUCING VOLTAGE SUPPLIED TO AN INSTRUCTION-PROCESSING PORTION OF A PROCESSOR
First Named Inventor/Applicant Name:	Lynn R. Youngs
Customer Number:	62096
Filer:	A.Richard Park
Filer Authorized By:	
Attorney Docket Number:	APL-P2766-C2
Receipt Date:	26-FEB-2008
Filing Date:	25-AUG-2005
Time Stamp:	12:50:30
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	yes
Payment Type	Credit Card
Payment was successfully received in RAM	\$810
RAM confirmation Number	8172
Deposit Account	
Authorized User	

File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
-----------------	----------------------	-----------	----------------------------------	------------------	------------------

1	Request for Continued Examination (RCE)	RCEe_fill.pdf	737847 6a958188366820e6930deb60d076f4f0 5a5feab4	no	3
Warnings:					
Information:					
2		APL-P2766-C2_Amendment _E.pdf	100281 734c975bac3a806bc14d5d4313e6b62 41e00bdc9	yes	10
Multipart Description/PDF files in .zip description					
Document Description		Start	End		
Amendment after Notice of Allowance (Rule 312)		1	1		
Claims		2	8		
Applicant Arguments/Remarks Made in an Amendment		9	10		
Warnings:					
Information:					
3	Fee Worksheet (PTO-06)	fee-info.pdf	8216 fe6ec9563c5abd99445be35247530ea fdcc8dd	no	2
Warnings:					
Information:					
Total Files Size (in bytes):			846344		
<p>This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.</p> <p><u>New Applications Under 35 U.S.C. 111</u> If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.</p> <p><u>National Stage of an International Application under 35 U.S.C. 371</u> If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.</p> <p><u>New International Application Filed with the USPTO as a Receiving Office</u> If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.</p>					

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

PATENT APPLICATION FEE DETERMINATION RECORD Substitute for Form PTO-875	Application or Docket Number 11/213,215	Filing Date 08/25/2005	<input type="checkbox"/> To be Mailed
---	---	----------------------------------	---------------------------------------

APPLICATION AS FILED – PART I			OTHER THAN SMALL ENTITY				
	(Column 1)	(Column 2)	SMALL ENTITY <input type="checkbox"/>	OR			
FOR	NUMBER FILED	NUMBER EXTRA	RATE (\$)	FEE (\$)	OR	RATE (\$)	FEE (\$)
<input type="checkbox"/> BASIC FEE <small>(37 CFR 1.16(a), (b), or (c))</small>	N/A	N/A	N/A			N/A	
<input type="checkbox"/> SEARCH FEE <small>(37 CFR 1.16(k), (l), or (m))</small>	N/A	N/A	N/A			N/A	
<input type="checkbox"/> EXAMINATION FEE <small>(37 CFR 1.16(o), (p), or (q))</small>	N/A	N/A	N/A			N/A	
TOTAL CLAIMS <small>(37 CFR 1.16(i))</small>	minus 20 =	*	X \$ =			X \$ =	
INDEPENDENT CLAIMS <small>(37 CFR 1.16(h))</small>	minus 3 =	*	X \$ =			X \$ =	
<input type="checkbox"/> APPLICATION SIZE FEE <small>(37 CFR 1.16(s))</small>	If the specification and drawings exceed 100 sheets of paper, the application size fee due is \$250 (\$125 for small entity) for each additional 50 sheets or fraction thereof. See 35 U.S.C. 41(a)(1)(G) and 37 CFR 1.16(s).						
<input type="checkbox"/> MULTIPLE DEPENDENT CLAIM PRESENT <small>(37 CFR 1.16(j))</small>							
* If the difference in column 1 is less than zero, enter "0" in column 2.			TOTAL			TOTAL	

APPLICATION AS AMENDED – PART II					OTHER THAN SMALL ENTITY				
	(Column 1)	(Column 2)	(Column 3)		SMALL ENTITY	OR			
AMENDMENT	02/28/2008	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST NUMBER PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE (\$)	ADDITIONAL FEE (\$)	OR	RATE (\$)	ADDITIONAL FEE (\$)
	Total <small>(37 CFR 1.16(i))</small>	* 21	Minus	** 22	=	0		X \$50=	0
	Independent <small>(37 CFR 1.16(h))</small>	* 3	Minus	*** 3	=	0		X \$210=	0
<input type="checkbox"/> Application Size Fee <small>(37 CFR 1.16(s))</small>									
<input type="checkbox"/> FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM <small>(37 CFR 1.16(j))</small>									
* If the entry in column 1 is less than the entry in column 2, write "0" in column 3.					TOTAL ADD'L FEE			TOTAL ADD'L FEE	0

	(Column 1)	(Column 2)	(Column 3)		SMALL ENTITY	OR			
AMENDMENT	Total <small>(37 CFR 1.16(i))</small>	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST NUMBER PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE (\$)	ADDITIONAL FEE (\$)	OR	RATE (\$)	ADDITIONAL FEE (\$)
	*	Minus	**	=	X \$ =			X \$ =	
	Independent <small>(37 CFR 1.16(h))</small>	*	Minus	***	=	X \$ =		X \$ =	
<input type="checkbox"/> Application Size Fee <small>(37 CFR 1.16(s))</small>									
<input type="checkbox"/> FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM <small>(37 CFR 1.16(j))</small>									
* If the entry in column 1 is less than the entry in column 2, write "0" in column 3.					TOTAL ADD'L FEE			TOTAL ADD'L FEE	

** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 20, enter "20".
 *** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 3, enter "3".
 The "Highest Number Previously Paid For" (Total or Independent) is the highest number found in the appropriate box in column 1.

Legal Instrument Examiner:
/INEZ R. MCMILLIAN/

This collection of information is required by 37 CFR 1.16. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

EXHIBIT C



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

NOTICE OF ALLOWANCE AND FEE(S) DUE

62096 7590 04/08/2008
PVF -- APPLE COMPUTER, INC.
c/o PARK, VAUGHAN & FLEMING LLP
2820 FIFTH STREET
DAVIS, CA 95618-7759

EXAMINER
STOYNOV, STEFAN
ART UNIT PAPER NUMBER
2116
DATE MAILED: 04/08/2008

Table with 5 columns: APPLICATION NO., FILING DATE, FIRST NAMED INVENTOR, ATTORNEY DOCKET NO., CONFIRMATION NO.

TITLE OF INVENTION: CONSERVING POWER BY REDUCING VOLTAGE SUPPLIED TO AN INSTRUCTION-PROCESSING PORTION OF A PROCESSOR

Table with 7 columns: APPLN. TYPE, SMALL ENTITY, ISSUE FEE DUE, PUBLICATION FEE DUE, PREV. PAID ISSUE FEE, TOTAL FEE(S) DUE, DATE DUE

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOVE DOES NOT REFLECT A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE IN THIS APPLICATION. IF AN ISSUE FEE HAS PREVIOUSLY BEEN PAID IN THIS APPLICATION (AS SHOWN ABOVE), THE RETURN OF PART B OF THIS FORM WILL BE CONSIDERED A REQUEST TO REAPPLY THE PREVIOUSLY PAID ISSUE FEE TOWARD THE ISSUE FEE NOW DUE.

HOW TO REPLY TO THIS NOTICE:

I. Review the SMALL ENTITY status shown above.

If the SMALL ENTITY is shown as YES, verify your current SMALL ENTITY status:

- A. If the status is the same, pay the TOTAL FEE(S) DUE shown above.
B. If the status above is to be removed, check box 5b on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and twice the amount of the ISSUE FEE shown above, or

If the SMALL ENTITY is shown as NO:

- A. Pay TOTAL FEE(S) DUE shown above, or
B. If applicant claimed SMALL ENTITY status before, or is now claiming SMALL ENTITY status, check box 5a on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and 1/2 the ISSUE FEE shown above.

II. PART B - FEE(S) TRANSMITTAL, or its equivalent, must be completed and returned to the United States Patent and Trademark Office (USPTO) with your ISSUE FEE and PUBLICATION FEE (if required). If you are charging the fee(s) to your deposit account, section "4b" of Part B - Fee(s) Transmittal should be completed and an extra copy of the form should be submitted. If an equivalent of Part B is filed, a request to reapply a previously paid issue fee must be clearly made, and delays in processing may occur due to the difficulty in recognizing the paper as an equivalent of Part B.

III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

PART B - FEE(S) TRANSMITTAL

Complete and send this form, together with applicable fee(s), to: **Mail** **Mail Stop ISSUE FEE**
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450
 or **Fax** **(571)-273-2885**

INSTRUCTIONS: This form should be used for transmitting the ISSUE FEE and PUBLICATION FEE (if required). Blocks 1 through 5 should be completed where appropriate. All further correspondence including the Patent, advance orders and notification of maintenance fees will be mailed to the current correspondence address as indicated unless corrected below or directed otherwise in Block 1, by (a) specifying a new correspondence address; and/or (b) indicating a separate "FEE ADDRESS" for maintenance fee notifications.

CURRENT CORRESPONDENCE ADDRESS (Note: Use Block 1 for any change of address)

Note: A certificate of mailing can only be used for domestic mailings of the Fee(s) Transmittal. This certificate cannot be used for any other accompanying papers. Each additional paper, such as an assignment or formal drawing, must have its own certificate of mailing or transmission.

62096 7590 04/08/2008

PVF -- APPLE COMPUTER, INC.
 c/o PARK, VAUGHAN & FLEMING LLP
 2820 FIFTH STREET
 DAVIS, CA 95618-7759

Certificate of Mailing or Transmission

I hereby certify that this Fee(s) Transmittal is being deposited with the United States Postal Service with sufficient postage for first class mail in an envelope addressed to the Mail Stop ISSUE FEE address above, or being facsimile transmitted to the USPTO (571) 273-2885, on the date indicated below.

(Depositor's name)
(Signature)
(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
11/213,215	08/25/2005	Lynn R. Youngs	APL-P2766-C2	9248

TITLE OF INVENTION: CONSERVING POWER BY REDUCING VOLTAGE SUPPLIED TO AN INSTRUCTION-PROCESSING PORTION OF A PROCESSOR

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1440	\$300	\$0	\$1740	07/08/2008

EXAMINER	ART UNIT	CLASS-SUBCLASS
STOYNOV, STEFAN	2116	713-300000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.563).
 Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
 "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. **Use of a Customer Number is required.**

2. For printing on the patent front page, list
 (1) the names of up to 3 registered patent attorneys or agents OR, alternatively, 1 _____
 (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed. 2 _____
 3 _____

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)
 PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE _____ (B) RESIDENCE: (CITY and STATE OR COUNTRY) _____

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

4a. The following fee(s) are submitted:
 Issue Fee
 Publication Fee (No small entity discount permitted)
 Advance Order - # of Copies _____

4b. Payment of Fee(s): (Please first reapply any previously paid issue fee shown above)
 A check is enclosed.
 Payment by credit card. Form PTO-2038 is attached.
 The Director is hereby authorized to charge the required fee(s), any deficiency, or credit any overpayment, to Deposit Account Number _____ (enclose an extra copy of this form).

5. Change in Entity Status (from status indicated above)
 a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

NOTE: The Issue Fee and Publication Fee (if required) will not be accepted from anyone other than the applicant; a registered attorney or agent; or the assignee or other party in interest as shown by the records of the United States Patent and Trademark Office.

Authorized Signature _____ Date _____
 Typed or printed name _____ Registration No. _____

This collection of information is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

Table with 5 columns: APPLICATION NO., FILING DATE, FIRST NAMED INVENTOR, ATTORNEY DOCKET NO., CONFIRMATION NO.
Row 1: 11/213,215, 08/25/2005, Lynn R. Youngs, APL-P2766-C2, 9248
Row 2: 62096, 7590, 04/08/2008, (empty), (empty)
Text: PVF -- APPLE COMPUTER, INC. c/o PARK, VAUGHAN & FLEMING LLP 2820 FIFTH STREET DAVIS, CA 95618-7759
Text: EXAMINER STOYNOV, STEFAN
Text: ART UNIT 2116 PAPER NUMBER
Text: DATE MAILED: 04/08/2008

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)
(application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 55 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 55 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (http://pair.uspto.gov).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 or (571)-272-4200.

Notice of Allowability	Application No.	Applicant(s)	
	11/213,215	YOUNGS, LYNN R.	
	Examiner	Art Unit	
	STEFAN STOYNOV	2116	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. This communication is responsive to communication filed on 02/26/2008.
2. The allowed claim(s) is/are 64-84 (renumbered as claims 1-21).
3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All b) Some* c) None of the:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) hereto or 2) to Paper No./Mail Date _____.
 - (b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

- | | |
|---|--|
| <ol style="list-style-type: none"> 1. <input type="checkbox"/> Notice of References Cited (PTO-892) 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) 3. <input type="checkbox"/> Information Disclosure Statements (PTO/SB/08),
Paper No./Mail Date _____ 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit
of Biological Material | <ol style="list-style-type: none"> 5. <input type="checkbox"/> Notice of Informal Patent Application 6. <input checked="" type="checkbox"/> Interview Summary (PTO-413),
Paper No./Mail Date _____. 7. <input checked="" type="checkbox"/> Examiner's Amendment/Comment 8. <input type="checkbox"/> Examiner's Statement of Reasons for Allowance 9. <input checked="" type="checkbox"/> Other <u>9-page fax, dated 03/26/08, filename
11213215FAX032608.pdf.</u> |
|---|--|

Continuation Sheet (PTOL-37)

Application No.

Interview Summary	Application No. 11/213,215	Applicant(s) YOUNGS, LYNN R.	
	Examiner STEFAN STOYNOV	Art Unit 2116	

All participants (applicant, applicant's representative, PTO personnel):

- (1) STEFAN STOYNOV. (3) _____
(2) SHUN YAO (Reg. No. 59,242). (4) _____

Date of Interview: 20 March 2008.

Type: a) Telephonic b) Video Conference
c) Personal [copy given to: 1) applicant 2) applicant's representative]

Exhibit shown or demonstration conducted: d) Yes e) No.
If Yes, brief description: _____.

Claim(s) discussed: 48, 64, 71, and 78.

Identification of prior art discussed: _____.

Agreement with respect to the claims f) was reached. g) was not reached. h) N/A.

Substance of Interview including description of the general nature of what was agreed to if an agreement was reached, or any other comments: After consultation with supervisor (Rehana Perveen), the examiner contacted the applicant's representative with proposed changes for independent claims 64, 71, and 78, and suggested cancelling omitted claim 48, upon which the application is put in condition for allowance. On 03/26/2008, the applicant's representative submitted a fax (9 pages attached with this Examiner's Amendment) cancelling claim 48 and incorporating the proposed changes for claims 64, 71, and 78.

(A fuller description, if necessary, and a copy of the amendments which the examiner agreed would render the claims allowable, if available, must be attached. Also, where no copy of the amendments that would render the claims allowable is available, a summary thereof must be attached.)

THE FORMAL WRITTEN REPLY TO THE LAST OFFICE ACTION MUST INCLUDE THE SUBSTANCE OF THE INTERVIEW. (See MPEP Section 713.04). If a reply to the last Office action has already been filed, APPLICANT IS GIVEN A NON-EXTENDABLE PERIOD OF THE LONGER OF ONE MONTH OR THIRTY DAYS FROM THIS INTERVIEW DATE, OR THE MAILING DATE OF THIS INTERVIEW SUMMARY FORM, WHICHEVER IS LATER, TO FILE A STATEMENT OF THE SUBSTANCE OF THE INTERVIEW. See Summary of Record of Interview requirements on reverse side or on attached sheet.

/Stefan Stoynov/
Examiner, Art Unit 2116

Examiner Note: You must sign this form unless it is an Attachment to a signed Office action. _____
Examiner's signature, if required

Summary of Record of Interview Requirements

Manual of Patent Examining Procedure (MPEP), Section 713.04, Substance of Interview Must be Made of Record

A complete written statement as to the substance of any face-to-face, video conference, or telephone interview with regard to an application must be made of record in the application whether or not an agreement with the examiner was reached at the interview.

Title 37 Code of Federal Regulations (CFR) § 1.133 Interviews Paragraph (b)

In every instance where reconsideration is requested in view of an interview with an examiner, a complete written statement of the reasons presented at the interview as warranting favorable action must be filed by the applicant. An interview does not remove the necessity for reply to Office action as specified in §§ 1.111, 1.135. (35 U.S.C. 132)

37 CFR §1.2 Business to be transacted in writing.

All business with the Patent or Trademark Office should be transacted in writing. The personal attendance of applicants or their attorneys or agents at the Patent and Trademark Office is unnecessary. The action of the Patent and Trademark Office will be based exclusively on the written record in the Office. No attention will be paid to any alleged oral promise, stipulation, or understanding in relation to which there is disagreement or doubt.

The action of the Patent and Trademark Office cannot be based exclusively on the written record in the Office if that record is itself incomplete through the failure to record the substance of interviews.

It is the responsibility of the applicant or the attorney or agent to make the substance of an interview of record in the application file, unless the examiner indicates he or she will do so. It is the examiner's responsibility to see that such a record is made and to correct material inaccuracies which bear directly on the question of patentability.

Examiners must complete an Interview Summary Form for each interview held where a matter of substance has been discussed during the interview by checking the appropriate boxes and filling in the blanks. Discussions regarding only procedural matters, directed solely to restriction requirements for which interview recordation is otherwise provided for in Section 812.01 of the Manual of Patent Examining Procedure, or pointing out typographical errors or unreadable script in Office actions or the like, are excluded from the interview recordation procedures below. Where the substance of an interview is completely recorded in an Examiners Amendment, no separate Interview Summary Record is required.

The Interview Summary Form shall be given an appropriate Paper No., placed in the right hand portion of the file, and listed on the "Contents" section of the file wrapper. In a personal interview, a duplicate of the Form is given to the applicant (or attorney or agent) at the conclusion of the interview. In the case of a telephone or video-conference interview, the copy is mailed to the applicant's correspondence address either with or prior to the next official communication. If additional correspondence from the examiner is not likely before an allowance or if other circumstances dictate, the Form should be mailed promptly after the interview rather than with the next official communication.

The Form provides for recordation of the following information:

- Application Number (Series Code and Serial Number)
- Name of applicant
- Name of examiner
- Date of interview
- Type of interview (telephonic, video-conference, or personal)
- Name of participant(s) (applicant, attorney or agent, examiner, other PTO personnel, etc.)
- An indication whether or not an exhibit was shown or a demonstration conducted
- An identification of the specific prior art discussed
- An indication whether an agreement was reached and if so, a description of the general nature of the agreement (may be by attachment of a copy of amendments or claims agreed as being allowable). Note: Agreement as to allowability is tentative and does not restrict further action by the examiner to the contrary.
- The signature of the examiner who conducted the interview (if Form is not an attachment to a signed Office action)

It is desirable that the examiner orally remind the applicant of his or her obligation to record the substance of the interview of each case. It should be noted, however, that the Interview Summary Form will not normally be considered a complete and proper recordation of the interview unless it includes, or is supplemented by the applicant or the examiner to include, all of the applicable items required below concerning the substance of the interview.

A complete and proper recordation of the substance of any interview should include at least the following applicable items:

- 1) A brief description of the nature of any exhibit shown or any demonstration conducted,
- 2) an identification of the claims discussed,
- 3) an identification of the specific prior art discussed,
- 4) an identification of the principal proposed amendments of a substantive nature discussed, unless these are already described on the Interview Summary Form completed by the Examiner,
- 5) a brief identification of the general thrust of the principal arguments presented to the examiner,
(The identification of arguments need not be lengthy or elaborate. A verbatim or highly detailed description of the arguments is not required. The identification of the arguments is sufficient if the general nature or thrust of the principal arguments made to the examiner can be understood in the context of the application file. Of course, the applicant may desire to emphasize and fully describe those arguments which he or she feels were or might be persuasive to the examiner.)
- 6) a general indication of any other pertinent matters discussed, and
- 7) if appropriate, the general results or outcome of the interview unless already described in the Interview Summary Form completed by the examiner.

Examiners are expected to carefully review the applicant's record of the substance of an interview. If the record is not complete and accurate, the examiner will give the applicant an extendable one month time period to correct the record.

Examiner to Check for Accuracy

If the claims are allowable for other reasons of record, the examiner should send a letter setting forth the examiner's version of the statement attributed to him or her. If the record is complete and accurate, the examiner should place the indication, "Interview Record OK" on the paper recording the substance of the interview along with the date and the examiner's initials.

Continuation Sheet (PTOL-413)

Application No.

EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it **MUST** be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Shun Yao, Reg. No. 59,242 on 03/26/2008.

The application has been amended as follows:

Amend the claims as follows:

See attached fax from Applicant's Representative (9 pages, dated 03/26/2008) cancelling claims 1-63 and amending claims 64, 71, 73, 78, and 84.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to STEFAN STOYNOV whose telephone number is (571)272-4236. The examiner can normally be reached on 8:30AM-5:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Rehana Perveen can be reached on (571) 272-3676. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.


Application/Control Number: 11/213,215
Art Unit: 2116

Page 3

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/S. S./
Examiner, Art Unit 2116

/Rehana Perveen/
Supervisory Patent Examiner, Art Unit 2116

Index of Claims 	Application/Control No. 11213215	Applicant(s)/Patent Under Reexamination YOUNGS, LYNN R.
	Examiner STEFAN STOYNOV	Art Unit 2116

✓	Rejected
=	Allowed


-	Cancelled
÷	Restricted

N	Non-Elected
I	Interference

A	Appeal
O	Objected

Claims renumbered in the same order as presented by applicant
 CPA
 T.D.
 R.1.47

CLAIM		DATE									
Final	Original	03/26/2008									
	1	-									
	2	-									
	3	-									
	4	-									
	5	-									
	6	-									
	7	-									
	8	-									
	9	-									
	10	-									
	11	-									
	12	-									
	13	-									
	14	-									
	15	-									
	16	-									
	17	-									
	18	-									
	19	-									
	20	-									
	21	-									
	22	-									
	23	-									
	24	-									
	25	-									
	26	-									
	27	-									
	28	-									
	29	-									
	30	-									
	31	-									
	32	-									
	33	-									
	34	-									
	35	-									
	36	-									

Index of Claims 	Application/Control No. 11213215	Applicant(s)/Patent Under Reexamination YOUNGS, LYNN R.
	Examiner STEFAN STOYNOV	Art Unit 2116

✓	Rejected
=	Allowed


-	Cancelled
÷	Restricted

N	Non-Elected
I	Interference

A	Appeal
O	Objected

Claims renumbered in the same order as presented by applicant
 CPA
 T.D.
 R.1.47

CLAIM		DATE									
Final	Original	03/26/2008									
	37	-									
	38	-									
	39	-									
	40	-									
	41	-									
	42	-									
	43	-									
	44	-									
	45	-									
	46	-									
	47	-									
	48	-									
	49	-									
	50	-									
	51	-									
	52	-									
	53	-									
	54	-									
	55	-									
	56	-									
	57	-									
	58	-									
	59	-									
	60	-									
	61	-									
	62	-									
	63	-									
1	64	=									
2	65	=									
3	66	=									
4	67	=									
5	68	=									
6	69	=									
7	70	=									
8	71	=									
9	72	=									

Index of Claims 	Application/Control No. 11213215	Applicant(s)/Patent Under Reexamination YOUNGS, LYNN R.
	Examiner STEFAN STOYNOV	Art Unit 2116

✓	Rejected
=	Allowed

-	Cancelled
÷	Restricted

N	Non-Elected
I	Interference

A	Appeal
O	Objected

Claims renumbered in the same order as presented by applicant
 CPA
 T.D.
 R.1.47

CLAIM		DATE								
Final	Original	03/26/2008								
10	73	=								
11	74	=								
12	75	=								
13	76	=								
14	77	=								
15	78	=								
16	79	=								
17	80	=								
18	81	=								
19	82	=								
20	83	=								
21	84	=								

Issue Classification



Application/Control No. 11213215	Applicant(s)/Patent Under Reexamination YOUNGS, LYNN R.
Examiner STEFAN STOYNOV	Art Unit 2116

ORIGINAL					INTERNATIONAL CLASSIFICATION								
CLASS		SUBCLASS			CLAIMED				NON-CLAIMED				
713		300			G	0	8	F	1 / 00 (2006.01.01)				
					G	0	6	F	1 / 26 (2006.01.01)				
CROSS REFERENCE(S)													
CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)												
713	320	324											

/Stefan Stoynov/ (Assistant Examiner)	03/26/2008 (Date)		Total Claims Allowed: 21	
/Rehana Perveen/ (Primary Examiner)	3/26/08 (Date)		O.G. Print Claim(s) 64	O.G. Print Figure 1A

Search Notes



Application/Control No.

11/213,215

Examiner

STEFAN STOYNOV

Applicant(s)/Patent under Reexamination

YOUNGS, LYNN R.

Art Unit

2116

SS

SEARCHED

Class	Subclass	Date	Examiner
713	300	8/14/2007	SS
713	300	12/7/2007	SS
713	300	3/26/2008	SS

INTERFERENCE SEARCHED

Class	Subclass	Date	Examiner
See attached Interference Search printout.		3/26/2008	SS

SEARCH NOTES (INCLUDING SEARCH STRATEGY)

	DATE	EXMR
EAST text search for USPAT, US-PGPUB, JPO, EPO, DERWENT, IBM_TDB (see attached search history printout).	8/14/2007	SS
713/300,320,322,324 text search only for USPAT, US-PGPUB, JPO, EPO, DERWENT, IBM_TDB (see attached search history printout).	8/14/2007	SS
Inventor's search.	8/14/2007	SS
Updated EAST text search for USPAT, US-PGPUB, JPO, EPO, DERWENT, IBM_TDB (see attached search history printout)	7/17/2007	SS
Updated Inventor's search.	7/17/2007	SS
Updated search (see attached file).	3/26/2008	SS
Updated Inventor's search.	3/26/2008	SS

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L35	0	((reduc\$3 or deareas\$3 or minum\$2 or minimiz\$3 or low\$5) near6 leakage\$1) and (core near2 area) and (core adj voltage\$1) and ((maintain\$3 or keep\$3) near6 ((process\$3 or microprocessor\$1 or cpu\$1) near2 state\$1))).clm.	US-PGPUB; USPAT	OR	OFF	2008/03/26 18:27
L36	0	((reduc\$3 or deareas\$3 or minum\$2 or minimiz\$3 or low\$5) near6 leakage\$1) and (core near2 area) and (core adj voltage\$1) and ((maintain\$3 or keep\$3) near6 ((process\$3 or microprocessor\$1 or cpu\$1) near6 state\$1))).clm.	US-PGPUB; USPAT	OR	OFF	2008/03/26 18:28
L37	0	((reduc\$3 or deareas\$3 or minum\$2 or minimiz\$3 or low\$5) near6 leakage\$1) and core and ((maintain\$3 or keep\$3) near6 ((process\$3 or microprocessor\$1 or cpu\$1) near6 state\$1))).clm.	US-PGPUB; USPAT	OR	OFF	2008/03/26 18:28
L38	0	((core near2 area) and (core adj voltage\$1) and ((maintain\$3 or keep\$3) near6 ((process\$3 or microprocessor\$1 or cpu\$1) near6 state\$1))).clm.	US-PGPUB; USPAT	OR	OFF	2008/03/26 18:29

EAST Search History

L39	0	((core near3 area) and (core adj voltage \$1) and ((maintain\$3 or keep\$3) near6 ((process\$3 or microprocessor\$1 or cpu\$1) near6 state \$1))).clm.	US-PGPUB; USPAT	OR	OFF	2008/03/26 18:29
L40	0	((((reduc\$3 or decreas \$3 or minum\$2 or minimiz\$3 or low\$5) near6 leakage\$1) and (core near3 area) and (core adj voltage\$1) and ((process\$3 or microprocessor\$1 or cpu\$1) near6 state \$1))).clm.	US-PGPUB; USPAT	OR	OFF	2008/03/26 18:31
L41	0	((core near3 area) and (core adj voltage \$1) and ((process\$3 or microprocessor\$1 or cpu\$1) near6 state \$1)).clm.	US-PGPUB; USPAT	OR	OFF	2008/03/26 18:31

3/ 26/ 2008 6:32:11 PM

C:\Documents and Settings\stoynov1\My Documents\EAST\Workspaces
 \11213215_2.wsp

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	4932	((reduc\$3 or decreas\$3 or minum\$2 or minimiz\$3 or low\$5) near6 leakage\$1) same core	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/03/26 17:44
L3	2574	((reduc\$3 or decreas\$3 or minum\$2 or minimiz\$3 or low\$5) near6 leakage\$1) with core	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/03/26 17:44
L4	355	2 same (process\$3 or microprocessor\$1 or CPU\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/03/26 17:45
L5	77	3 with (process\$3 or microprocessor\$1 or CPU\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/03/26 17:45
L6	65	4 same (area\$1 or segment\$1 or section\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/03/26 17:46
L7	8	5 with (area\$1 or segment\$1 or section\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/03/26 17:46
L8	0	6 same (maintain\$3 or keep\$3) near2 state\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/03/26 17:47
L9	13363	((reduc\$3 or decreas\$3 or minum\$2 or minimiz\$3 or low\$5) near6 voltage\$1) same core	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/03/26 17:54

EAST Search History

L10	6322	((reduc\$3 or decreas\$3 or minum\$2 or minimiz\$3 or low\$5) near6 voltage\$1) with core	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/03/26 17:54
L11	38	9 same ((maintain\$3 or keep\$3) near6 ((process\$3 or microprocessor\$1 or cpu\$1) near2 state \$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/03/26 17:56
L12	32	10 with ((maintain\$3 or keep\$3) near6 ((process\$3 or microprocessor\$1 or cpu\$1) near2 state \$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/03/26 17:56
L13	0	11 same ((maintain\$3 or keep\$3) near6 (data or information))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/03/26 17:59
L14	0	12 with ((maintain\$3 or keep\$3) near6 (data or information))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/03/26 17:59
L15	0	11 same (("non" or "not") adj core)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/03/26 18:00
L16	0	12 with (("non" or "not") adj core)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/03/26 18:00
L17	0	11 same ((plural\$3 or multiple or several or different or first or second or core) near2 (area\$1 or section\$1 or block \$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/03/26 18:02

EAST Search History

L18	0	12 with ((plural\$3 or multiple or several or different or first or second or core) near2 (area\$1 or section\$1 or block \$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/03/26 18:02
L19	22	((reduc\$3 or decreas \$3 or minum\$2 or minimiz\$3 or low\$5) near6 leakage\$1) same ((maintain\$3 or keep\$3) near6 ((process\$3 or microprocessor\$1 or cpu\$1) near2 state \$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/03/26 18:04
L20	1	((reduc\$3 or decreas \$3 or minum\$2 or minimiz\$3 or low\$5) near6 leakage\$1) with ((maintain\$3 or keep\$3) near6 ((process\$3 or microprocessor\$1 or cpu\$1) near2 state \$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/03/26 18:05
L21	1	19 same (core near2 voltage\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/03/26 18:05
L22	80	((reduc\$3 or decreas \$3 or minum\$2 or minimiz\$3 or low\$5) near6 leakage\$1) same (core near2 voltage\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/03/26 18:06
L23	32	((reduc\$3 or decreas \$3 or minum\$2 or minimiz\$3 or low\$5) near6 leakage\$1) with (core near2 voltage\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/03/26 18:07
L24	1	22 same ((maintain \$3 or keep\$3) near6 ((process\$3 or microprocessor\$1 or cpu\$1) near2 state \$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/03/26 18:07

EAST Search History

L25	166	9 and 713/300.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/03/26 18:08
L26	26	25 and ((maintain\$3 or keep\$3) near6 ((process\$3 or microprocessor\$1 or cpu\$1) near2 state \$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/03/26 18:09
L27	87	9 and 713/320.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/03/26 18:10
L28	15	27 and ((maintain\$3 or keep\$3) near6 ((process\$3 or microprocessor\$1 or cpu\$1) near2 state \$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/03/26 18:10
L29	24	9 and 713/324.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/03/26 18:11
L30	3	29 and ((maintain\$3 or keep\$3) near6 ((process\$3 or microprocessor\$1 or cpu\$1) near2 state \$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/03/26 18:11
L31	1	2 same ((maintain\$3 or keep\$3) near6 ((process\$3 or microprocessor\$1 or cpu\$1) near2 state \$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/03/26 18:13
L32	0	3 with ((maintain\$3 or keep\$3) near6 ((process\$3 or microprocessor\$1 or cpu\$1) near2 state \$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/03/26 18:15
L33	12	4 same (core near2 voltage\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/03/26 18:20

EAST Search History

L34	2	5 with (core near2 voltage\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/03/26 18:21
-----	---	-----------------------------------	---	----	-----	---------------------

3/26/2008 6:24:26 PM

C:\Documents and Settings\sstoynov1\My Documents\EAST\Workspaces\11213215_2.
wsp



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
 Address: COMMISSIONER FOR PATENTS
 P.O. Box 1450
 Alexandria, Virginia 22313-1450
 www.uspto.gov

BIB DATA SHEET

CONFIRMATION NO. 9248

SERIAL NUMBER 11/213,215	FILING or 371(c) DATE 08/25/2005 RULE	CLASS 713	GROUP ART UNIT 2116	ATTORNEY DOCKET NO. APL-P2766-C2	
APPLICANTS Lynn R. Youngs, Cupertino, CA; ** CONTINUING DATA ***** <input type="checkbox"/> SS This application is a CON of 11/103,911 04/11/2005 PAT 6,973,585 which is a CON of 10/135,116 04/29/2002 PAT 6,920,574 ** FOREIGN APPLICATIONS ***** <input type="checkbox"/> SS ** IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 09/13/2005					
Foreign Priority claimed <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No 35 USC 119(a-d) conditions met <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No Verified and Acknowledged <u>/STEFAN STOYNOV/</u> Examiner's Signature	<input type="checkbox"/> Met after Allowance SS Initials	STATE OR COUNTRY CA	SHEETS DRAWINGS 3	TOTAL CLAIMS -22 <input type="checkbox"/> 21	INDEPENDENT CLAIMS 3
ADDRESS PVF -- APPLE COMPUTER, INC. c/o PARK, VAUGHAN & FLEMING LLP 2820 FIFTH STREET DAVIS, CA 95618-7759 UNITED STATES					
TITLE CONSERVING POWER BY REDUCING VOLTAGE SUPPLIED TO AN INSTRUCTION-PROCESSING PORTION OF A PROCESSOR					
FILING FEE RECEIVED 1100	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		

Application Number : 11/213,215 Confirmation Number: 9248
Applicant : Lynn R. Youngs
Filed : 25 August 2005
TC/A.U. : 2116
Examiner : Stoynov, Stefan

Docket Number : APL-P2766-C2
Customer No. : 62,096

Proposed Amendments
VIA FAX (571) 273-4236

AMENDMENT

Dear Examiner Stoynov:

Please consider the proposed amendments which are reflected in the listing of claims beginning on page 2 of this paper.

1

SY Amendment # APL-P2766-C2 (proposed round 3) doc.

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

- 1 1-42 (Cancelled)
- 1 43. (Cancelled)
- 1 44. (Cancelled)
- 1 45. (Cancelled)
- 1 46. (Cancelled)
- 1 47. (Cancelled)
- 1 48. (Cancelled)
- 1 49. (Cancelled)
- 1 50. (Cancelled)
- 1 51. (Cancelled)
- 1 52. (Cancelled)

1 53. (Cancelled)

1 54. ((Cancelled)

1 55. (Cancelled)

1 56. (Cancelled)

1 57. (Cancelled)

1 58. (Cancelled)

1 59. (Cancelled)

1 60. (Cancelled)

1 61. (Cancelled)

1 62. (Cancelled)

1 63. (Cancelled)

1 64. (Currently Amended) An instruction-processing system with
 2 ~~minimal~~ minimized static power leakage, the instruction-processing system
 3 comprising:
 4 a core with instruction-processing circuitry;
 5 an area coupled to the core;
 6 a core voltage provided to the core; and

7 an area voltage provided to the area;
 8 wherein in a normal operation mode:
 9 a clock signal to the core is active;
 10 the core voltage is a first value that is sufficient to maintain the
 11 state information of the instruction-processing circuitry;
 12 the core is active;
 13 the area voltage is a second value that is sufficient to maintain the
 14 data stored in the area; and
 15 the area is active;
 16 wherein in a first power-saving mode that ~~is~~ can be exited upon receipt of an
 17 interrupt signal:
 18 the clock signal to the core is inactive;
 19 the core voltage is equal to or greater than the first value sufficient to
 20 maintain the state information of the instruction-processing circuitry; and
 21 the area voltage is equal to or greater than the second value sufficient
 22 to maintain the data stored in the area;
 23 wherein in a second power-saving mode that can be exited upon receipt of a
 24 signal that is not an interrupt signal:
 25 the clock signal to the core is inactive;
 26 the core voltage is less than the first value; and
 27 the area voltage is equal to or greater than the second value sufficient
 28 to maintain the data stored in the area.

1 65. (Previously presented) The instruction-processing system of claim
 2 64, wherein the first power-saving mode can be exited upon receipt of a signal that
 3 is not an interrupt signal.

1 66. (Previously presented) The instruction-processing system of claim
 2 64, wherein the area comprises a cache.

1 67. (Previously presented) The instruction-processing system of claim
2 66, wherein the area further comprises cache tags.

1 68. (Previously presented) The instruction-processing system of claim
2 64, wherein prior to entering the second power-saving mode, the state of the core is
3 saved to a memory.

1 69. (Previously presented) The instruction-processing system of claim
2 64, wherein upon exiting the second power-saving mode, the state of the core is
3 restored.

1 70. (Previously presented) The instruction-processing system of claim
2 64, wherein in the second power-saving mode, the core voltage is at zero.

1 71. (Currently amended) A method for minimizing static power leakage
2 in an instruction-processing system, wherein the instruction-processing system
3 comprises a core with instruction-processing circuitry, an area coupled to the core, a
4 core voltage provided to the core, and an area voltage provided to the area, the
5 method comprising:

6 entering a normal operation mode by:
7 providing a clock signal to the core;
8 providing the core with a core voltage that is equal to a first value
9 that is sufficient to maintain the state information of the instruction-
10 processing circuitry;
11 providing the area with an area voltage that is equal to a second
12 value that is sufficient to maintain the data stored in the area;
13 entering a first power-saving mode by:
14 disabling the clock signal to the core;
15 providing the core with a core voltage that is sufficient to
16 maintain the state information of the instruction-processing circuitry equal to

17 or-greater-than
 18 -----the-first-value; and
 19 providing the area with an area voltage that is sufficient
 20 -----to maintain the data stored in the area equal to or-greater-than
 21 -----the-second-value;
 22 exiting the first power-saving mode upon receipt of an interrupt signal;
 23 entering a second power-saving mode by
 24 disabling the clock signal to the core;
 25 setting the core voltage to a value less than the first value; and
 26 providing the area with an area voltage that is equal to or-greater-than
 27 -----the-second-value sufficient to maintain the data stored in the area, and
 28 exiting the second power-saving mode upon receipt of a signal that is not an
 29 interrupt signal.

1 72. (Previously presented) The method of claim 71, further comprising
 2 exiting the first power-saving mode upon receipt of a signal that is not an interrupt
 3 signal.

1 73. (Currently amended) The instruction-processing-system method of
 2 claim 71, wherein the area comprises a cache.

1 74. (Previously presented) The method of claim 73, wherein the area
 2 further comprises cache tags.

1 75. (Previously presented) The method of claim 71, further comprising
 2 saving the state of the core to a memory prior to entering the second power-saving
 3 mode.

1 76. (Previously presented) The method of claim 71, further comprising
 2 restoring the state of the core upon exiting the second power-saving mode.

1 77. (Previously presented) The method of claim 71, wherein in the
2 second power-saving mode, setting the core voltage to the value less than the first
3 value comprises setting the core voltage to zero.

1 78. (Currently amended) A computer-readable medium containing data
2 representing storing code which represents an instruction-processing system with
3 minimal/minimized static power leakage, the instruction-processing system
4 comprising:

5 a core with instruction-processing circuitry;

6 an area coupled to the core;

7 a core voltage provided to the core; and

8 an area voltage provided to the area;

9 wherein in a normal operation mode:

10 a clock signal to the core is active;

11 the core voltage is a first value that is sufficient to maintain the
12 state information of the instruction-processing circuitry;

13 the core is active;

14 the area voltage is a second value that is sufficient to maintain the
15 data stored in the area; and

16 the area is active;

17 wherein in a first power-saving mode that is can be exited upon receipt of an
18 interrupt signal:

19 the clock signal to the core is inactive;

20 the core voltage is sufficient to maintain the state information of the
21 instruction-processing circuitry equal to or greater than the first value; and

22 the area voltage is sufficient to maintain the data stored in the
23 area equal to or greater than the second value;

24 wherein in a second power-saving mode that can be exited upon receipt of a
25 signal that is not an interrupt signal:

26 the clock signal to the core is inactive;

27 the core voltage is less than the first value; and
 28 the area voltage is ~~sufficient to maintain the data stored in the~~
 29 ~~area equal to or greater than the second value.~~

1 79. (Previously presented) The computer-readable medium of claim 78,
 2 wherein the first power-saving mode can be exited upon receipt of a signal that is
 3 not an interrupt signal.

1 80. (Previously presented) The computer-readable medium of claim 78,
 2 wherein the area comprises a cache.

1 81. (Previously presented) The computer-readable medium of claim 80,
 2 wherein the area further comprises cache tags.

1 82. (Previously presented) The computer-readable medium of claim 78,
 2 wherein prior to entering the second power-saving mode, the state of the core is
 3 saved to a memory.

1 83. (Previously presented) The computer-readable medium of claim 78,
 2 wherein upon exiting the second power-saving mode, the state of the core is
 3 restored.

1 84. (Currently amended) The computer-readable medium ~~stem~~ of claim
 2 78, wherein in the second power-saving mode, the core voltage is at zero.

Respectfully submitted,

By /Shun Yao/
Shun Yao
Registration No. 59,242

Date: 26 March 2008

Shun Yao
Park, Vaughan & Fleming LLP
2820 Fifth Street
Davis, CA 95618-7759
Tel: (530) 759-1667
Fax: (530) 759-1665
Email: shun@parklegal.com

0

SY Amendment E APL P2766.1.2 (proposed round 3).doc

EXHIBIT D

PART B - FEE(S) TRANSMITTAL

Complete and send this form, together with applicable fee(s), to: **Mail** Mail Stop ISSUE FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450
or Fax (571)-273-2885

INSTRUCTIONS: This form should be used for transmitting the ISSUE FEE and PUBLICATION FEE (if required). Blocks 1 through 5 should be completed where appropriate. All further correspondence including the Patent, advance orders and notification of maintenance fees will be mailed to the current correspondence address as indicated unless corrected below or directed otherwise in Block 1, by (a) specifying a new correspondence address; and/or (b) indicating a separate "FEE ADDRESS" for maintenance fee notifications.

CURRENT CORRESPONDENCE ADDRESS (Note: Use Block 1 for any change of address)

Note: A certificate of mailing can only be used for domestic mailings of the Fee(s) Transmittal. This certificate cannot be used for any other accompanying papers. Each additional paper, such as an assignment or formal drawing, must have its own certificate of mailing or transmission.

Certificate of Mailing or Transmission

I hereby certify that this Fee(s) Transmittal is being deposited with the United States Postal Service with sufficient postage for first class mail in an envelope addressed to the Mail Stop ISSUE FEE address above, or being facsimile transmitted to the USPTO (571) 273-2885, on the date indicated below.

_____ (Depositor's name)
_____ (Signature)
_____ (Date)

62096 7590 04/08/2008
 PVF -- APPLE COMPUTER, INC.
 c/o PARK, VAUGHAN & FLEMING LLP
 2820 FIFTH STREET
 DAVIS, CA 95618-7759

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
11/213,215	08/25/2005	Lynn R. Youngs	APL-P2766-C2	9248

TITLE OF INVENTION: CONSERVING POWER BY REDUCING VOLTAGE SUPPLIED TO AN INSTRUCTION-PROCESSING PORTION OF A PROCESSOR

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1440	\$300	\$0	\$1740	07/08/2008

EXAMINER	ART UNIT	CLASS-SUBCLASS
STOYNOV, STEFAN	2116	713-300000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).
 Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
 "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. Use of a Customer Number is required.

2. For printing on the patent front page, list
 (1) the names of up to 3 registered patent attorneys or agents OR, alternatively,
 (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.
 1 Park, Vaughan & Fleming, LLP
 2 Fleming, LLP
 3 Edward J. Grundler

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)
 PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE: Apple, Inc (B) RESIDENCE: (CITY and STATE OR COUNTRY) Cupertino, CA

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

4a. The following fee(s) are submitted:
 Issue Fee
 Publication Fee (No small entity discount permitted)
 Advance Order - # of Copies 12

4b. Payment of Fee(s): (Please first reapply any previously paid issue fee shown above)
 A check is enclosed.
 Payment by credit card. Form PTO-2038 is attached.
 The Director is hereby authorized to charge the required fee(s), any deficiency, or credit any overpayment, to Deposit Account Number _____ (enclose an extra copy of this form).

5. Change in Entity Status (from status indicated above)
 a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

NOTE: The Issue Fee and Publication Fee (if required) will not be accepted from anyone other than the applicant; a registered attorney or agent; or the assignee or other party in interest as shown by the records of the United States Patent and Trademark Office.

Authorized Signature: Edward J. Grundler Date: 16 April 2008
 Typed or printed name: Edward J. Grundler Registration No.: 47,615

This collection of information is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

Electronic Patent Application Fee Transmittal

Application Number:	11213215			
Filing Date:	25-Aug-2005			
Title of Invention:	CONSERVING POWER BY REDUCING VOLTAGE SUPPLIED TO AN INSTRUCTION-PROCESSING PORTION OF A PROCESSOR			
First Named Inventor/Applicant Name:	Lynn R. Youngs			
Filer:	A.Richard Park			
Attorney Docket Number:	APL-P2766-C2			
Filed as Large Entity				
Utility Filing Fees				
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Basic Filing:				
Pages:				
Claims:				
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				
Utility Appl issue fee	1501	1	1440	1440
Publ. Fee- early, voluntary, or normal	1504	1	300	300

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Extension-of-Time:				
Miscellaneous:				
Printed copy of patent - no color	8001	12	3	36
Total in USD (\$)				1776

Electronic Acknowledgement Receipt

EFS ID:	3164497
Application Number:	11213215
International Application Number:	
Confirmation Number:	9248
Title of Invention:	CONSERVING POWER BY REDUCING VOLTAGE SUPPLIED TO AN INSTRUCTION-PROCESSING PORTION OF A PROCESSOR
First Named Inventor/Applicant Name:	Lynn R. Youngs
Customer Number:	62096
Filer:	A.Richard Park/Edward Grundler
Filer Authorized By:	A.Richard Park
Attorney Docket Number:	APL-P2766-C2
Receipt Date:	16-APR-2008
Filing Date:	25-AUG-2005
Time Stamp:	18:52:20
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	yes
Payment Type	Credit Card
Payment was successfully received in RAM	\$ 1776
RAM confirmation Number	3335
Deposit Account	
Authorized User	

File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
-----------------	----------------------	-----------	----------------------------------	------------------	------------------

1	Miscellaneous Incoming Letter	Authorization-deposit-account.pdf	20868 1fbc081b27b55386b6362a1237coe587 abb1f014	no	1
Warnings:					
Information:					
2	Issue Fee Payment (PTO-85B)	PTOL-85.pdf	98784 7c134523a033c2f44699410a15da71f3 365b8426	no	1
Warnings:					
Information:					
3	Fee Worksheet (PTO-06)	fee-info.pdf	8493 3dab8e8cee29f9194190b150763foc126 95a2dea	no	2
Warnings:					
Information:					
Total Files Size (in bytes):				128145	
<p>This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.</p> <p><u>New Applications Under 35 U.S.C. 111</u> If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.</p> <p><u>National Stage of an International Application under 35 U.S.C. 371</u> If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.</p> <p><u>New International Application Filed with the USPTO as a Receiving Office</u> If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.</p>					

AUTHORIZATION TO DEDUCT ANY UNDERPAYMENTS
OR CREDIT ANY OVERPAYMENTS TO DEPOSIT
ACCOUNT 50-1003

Please deduct any underpayments, credit any overpayments, and charge all required extension of time fees associated with attached filing to Deposit Account Number 50-1003.

Park, Vaughan & Fleming LLP
2820 Fifth Street
Davis, CA 95618-7759
Tel: (530) 759-1661
Fax: (530) 759-1665
Email: richard@parklegal.com

Respectfully submitted,

By



A. Richard Park
Registration No. 41,241



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	ISSUE DATE	PATENT NO.	ATTORNEY DOCKET NO.	CONFIRMATION NO.
11/213,215	06/03/2008	7383453	APL-P2766-C2	9248

62096 7590 05/14/2008
PVF -- APPLE COMPUTER, INC.
c/o PARK, VAUGHAN & FLEMING LLP
2820 FIFTH STREET
DAVIS, CA 95618-7759

ISSUE NOTIFICATION

The projected patent number and issue date are specified above.

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)
(application filed on or after May 29, 2000)

The Patent Term Adjustment is 55 day(s). Any patent to issue from the above-identified application will include an indication of the adjustment on the front page.

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (<http://pair.uspto.gov>).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at (571)-272-4200.

APPLICANT(s) (Please see PAIR WEB site <http://pair.uspto.gov> for additional applicants):

Lynn R. Youngs, Cupertino, CA;

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 7,383,453
APPLICATION NO. : 11/213,215
DATED : 3 June 2008
INVENTOR(S) : Lynn R. Youngs

It is certified that an error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

In the Assignee Name (on page 1), please delete "Apple, Inc.".

In the Assignee Name (on page 1), please insert --Apple Inc.--.

MAILING ADDRESS OF SENDER:

A. Richard Park
Park, Vaughan & Fleming LLP
2820 Fifth Street
Davis, CA 95618

PATENT NO: 7,383,453

Burden Hour Statement: This form is estimated to take 1.0 hour to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, P.O. Box 1450, Arlington, VA 22313-1450.

Electronic Patent Application Fee Transmittal

Application Number:	11213215			
Filing Date:	25-Aug-2005			
Title of Invention:	CONSERVING POWER BY REDUCING VOLTAGE SUPPLIED TO AN INSTRUCTION-PROCESSING PORTION OF A PROCESSOR			
First Named Inventor/Applicant Name:	Lynn R. Youngs			
Filer:	A.Richard Park			
Attorney Docket Number:	APL-P2766-G2			
Filed as Large Entity				
Utility Filing Fees				
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Basic Filing:				
Pages:				
Claims:				
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				
Certificate of correction	1811	1	100	100
Extension-of-Time:				

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Total in USD (\$)				100

Electronic Acknowledgement Receipt

EFS ID:	3398147
Application Number:	11213215
International Application Number:	
Confirmation Number:	9248
Title of Invention:	CONSERVING POWER BY REDUCING VOLTAGE SUPPLIED TO AN INSTRUCTION-PROCESSING PORTION OF A PROCESSOR
First Named Inventor/Applicant Name:	Lynn R. Youngs
Customer Number:	62096
Filer:	A.Richard Park
Filer Authorized By:	
Attorney Docket Number:	APL-P2766-C2
Receipt Date:	03-JUN-2008
Filing Date:	25-AUG-2005
Time Stamp:	18:02:45
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	yes
Payment Type	Credit Card
Payment was successfully received in RAM	\$ 100
RAM confirmation Number	2998
Deposit Account	
Authorized User	

File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
-----------------	----------------------	-----------	----------------------------------	------------------	------------------

1	Miscellaneous Incoming Letter	Authorization-deposit-account.pdf	20868 11bc081b27b55366b6362a1237cce587 abb11014	no	1
Warnings:					
Information:					
2	Request for Certificate of Correction	APL-P2766-C2_Certificate_of_Correction.pdf	52740 bf289013c2661ae65e5a36dbce333b40 2e871bf9	no	1
Warnings:					
Information:					
3	Fee Worksheet (PTO-06)	fee-info.pdf	8191 d4dd91b98c1257c1a5b71233724e126 52305de0	no	2
Warnings:					
Information:					
Total Files Size (in bytes):				81799	
<p>This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.</p> <p><u>New Applications Under 35 U.S.C. 111</u> If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.</p> <p><u>National Stage of an International Application under 35 U.S.C. 371</u> If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.</p> <p><u>New International Application Filed with the USPTO as a Receiving Office</u> If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.</p>					

AUTHORIZATION TO DEDUCT ANY UNDERPAYMENTS
OR CREDIT ANY OVERPAYMENTS TO DEPOSIT
ACCOUNT 50-1003

Please deduct any underpayments, credit any overpayments, and charge all required extension of time fees associated with attached filing to Deposit Account Number 50-1003.

Park, Vaughan & Fleming LLP
2820 Fifth Street
Davis, CA 95618-7759
Tel: (530) 759-1661
Fax: (530) 759-1665
Email: richard@parklegal.com

Respectfully submitted,

By



A. Richard Park
Registration No. 41,241

EXHIBIT E

(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 7,383,453 B2
 APPLICATION NO. : 11/213,215
 DATED : 3 June 2008
 INVENTOR(S) : Lynn R. Young

It is certified that an error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

In claim 1 (at column 5, line 36), please delete the word "minimal" and insert the word --minimized-- so the line reads "A instruction-processing system with minimized static".

In claim 1 (at column 5, line 45), please insert the words, --that is sufficient to maintain the state information of the instruction-processing circuitry-- so the line reads "the core voltage is a first value that is sufficient to maintain the state information of the instruction-processing circuitry".

In claim 1 (at column 5, line 47), please insert the words, --that is sufficient to maintain the data stored in the area-- so the line reads "the area voltage is a second value that is sufficient to maintain the data stored in the area".

In claim 1 (at column 5, line 49), please delete the word "is" and insert the words, --can be-- so the line reads "wherein in a first power-saving mode that can be exited upon".

In claim 1 (at column 5, line 53), please delete the words "equal to or greater than the first value" and insert the words, --sufficient to maintain the state information of the instruction-processing circuitry-- so the line reads "the core voltage is sufficient to maintain the state information of the instruction-processing circuitry".

In claim 1 (at column 5, line 55), please delete the words "equal to or greater than the second value" and insert the words, --sufficient to maintain the data stored in the area-- so the line reads "the area voltage is sufficient to maintain the data stored in the area".

In claim 1 (at column 5, line 61), please delete the words "equal to or greater than the second value" and insert the words, --sufficient to maintain the data stored in the area-- so the line reads "the area voltage is sufficient to maintain the data stored in the area"

MAILING ADDRESS OF SENDER:

Shun Yao
 Park, Vaughan & Fleming LLP
 2820 Fifth Street
 Davis, CA 95618

PATENT NO: 7,383,453 B2

Burden Hour Statement: This form is estimated to take 1.0 hour to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, P.O. Box 1450, Arlington, VA 22313-1450.

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 7,383,453 B2
APPLICATION NO. : 11/213,215
DATED : 3 June 2008
INVENTOR(S) : Lynn R. Youngs

It is certified that an error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

In claim 8 (at column 6, line 19), please delete the words "that is" so the line reads "providing the core with a core voltage equal to".

In claim 8 (at column 6, line 20), please insert the words, --that is sufficient to maintain the state information of the instruction-processing circuitry-- so the line reads "a first value that is sufficient to maintain the state information of the instruction-processing circuitry".

In claim 8 (at column 6, line 21), please delete the words "that is" so the line reads "providing the area with an area voltage equal to".

In claim 8 (at column 6, line 22), please insert the words, --that is sufficient to maintain the data stored in the area-- so the line reads "a second value that is sufficient to maintain the data stored in the area".

In claim 8 (at column 6, line 25), please delete the words "equal to" and insert the words, --sufficient to maintain the state information of the instruction-processing circuitry-- so the line reads "providing the core with a core voltage that is sufficient to maintain the state information of the instruction-processing circuitry".

In claim 8 (at column 6, line 26), please delete the words "or greater than the first value".

In claim 8 (at column 6, line 27), please delete the words "equal to" and insert the words, --sufficient to maintain the data stored in the area-- so the line reads "providing the area with an area voltage that is sufficient to maintain the data stored in the area"

In claim 8 (at column 6, line 28), please delete the words "or greater than the second value".

MAILING ADDRESS OF SENDER:

Shun Yao
Park, Vaughan & Fleming LLP
2820 Fifth Street
Davis, CA 95618

PATENT NO: 7,383,453 B2

Burden Hour Statement: This form is estimated to take 1.0 hour to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, P.O. Box 1450, Arlington, VA 22313-1450.

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 7,383,453 B2
 APPLICATION NO. : 11/213,215
 DATED : 3 June 2008
 INVENTOR(S) : Lynn R. Youngs

It is certified that an error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

In claim 10 (at column 6, line 42), please delete the words "instruction-processing system" and insert the word --method-- so the line reads "the method of claim 8, wherein".

In claim 15 (at column 6, line 56), please delete the words "containing data repre-" and insert the words, --storing code which represents-- so the line reads "A computer-readable medium storing code which represents".

In claim 15 (at column 6, line 57), please delete the words "senting" and "minimal" and insert the word --minimized-- so the line reads "an instruction-processing system with minimized static".

In claim 15 (at column 6, line 66), please insert the words, --that is sufficient to maintain the state information of the instruction-processing circuitry-- so the line reads "the core voltage is a first value that is sufficient to maintain the state information of the instruction-processing circuitry".

In claim 15 (at column 7, line 1), please insert the words, --that is sufficient to maintain the data stored in the area-- so the line reads "the area voltage is a second value that is sufficient to maintain the data stored in the area".

In claim 15 (at column 7, line 3), please delete the word "is" and insert the words --can be - so the line reads "wherein in a first power-saving mode that can be exited upon".

In claim 15 (at column 7, line 6), please delete the words "equal to or greater than the first" and insert the words, --sufficient to maintain the state information of the instruction-processing circuitry-- so the line reads "the core voltage is sufficient to maintain the state information of the instruction-processing circuitry".

In claim 15 (at column 7, line 7), please delete the word "value".

MAILING ADDRESS OF SENDER:

Shun Yao
 Park, Vaughan & Fleming LLP
 2820 Fifth Street
 Davis, CA 95618

PATENT NO: 7,383,453 B2

Burden Hour Statement: This form is estimated to take 1.0 hour to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, P.O. Box 1450, Arlington, VA 22313-1450.

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 7,383,453 B2
APPLICATION NO. : 11/213,215
DATED : 3 June 2008
INVENTOR(S) : Lynn R. Youngs

It is certified that an error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

In claim 15 (at column 7, line 8), please delete the words "equal to or greater than the second" and insert the words --sufficient to maintain the data stored in the area-- so the line reads "the area voltage is sufficient to maintain the data stored in the area".

In claim 15 (at column 7, line 9), please delete the word "value".

In claim 15 (at column 7, line 14), please delete the words "is equal to or greater than the second" and insert the words --sufficient to maintain the data stored in the area-- so the line reads "the area voltage is sufficient to maintain the data stored in the area".

In claim 21 (at column 8, line 12), please delete the word, --stem-- so the line reads "The computer-readable medium of claim 15".

MAILING ADDRESS OF SENDER:

Shun Yao
Park, Vaughan & Fleming LLP
2820 Fifth Street
Davis, CA 95618

PATENT NO: 7,383,453 B2

Burden Hour Statement: This form is estimated to take 1.0 hour to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, P.O. Box 1450, Arlington, VA 22313-1450.

Application/Control Number: 11/213,215
Art Unit: 2116

Page 2

EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Shun Yao, Reg. No. 59,242 on 03/26/2008.

The application has been amended as follows:

Amend the claims as follows:

See attached fax from Applicant's Representative (9 pages, dated 03/26/2008) cancelling claims 1-63 and amending claims 64, 71, 73, 78, and 84.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to STEFAN STOYNOV whose telephone number is (571)272-4236. The examiner can normally be reached on 8:30AM-5:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Rehana Perveen can be reached on (571) 272-3676. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Application/Control Number: 11/213,215
Art Unit: 2116

Page 3

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/S. S./
Examiner, Art Unit 2116

/Rehana Perveen/
Supervisory Patent Examiner, Art Unit 2116

Application Number : 11/213,215 Confirmation Number: 9248
Applicant : Lynn R. Youngs
Filed : 25 August 2005
TC/A.U. : 2116
Examiner : Stoynov, Stefan

Docket Number : APL-P2766-C2
Customer No. : 62,096

Proposed Amendments
VIA FAX (571) 273-4236

AMENDMENT

Dear Examiner Stoynov:

Please consider the proposed amendments which are reflected in the listing of claims beginning on page 2 of this paper.

1

SY Amendment E APL P2766-C2 (proposed round 3) doc

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

- 1 1-42 (Cancelled)
- 1 43. (Cancelled)
- 1 44. (Cancelled)
- 1 45. (Cancelled)
- 1 46. (Cancelled)
- 1 47. (Cancelled)
- 1 48. (Cancelled)
- 1 49. (Cancelled)
- 1 50. (Cancelled)
- 1 51. (Cancelled)
- 1 52. (Cancelled)

- 1 53. (Cancelled)
- 1 54. ((Cancelled)
- 1 55. (Cancelled)
- 1 56. (Cancelled)
- 1 57. (Cancelled)
- 1 58. (Cancelled)
- 1 59. (Cancelled)
- 1 60. (Cancelled)
- 1 61. (Cancelled)
- 1 62. (Cancelled)
- 1 63. (Cancelled)
- 1 64. (Currently Amended) An instruction-processing system with
2 | ~~minimal~~minimized static power leakage, the instruction-processing system
3 | comprising:
4 | a core with instruction-processing circuitry;
5 | an area coupled to the core;
6 | a core voltage provided to the core; and

7 an area voltage provided to the area;
8 wherein in a normal operation mode:
9 a clock signal to the core is active;
10 the core voltage is a first value that is sufficient to maintain the
11 state information of the instruction-processing circuitry;
12 the core is active;
13 the area voltage is a second value that is sufficient to maintain the
14 data stored in the area; and
15 the area is active;
16 wherein in a first power-saving mode that ~~is~~ can be exited upon receipt of an
17 interrupt signal:
18 the clock signal to the core is inactive;
19 the core voltage is ~~equal to or greater than the first value~~ sufficient to
20 maintain the state information of the instruction-processing circuitry; and
21 the area voltage is ~~equal to or greater than the second value~~ sufficient
22 to maintain the data stored in the area;
23 wherein in a second power-saving mode that can be exited upon receipt of a
24 signal that is not an interrupt signal:
25 the clock signal to the core is inactive;
26 the core voltage is less than the first value; and
27 the area voltage is ~~equal to or greater than the second value~~ sufficient
28 to maintain the data stored in the area.

1 65. (Previously presented) The instruction-processing system of claim
2 64, wherein the first power-saving mode can be exited upon receipt of a signal that
3 is not an interrupt signal.

1 66. (Previously presented) The instruction-processing system of claim
2 64, wherein the area comprises a cache.

1 67. (Previously presented) The instruction-processing system of claim
2 66, wherein the area further comprises cache tags.

1 68. (Previously presented) The instruction-processing system of claim
2 64, wherein prior to entering the second power-saving mode, the state of the core is
3 saved to a memory.

1 69. (Previously presented) The instruction-processing system of claim
2 64, wherein upon exiting the second power-saving mode, the state of the core is
3 restored.

1 70. (Previously presented) The instruction-processing system of claim
2 64, wherein in the second power-saving mode, the core voltage is at zero.

1 71. (Currently amended) A method for minimizing static power leakage
2 in an instruction-processing system, wherein the instruction-processing system
3 comprises a core with instruction-processing circuitry, an area coupled to the core, a
4 core voltage provided to the core, and an area voltage provided to the area, the
5 method comprising:

- 6 entering a normal operation mode by:
 - 7 providing a clock signal to the core;
 - 8 providing the core with a core voltage ~~that is equal to a first value~~
 - 9 that is sufficient to maintain the state information of the instruction-
 - 10 processing circuitry;
 - 11 providing the area with an area voltage ~~that is equal to a second~~
 - 12 value that is sufficient to maintain the data stored in the area;
- 13 entering a first power-saving mode by:
 - 14 disabling the clock signal to the core;
 - 15 providing the core with a core voltage that is sufficient to
 - 16 maintain the state information of the instruction-processing circuitry equal to

17 | ~~or greater than~~
 18 | ~~the first value; and~~
 19 | providing the area with an area voltage that is sufficient
 20 | ~~to maintain the data stored in the area equal to or greater than~~
 21 | ~~the second value;~~
 22 | exiting the first power-saving mode upon receipt of an interrupt signal;
 23 | entering a second power-saving mode by:
 24 | disabling the clock signal to the core;
 25 | setting the core voltage to a value less than the first value; and
 26 | providing the area with an area voltage that is ~~equal to or greater than~~
 27 | ~~the second value~~ sufficient to maintain the data stored in the area; and
 28 | exiting the second power-saving mode upon receipt of a signal that is not an
 29 | interrupt signal.

1 72. (Previously presented) The method of claim 71, further comprising
 2 exiting the first power-saving mode upon receipt of a signal that is not an interrupt
 3 signal.

1 | 73. (Currently amended) The ~~instruction-processing system~~ method of
 2 claim 71, wherein the area comprises a cache.

1 74. (Previously presented) The method of claim 73, wherein the area
 2 further comprises cache tags.

1 75. (Previously presented) The method of claim 71, further comprising
 2 saving the state of the core to a memory prior to entering the second power-saving
 3 mode.

1 76. (Previously presented) The method of claim 71, further comprising
 2 restoring the state of the core upon exiting the second power-saving mode.

1 77. (Previously presented) The method of claim 71, wherein in the
 2 second power-saving mode, setting the core voltage to the value less than the first
 3 value comprises setting the core voltage to zero.

1 78. (Currently amended) A computer-readable medium ~~containing data~~
 2 ~~representing~~ storing code which represents an instruction-processing system with
 3 ~~minimal~~ minimized static power leakage, the instruction-processing system
 4 comprising:

- 5 a core with instruction-processing circuitry;
- 6 an area coupled to the core;
- 7 a core voltage provided to the core; and
- 8 an area voltage provided to the area;
- 9 wherein in a normal operation mode:
 - 10 a clock signal to the core is active;
 - 11 the core voltage is a first value that is sufficient to maintain the
 12 state information of the instruction-processing circuitry;
 - 13 the core is active;
 - 14 the area voltage is a second value that is sufficient to maintain the
 15 data stored in the area; and
 - 16 the area is active;
- 17 wherein in a first power-saving mode that is can be exited upon receipt of an
 18 interrupt signal:
 - 19 the clock signal to the core is inactive;
 - 20 the core voltage is sufficient to maintain the state information of the
 21 instruction-processing circuitry equal to or greater than the first value; and
 - 22 the area voltage is sufficient to maintain the data stored in the
 23 area equal to or greater than the second value;
- 24 wherein in a second power-saving mode that can be exited upon receipt of a
 25 signal that is not an interrupt signal:
 - 26 the clock signal to the core is inactive;

27 the core voltage is less than the first value; and
28 the area voltage is sufficient to maintain the data stored in the
29 ~~area equal to or greater than the second value.~~

1 79. (Previously presented) The computer-readable medium of claim 78,
2 wherein the first power-saving mode can be exited upon receipt of a signal that is
3 not an interrupt signal.

1 80. (Previously presented) The computer-readable medium of claim 78,
2 wherein the area comprises a cache.

1 81. (Previously presented) The computer-readable medium of claim 80,
2 wherein the area further comprises cache tags.

1 82. (Previously presented) The computer-readable medium of claim 78,
2 wherein prior to entering the second power-saving mode, the state of the core is
3 saved to a memory.

1 83. (Previously presented) The computer-readable medium of claim 78,
2 wherein upon exiting the second power-saving mode, the state of the core is
3 restored.

1 84. (Currently amended) The computer-readable medium ~~stem~~ of claim
2 78, wherein in the second power-saving mode, the core voltage is at zero.

Respectfully submitted.

By /Shun Yao/
Shun Yao
Registration No. 59,242

Date: 26 March 2008

Shun Yao
Park, Vaughan & Fleming LLP
2820 Fifth Street
Davis, CA 95618-7759
Tel: (530) 759-1667
Fax: (530) 759-1665
Email: shun@parklegal.com

Electronic Acknowledgement Receipt

EFS ID:	3626749
Application Number:	11213215
International Application Number:	
Confirmation Number:	9248
Title of Invention:	CONSERVING POWER BY REDUCING VOLTAGE SUPPLIED TO AN INSTRUCTION-PROCESSING PORTION OF A PROCESSOR
First Named Inventor/Applicant Name:	Lynn R. Youngs
Customer Number:	62096
Filer:	A.Richard Park
Filer Authorized By:	
Attorney Docket Number:	APL-P2766-C2
Receipt Date:	17-JUL-2008
Filing Date:	25-AUG-2005
Time Stamp:	12:02:59
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	no
------------------------	----

File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Miscellaneous Incoming Letter	Authorization-deposit-account.pdf	20868 <small>1fbc08fb27b5536bb6362a1237cc6587 abb1f014</small>	no	1

Warnings:

Information:

2	Request for Certificate of Correction	APL-P2766-C2_Certificate_of_Corr.pdf	586684 16cd635aad7035e343de2686aade2c223a50ad5a	no	15
Warnings:					
Information:					
Total Files Size (in bytes):			607552		
<p>This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.</p> <p><u>New Applications Under 35 U.S.C. 111</u> If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.</p> <p><u>National Stage of an International Application under 35 U.S.C. 371</u> If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.</p> <p><u>New International Application Filed with the USPTO as a Receiving Office</u> If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.</p>					

AUTHORIZATION TO DEDUCT ANY UNDERPAYMENTS
OR CREDIT ANY OVERPAYMENTS TO DEPOSIT
ACCOUNT 50-1003

Please deduct any underpayments, credit any overpayments, and charge all required extension of time fees associated with attached filing to Deposit Account Number 50-1003.

Park, Vaughan & Fleming LLP
2820 Fifth Street
Davis, CA 95618-7759
Tel: (530) 759-1661
Fax: (530) 759-1665
Email: richard@parklegal.com

Respectfully submitted,

By



A. Richard Park
Registration No. 41,241

EXHIBIT F

SCAN! Please forward case and paper to address shown below.

SPE RESPONSE FOR CERTIFICATE OF CORRECTION

DATE : 7/23/08 Paper No.: _____
TO SPE OF : ART UNIT 2114 StEFAN STOYNOV
SUBJECT : Request for Certificate of Correction for Appl. No.: 11/213215 Patent No.: 7383453

Please respond to this request for a certificate of correction within 7 days.

Please review the requested changes/corrections as shown in the attached certificate of correction. Please complete this form (see below) and forward it with the file to:

Certificates of Correction Branch (CofC)
South Tower - 9A22
Palm Location 7580

HENRY RANDALL
Certificates of Correction Branch
703-308-9390 ext. _____

Thank You For Your Assistance

The request for issuing the above-identified correction(s) is hereby:

Note your decision on the appropriate box.

- | | |
|---|---|
| <input type="checkbox"/> Approved | All changes apply. |
| <input type="checkbox"/> Approved in Part | Specify below which changes do not apply. |
| <input type="checkbox"/> Denied | State the reasons for denial below. |

Comments: _____

SCAN! Please forward case and paper to address shown below.

SPE RESPONSE FOR CERTIFICATE OF CORRECTION

DATE : 8/8/08 Paper No.: _____
TO SPE OF : ART UNIT 2116 Perveen Rehana (Spe)
SUBJECT : Request for Certificate of Correction for Appl. No.: 11/213215 Patent No.: 7383453

Please respond to this request for a certificate of correction within 7 days.

Please review the requested changes/corrections as shown in the attached certificate of correction. Please complete this form (see below) and forward it with the file to:

Certificates of Correction Branch (CofC)
South Tower - 9A22
Palm Location 7580

HENRY RANDALL
Certificates of Correction Branch
703-308-9390 ext. _____

Thank You For Your Assistance

The request for issuing the above-identified correction(s) is hereby:

Note your decision on the appropriate box.

- Approved All changes apply.
- Approved In Part Specify below which changes do not apply.
- Denied State the reasons for denial below.

Comments: _____

SCAN! Please forward case and paper to address shown below.

SPE RESPONSE FOR CERTIFICATE OF CORRECTION

DATE : 8/8/08 Paper No.: _____
TO SPE OF : ART UNIT 2116 Perveen Rehana (Spe)
SUBJECT : Request for Certificate of Correction for Appl. No.: 11/213215 Patent No.: 7383453

Please respond to this request for a certificate of correction within 7 days.

Please review the requested changes/corrections as shown in the attached certificate of correction. Please complete this form (see below) and forward it with the file to:

Certificates of Correction Branch (CofC)
South Tower - 9A22
Palm Location 7580

HENRY RANDALL
Certificates of Correction Branch
703-308-9390 ext. _____

Thank You For Your Assistance

The request for issuing the above-identified correction(s) is hereby:

Note your decision on the appropriate box.

- | | |
|--|---|
| <input type="checkbox"/> Approved | All changes apply. |
| <input checked="" type="checkbox"/> Approved in Part | Specify below which changes do not apply. |
| <input type="checkbox"/> Denied | State the reasons for denial below. |

Comments: THE FOLLOWING CHANGE WAS OMITTED
FOR CLAIM 15:

IN CLAIM 15 (AT COLUMN 7, LINE 15), PLEASE
DELETE THE WORD "VALUE".


REHANA PERVEEN
SUPERVISORY PATENT EXAMINER

2116

SPE

Art Unit

EXHIBIT G

Application Number : 11/213,215 Confirmation Number: 9248
Issued U.S. Pat. No. : 7,383,453
Applicant : Lynn R. Youngs
Filed : 25 August 2005
Issued : 3 June 2008
T.C./A.U. : 2116
Examiner : Stoynov, Stefan

Docket Number : APL-P2766-C2
Customer No. : 62,096

Petition to Correct Assignee
Via Electronic Filing

PETITION TO CORRECT ASSIGNEE UNDER 37 C.F.R. § 3.81(b)

Sir:

Applicant inadvertently failed to include the correct assignee name on the PTOL-85B form (i.e., the issue fee transmittal) filed on 16 April 2008 for the above-mentioned patent application. Applicant hereby petitions for the correction of the assignee name and residence data incorrectly submitted with the issue fee transmittal. The correct assignee name and residence data are as follows:

Name: APPLE INC.
Residence: CUPERTINO, CA

Enclosed is a copy of the notice of recordation of assignment, which shows that the assignment was submitted for recordation on 07 May 2007, along with a certificate of correction and payment in the amount of \$130 for this petition.

Upon grant of this request, please forward the request to the certificate of correction branch for issuance of the certificate of correction. (Note that the certificate of correction fee of \$100 was previously paid on 03 June 2008.)

1

JC/APJ APL-P2766-C2 Petition to correct assignee.doc

Respectfully submitted,

By /Anthony Jones/

Anthony Jones
Registration No. 59,521

Date: 06 October 2008

Anthony Jones
Park, Vaughan & Fleming LLP
2820 Fifth Street
Davis, CA 95618-7759
Tel: (530) 759-1666
Fax: (530) 759-1665
Email: tony@parklegal.com



UNITED STATES PATENT AND TRADEMARK OFFICE

UNDER SECRETARY OF COMMERCE FOR INTELLECTUAL PROPERTY AND
DIRECTOR OF THE UNITED STATES PATENT AND TRADEMARK OFFICE

MAY 11, 2007

PTAS



103403779A

A. RICHARD PARK
PARK, VAUGHAN & FLEMING LLP
2820 FIFTH STREET
DAVIS, CA 95618-7759

UNITED STATES PATENT AND TRADEMARK OFFICE
NOTICE OF RECORDATION OF ASSIGNMENT DOCUMENT

THE ENCLOSED DOCUMENT HAS BEEN RECORDED BY THE ASSIGNMENT DIVISION OF THE U.S. PATENT AND TRADEMARK OFFICE. A COMPLETE MICROFILM COPY IS AVAILABLE AT THE ASSIGNMENT SEARCH ROOM ON THE REEL AND FRAME NUMBER REFERENCED BELOW.

PLEASE REVIEW ALL INFORMATION CONTAINED ON THIS NOTICE. THE INFORMATION CONTAINED ON THIS RECORDATION NOTICE REFLECTS THE DATA PRESENT IN THE PATENT AND TRADEMARK ASSIGNMENT SYSTEM. IF YOU SHOULD FIND ANY ERRORS OR HAVE QUESTIONS CONCERNING THIS NOTICE, YOU MAY CONTACT THE EMPLOYEE WHOSE NAME APPEARS ON THIS NOTICE AT 571-272-3350. PLEASE SEND REQUEST FOR CORRECTION TO: U.S. PATENT AND TRADEMARK OFFICE, MAIL STOP: ASSIGNMENT SERVICES BRANCH, P.O. BOX 1450, ALEXANDRIA, VA 22313.

RECORDATION DATE: 05/07/2007

REEL/FRAME: 019265/0922
NUMBER OF PAGES: 8

BRIEF: CHANGE OF NAME (SEE DOCUMENT FOR DETAILS).

ASSIGNOR:

APPLE COMPUTER, INC.

DOC DATE: 01/09/2007

ASSIGNEE:

APPLE INC.
1 INFINITE LOOP
CUPERTINO, CALIFORNIA 95014

SERIAL NUMBER: 11179695

FILING DATE: 07/11/2005

PATENT NUMBER:

ISSUE DATE:

TITLE: METHOD AND APPARATUS FOR INCREASING THE OPERATING FREQUENCY OF AN ELECTRONIC CIRCUIT

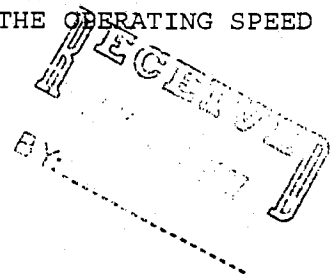
SERIAL NUMBER: 11591866

FILING DATE: 11/01/2006

PATENT NUMBER:

ISSUE DATE:

TITLE: METHOD AND APPARATUS FOR SELECTIVELY INCREASING THE OPERATING SPEED OF AN ELECTRONIC CIRCUIT



019265/0922 PAGE 2

SERIAL NUMBER: 11103896 FILING DATE: 04/11/2005
PATENT NUMBER: ISSUE DATE:
TITLE: APPARATUS AND METHOD TO FACILITATE UNIVERSAL REMOTE CONTROL

SERIAL NUMBER: 11213547 FILING DATE: 08/25/2005
PATENT NUMBER: ISSUE DATE:
TITLE: APPARATUS AND METHOD TO FACILITATE UNIVERSAL REMOTE CONTROL

SERIAL NUMBER: 11710277 FILING DATE: 02/23/2007
PATENT NUMBER: ISSUE DATE:
TITLE: APPARATUS AND METHOD TO FACILITATE UNIVERSAL REMOTE CONTROL

SERIAL NUMBER: 10102321 FILING DATE: 03/19/2002
PATENT NUMBER: ISSUE DATE:
TITLE: METHOD AND APPARATUS FOR CONFIGURING A WIRELESS DEVICE THROUGH
REVERSE ADVERTISING

SERIAL NUMBER: 10228712 FILING DATE: 08/27/2002
PATENT NUMBER: ISSUE DATE:
TITLE: METHOD AND APPARATUS FOR UPLOADING MASS-DISTRIBUTED CONTENT TO A
SERVER

SERIAL NUMBER: 10236262 FILING DATE: 09/06/2002
PATENT NUMBER: ISSUE DATE:
TITLE: METHOD AND APPARATUS FOR MARKING CONTENT DURING DISTRIBUTION OF THE
CONTENT TO A CLIENT

SERIAL NUMBER: 11213215 FILING DATE: 08/25/2005
PATENT NUMBER: ISSUE DATE:
TITLE: CONSERVING POWER BY REDUCING VOLTAGE SUPPLIED TO AN INSTRUCTION-
PROCESSING PORTION OF A PROCESSOR

SERIAL NUMBER: 11515315 FILING DATE: 09/01/2006
PATENT NUMBER: ISSUE DATE:
TITLE: CONSERVING POWER BY REDUCING VOLTAGE SUPPLIED TO AN INSTRUCTION-
PROCESSING PORTION OF A PROCESSOR

SERIAL NUMBER: 11715092 FILING DATE: 03/07/2007
PATENT NUMBER: ISSUE DATE:
TITLE: CONSERVING POWER BY REDUCING VOLTAGE SUPPLIED TO AN INSTRUCTION-
PROCESSING PORTION OF A PROCESSOR

SERIAL NUMBER: 10102174 FILING DATE: 03/19/2002
PATENT NUMBER: ISSUE DATE:
TITLE: METHOD AND APPARATUS FOR SUPPORTING DUPLICATE SUPPRESSION WHEN
ISSUING MULTICAST QUERIES USING DNS-FORMAT MESSAGE PACKETS

SERIAL NUMBER: 10453278 FILING DATE: 06/02/2003
PATENT NUMBER: ISSUE DATE:
TITLE: METHOD AND APPARATUS FOR DISTRIBUTING COMPUTER FILES ACROSS A
NETWORK

019265/0922 PAGE 3

SERIAL NUMBER: 10406297 FILING DATE: 04/02/2003
PATENT NUMBER: ISSUE DATE:
TITLE: METHOD AND APPARATUS FOR COMMUNICATING BETWEEN DEVICE DRIVERS IN A
COMPUTER SYSTEM

SERIAL NUMBER: 10421563 FILING DATE: 04/23/2003
PATENT NUMBER: ISSUE DATE:
TITLE: APPARATUS AND METHOD FOR INDICATING PASSWORD QUALITY AND VARIETY

SERIAL NUMBER: 11483750 FILING DATE: 07/10/2006
PATENT NUMBER: ISSUE DATE:
TITLE: METHOD AND APPARATUS FOR IMPLEMENTING A SLEEP PROXY FOR SERVICES ON
A NETWORK

SERIAL NUMBER: 11581866 FILING DATE: 10/16/2006
PATENT NUMBER: ISSUE DATE:
TITLE: METHOD AND APPARATUS FOR IMPLEMENTING A SLEEP PROXY FOR SERVICES ON
A NETWORK

SERIAL NUMBER: 10676572 FILING DATE: 09/30/2003
PATENT NUMBER: ISSUE DATE:
TITLE: METHOD AND APPARATUS FOR ACCELERATING THE EXPIRATION OF RESOURCE
RECORDS IN A LOCAL CACHE

SERIAL NUMBER: 10748024 FILING DATE: 12/30/2003
PATENT NUMBER: ISSUE DATE:
TITLE: SELF-IDENTIFYING MICROPHONE

SERIAL NUMBER: 10738055 FILING DATE: 12/16/2003
PATENT NUMBER: ISSUE DATE:
TITLE: ALMOST-SYMMETRIC MULTIPROCESSOR THAT SUPPORTS HIGH-PERFORMANCE AND
ENERGY-EFFICIENT EXECUTION

SERIAL NUMBER: 10746584 FILING DATE: 12/23/2003
PATENT NUMBER: ISSUE DATE:
TITLE: METHOD AND APPARATUS FOR ADVERTISING A USER INTERFACE FOR
CONFIGURING, CONTROLLING AND/OR MONITORING A SERVICE

SERIAL NUMBER: 10916988 FILING DATE: 08/11/2004
PATENT NUMBER: ISSUE DATE:
TITLE: I/O DESCRIPTOR CACHE FOR BUS MASTERING I/O CONTROLLERS

SERIAL NUMBER: 10925173 FILING DATE: 08/23/2004
PATENT NUMBER: ISSUE DATE:
TITLE: METHOD AND APPARATUS FOR ENCODING MEMORY CONTROL SIGNALS TO REDUCE
PIN COUNT

SERIAL NUMBER: 10871845 FILING DATE: 06/17/2004
PATENT NUMBER: ISSUE DATE:
TITLE: INTERPOSER CONTAINING BYPASS CAPACITORS FOR REDUCING VOLTAGE NOISE
IN AN IC DEVICE

019265/0922 PAGE 4

SERIAL NUMBER: 10877401 FILING DATE: 06/25/2004
PATENT NUMBER: ISSUE DATE:
TITLE: METHOD AND APPARATUS FOR LOOKING UP CONFIGURATION INFORMATION FOR A NETWORK NODE

SERIAL NUMBER: 10877518 FILING DATE: 06/25/2004
PATENT NUMBER: ISSUE DATE:
TITLE: METHOD AND APPARATUS FOR FACILITATING LONG-LIVED DNS QUERIES

SERIAL NUMBER: 10877414 FILING DATE: 06/25/2004
PATENT NUMBER: ISSUE DATE:
TITLE: METHOD AND APPARATUS FOR UPDATING RESOURCE RECORDS IN A NAME-SERVER DATABASE

SERIAL NUMBER: 10877533 FILING DATE: 06/25/2004
PATENT NUMBER: ISSUE DATE:
TITLE: METHOD AND APPARATUS FOR PROVIDING LINK-LOCAL IPV4 ADDRESSING ACROSS MULTIPLE INTERFACES OF A NETWORK NODE

SERIAL NUMBER: 11172332 FILING DATE: 06/29/2005
PATENT NUMBER: ISSUE DATE:
TITLE: METHOD AND APPARATUS FOR INCREASING DATA TRANSFER RATES THROUGH A COMMUNICATION CHANNEL

SERIAL NUMBER: 10925174 FILING DATE: 08/23/2004
PATENT NUMBER: ISSUE DATE:
TITLE: REDUCING THE NUMBER OF POWER AND GROUND PINS REQUIRED TO DRIVE ADDRESS SIGNALS TO MEMORY MODULES

SERIAL NUMBER: 11098135 FILING DATE: 04/04/2005
PATENT NUMBER: ISSUE DATE:
TITLE: METHOD AND APPARATUS FOR DETECTING A ROUTER THAT IMPROPERLY RESPONDS TO ARP REQUESTS

SERIAL NUMBER: 11445609 FILING DATE: 06/02/2006
PATENT NUMBER: ISSUE DATE:
TITLE: METHOD AND APPARATUS FOR DETECTING INCORRECT RESPONSES TO NETWORK QUERIES

SERIAL NUMBER: 11159956 FILING DATE: 06/22/2005
PATENT NUMBER: ISSUE DATE:
TITLE: METHOD AND APPARATUS FOR ESTABLISHING A SECURE CONNECTION

SERIAL NUMBER: 11542573 FILING DATE: 10/03/2006
PATENT NUMBER: ISSUE DATE:
TITLE: METHOD AND APPARATUS FOR ESTABLISHING A SECURE CONNECTION

SERIAL NUMBER: 11178152 FILING DATE: 07/08/2005
PATENT NUMBER: ISSUE DATE:
TITLE: SEMICONDUCTOR DIE PACKAGE WITH INTERNAL BYPASS CAPACITORS

SERIAL NUMBER: 11172333 FILING DATE: 06/29/2005
PATENT NUMBER: ISSUE DATE:
TITLE: METHOD AND APPARATUS FOR PROVIDING WAFER-LEVEL CAPACITIVE DECOUPLING

019265/0922 PAGE 5

SERIAL NUMBER: 11210571 FILING DATE: 08/23/2005
PATENT NUMBER: ISSUE DATE:
TITLE: METHOD AND APPARATUS FOR WAKING UP A SLEEPING SYSTEM

SERIAL NUMBER: 11173109 FILING DATE: 07/01/2005
PATENT NUMBER: ISSUE DATE:
TITLE: HYBRID VOLTAGE/CURRENT-MODE TRANSMISSION LINE DRIVER

SERIAL NUMBER: 11234789 FILING DATE: 09/23/2005
PATENT NUMBER: ISSUE DATE:
TITLE: COSMETICALLY UNIFORM REFLECTIVE BORDER AREA IN A TRANSFLECTIVE DISPLAY

SERIAL NUMBER: 11304439 FILING DATE: 12/14/2005
PATENT NUMBER: ISSUE DATE:
TITLE: METHOD AND APPARATUS FOR SELECTIVELY SWITCHING IC PORTS TO CARD SLOTS

SERIAL NUMBER: 11367813 FILING DATE: 03/03/2006
PATENT NUMBER: ISSUE DATE:
TITLE: METHOD AND APPARATUS FOR CHANGING THE CLOCK FREQUENCY OF A MEMORY SYSTEM

SERIAL NUMBER: 11354721 FILING DATE: 02/15/2006
PATENT NUMBER: ISSUE DATE:
TITLE: METHOD AND APPARATUS FOR MEASURING DIE-LEVEL INTEGRATED CIRCUIT POWER VARIATIONS

SERIAL NUMBER: 11303760 FILING DATE: 12/15/2005
PATENT NUMBER: ISSUE DATE:
TITLE: METHOD AND APPARATUS FOR MASKING ACOUSTIC KEYBOARD EMANATIONS

SERIAL NUMBER: 11327531 FILING DATE: 01/05/2006
PATENT NUMBER: ISSUE DATE:
TITLE: METHOD AND APPARATUS FOR MASKING ACOUSTIC KEYBOARD EMANATIONS

SERIAL NUMBER: 11411363 FILING DATE: 04/25/2006
PATENT NUMBER: ISSUE DATE:
TITLE: METHOD AND APPARATUS FOR FACILITATING DEVICE HIBERNATION

SERIAL NUMBER: 11445632 FILING DATE: 06/02/2006
PATENT NUMBER: ISSUE DATE:
TITLE: METHOD AND APPARATUS FOR QUICKLY REANIMATING DEVICES FROM HIBERNATION

SERIAL NUMBER: 11433620 FILING DATE: 05/12/2006
PATENT NUMBER: ISSUE DATE:
TITLE: METHOD AND APPARATUS FOR COMMUNICATING WITH AN EMBEDDED CONTROLLER WITHIN A COMPUTING DEVICE

SERIAL NUMBER: 11591804 FILING DATE: 11/01/2006
PATENT NUMBER: ISSUE DATE:
TITLE: INSTRUCTIONS FOR EFFICIENTLY ACCESSING UNALIGNED VECTORS

019265/0922 PAGE 6

SERIAL NUMBER: 11500677 FILING DATE: 08/08/2006
PATENT NUMBER: ISSUE DATE:
TITLE: DISPLAY THAT EMITS CIRCULARLY-POLARIZED LIGHT

SERIAL NUMBER: 11498616 FILING DATE: 08/03/2006
PATENT NUMBER: ISSUE DATE:
TITLE: MULTIPLEXED GRAPHICS ARCHITECTURE FOR GRAPHICS POWER MANAGEMENT

SERIAL NUMBER: 11498617 FILING DATE: 08/03/2006
PATENT NUMBER: ISSUE DATE:
TITLE: METHOD AND APPARATUS FOR USING A 32-BIT OPERATING SYSTEM KERNEL TO SUPPORT 64-BIT APPLICATIONS

SERIAL NUMBER: 11607571 FILING DATE: 11/30/2006
PATENT NUMBER: ISSUE DATE:
TITLE: ENHANCED VENT FOR OUTLET FOR A COOLING SYSTEM

SERIAL NUMBER: 11449167 FILING DATE: 06/08/2006
PATENT NUMBER: ISSUE DATE:
TITLE: CULTURING HUMAN CELLS AND TISSUES IN AN N-GLYCOLYLNEURAMINIC ACID-FREE ENVIRONMENT

SERIAL NUMBER: 09991092 FILING DATE: 11/16/2001
PATENT NUMBER: 6813719 ISSUE DATE: 11/02/2004
TITLE: METHOD AND APPARATUS FOR INCREASING THE OPERATING FREQUENCY OF AN ELECTRONIC CIRCUIT

SERIAL NUMBER: 10256610 FILING DATE: 09/27/2002
PATENT NUMBER: 6848032 ISSUE DATE: 01/25/2005
TITLE: PIPELINING CACHE-COHERENCE OPERATIONS IN A SHARED-MEMORY MULTIPROCESSING SYSTEM

SERIAL NUMBER: 09991098 FILING DATE: 11/16/2001
PATENT NUMBER: 6889235 ISSUE DATE: 05/03/2005
TITLE: METHOD AND APPARATUS FOR QUANTIFYING THE NUMBER OF IDENTICAL CONSECUTIVE DIGITS WITHIN A STRING

SERIAL NUMBER: 10122056 FILING DATE: 04/12/2002
PATENT NUMBER: 6914551 ISSUE DATE: 07/05/2005
TITLE: APPARATUS AND METHOD TO FACILITATE UNIVERSAL REMOTE CONTROL

SERIAL NUMBER: 10135116 FILING DATE: 04/29/2002
PATENT NUMBER: 6920574 ISSUE DATE: 07/19/2005
TITLE: CONSERVING POWER BY REDUCING VOLTAGE SUPPLIED TO AN INSTRUCTION-PROCESSING PORTION OF A PROCESSOR

SERIAL NUMBER: 10975520 FILING DATE: 10/27/2004
PATENT NUMBER: 6966008 ISSUE DATE: 11/15/2005
TITLE: METHOD AND APPARATUS FOR INCREASING THE OPERATING FREQUENCY OF AN ELECTRONIC CIRCUIT

019265/0922 PAGE 7

SERIAL NUMBER: 11103911 FILING DATE: 04/11/2005
PATENT NUMBER: 6973585 ISSUE DATE: 12/06/2005
TITLE: CONSERVING POWER BY REDUCING VOLTAGE SUPPLIED TO AN INSTRUCTION-
 PROCESSING PORTION OF A PROCESSOR

SERIAL NUMBER: 10676573 FILING DATE: 09/30/2003
PATENT NUMBER: 7107442 ISSUE DATE: 09/12/2006
TITLE: METHOD AND APPARATUS FOR IMPLEMENTING A SLEEP PROXY FOR SERVICES ON
 A NETWORK

SERIAL NUMBER: 10838310 FILING DATE: 05/03/2004
PATENT NUMBER: 7171570 ISSUE DATE: 01/30/2007
TITLE: METHOD AND APPARATUS FOR SELECTIVELY INCREASING THE OPERATING SPEED
 OF AN ELECTRONIC CIRCUIT

MARY BENTON, EXAMINER
ASSIGNMENT SERVICES BRANCH
PUBLIC RECORDS DIVISION

5707

FORM PTO-1595
1-31-92

05-09-2007



103403779

COVER SHEET

U.S. DEPARTMENT OF COMMERCE
Patent and Trademark Office

To the Assistant Commissioner for Patents

with original documents or copy thereof.

1. Name of conveying party(ies):
Apple Computer, Inc.
1 Infinite Loop
Cupertino, CA 95014
Additional name(s) of conveying party(ies) attached? Yes No

2. Name and address of receiving party(ies):
Name: Apple Inc.
Street Address: 1 Infinite Loop
City: State: Zip: Cupertino, CA 95014
Additional name(s) & address(es) attached?
 Yes No

3. Nature of conveyance:
 Assignment Merger
 Security Agreement Change of Name
 Other
Execution Date: 09 January 2007

4. Application number(s) or patent number(s):
If this document is being filed together with a new application, the execution date of the application is:
A. Patent Application No.(s): Attached Appendix A
Patent No.(s): Attached Appendix B
Additional numbers attached? Yes No

OFFICE OF PUBLIC RECORDS
MAY - 7 AM 7:19
FINANCE SECTION

5. Name and address of party to whom correspondence concerning document should be mailed:
Name: A. Richard Park (Reg. No. 41,241)
Park, Vaughan & Fleming LLP
2820 Fifth Street
Davis, CA 95618-7759

6. Total number of applications and patents involved: 67 62
7. Total fee (37 CFR 3.41) \$2,480.00
 Enclosed
 Authorized to be charged to deposit account
8. Deposit account number:

DO NOT USE THIS SPACE

9. Statement and signature.
To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.
A. Richard Park _____ 03 May 2007
Name of Person Signing Signature Date
Total number of pages including cover sheet, attachments and document: [8]

05/08/2007 MJAMA1 00000057 11179695
01 FC:8021 (2480.00 DP)

State of California
Secretary of State

CERTIFICATE OF STATUS
DOMESTIC CORPORATION

I, DEBRA BOWEN, Secretary of State of the State of California, hereby certify:

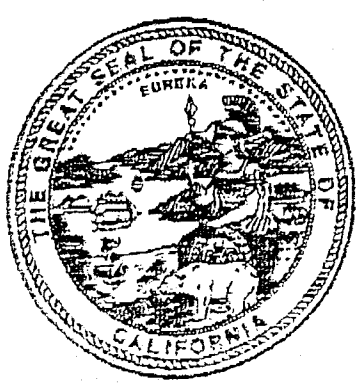
That on the 3RD day of JANUARY, 1977, APPLE INC. became incorporated under the laws of the State of California by filing its Articles of Incorporation in this office; and

That said corporation's corporate powers, rights and privileges are not suspended on the records of this office; and

That according to the records of this office, the said corporation is authorized to exercise all its corporate powers, rights and privileges and is in good legal standing in the State of California; and

That no information is available in this office on the financial condition, business activity or practices of this corporation.

IN WITNESS WHEREOF, I execute
this certificate and affix the Great Seal
of the State of California this day of
January 10, 2007.



Debra Bowen
DEBRA BOWEN
Secretary of State

S4W

State of California
Secretary of State



I, DEBRA BOWEN, Secretary of State of the State of California,
hereby certify:

That the attached transcript of 2 page(s) has been compared
with the record on file in this office, of which it purports to be a copy, and
that it is full, true and correct.

IN WITNESS WHEREOF, I execute this
certificate and affix the Great Seal of the
State of California this day of

JAN 09 2007



Debra Bowen

DEBRA BOWEN
Secretary of State

ENDORSED - FILED
in the office of the Secretary of State
of the State of California

CERTIFICATE OF OWNERSHIP

JAN 09 2007

Peter L. Oppenheimer and Donald J. Rosenberg certify that:

1. They are the Senior Vice President and Chief Financial Officer, and Senior Vice President, General Counsel and Secretary, respectively, of Apple Computer, Inc., a California corporation (the "Corporation").
2. The Corporation owns all of the outstanding shares of Apple Inc., a California corporation ("Merger Sub").
3. The board of directors of the Corporation duly adopted the following resolution:

RESOLVED, that the Corporation merge Merger Sub, its wholly-owned subsidiary corporation, into itself and assume all its obligations pursuant to Section 1110 of the California Corporations Code; and

RESOLVED FURTHER, that Article I of the Corporation's Restated Articles of Incorporation, as amended, shall be amended in its entirety to read as follows:

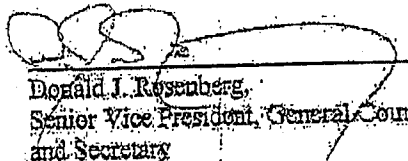
- I. The name of the corporation is Apple Inc.

We further declare under penalty of perjury under the laws of the State of California that the matters set forth in this certificate are true and correct of our own knowledge.

Date: January 9, 2007



Peter L. Oppenheimer,
Senior Vice President and
Chief Financial Officer



Donald J. Rosenberg,
Senior Vice President, General Counsel
and Secretary



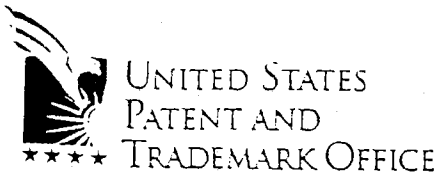
Appendix A
Patent Applications

No	Appl. No.	Filing Date	Docket No.	Title
1	11/179,695	7/11/2005	APL-P2682-C2	METHOD AND SYSTEM FOR INCREASING THE OPERATING FREQUENCY OF AN ELECTRONIC CIRCUIT
2	11/591,866	11/1/2006	APL-P2682US-C3	METHOD AND APPARATUS FOR INCREASING THE OPERATING FREQUENCY OF AN ELECTRONIC CIRCUIT
3	11/103,896	4/11/2005	APL-P2736-C1	APPARATUS AND METHOD TO FACILITATE UNIVERSAL REMOTE CONTROL
4	11/213,547	8/25/2005	APL-P2736-C2	APPARATUS AND METHOD TO FACILITATE UNIVERSAL REMOTE CONTROL
5	11/710,277	2/23/2007	APL-P2736-C3	APPARATUS AND METHOD TO FACILITATE UNIVERSAL REMOTE CONTROL
6	10/102,321	3/19/2002	APL-P2757US1	METHOD AND APPARATUS FOR CONFIGURING A WIRELESS DEVICE THROUGH REVERSE ADVERTISING
7	10/228,712	8/27/2002	APL-P2762	METHOD AND APPARATUS FOR UPLOADING MASS-DISTRIBUTED CONTENT TO A SERVER
8	10/236,262	9/6/2002	APL-P2765	METHOD AND APPARATUS FOR MARKING CONTENT DURING DISTRIBUTION OF THE CONTENT TO A CLIENT
9	11/213,215	8/25/2005	APL-P2766-C2	CONSERVING POWER BY REDUCING VOLTAGE SUPPLIED TO AN INSTRUCTION-PROCESSING PORTION OF A PROCESSOR
10	11/515,315	9/1/2006	APL-P2766-C3	CONSERVING POWER BY REDUCING VOLTAGE SUPPLIED TO AN INSTRUCTION-PROCESSING PORTION OF A PROCESSOR
11	11/715,092	3/7/2007	APL-P2766-C4	CONSERVING POWER BY REDUCING VOLTAGE SUPPLIED TO AN INSTRUCTION-PROCESSING PORTION OF A PROCESSOR
12	10/102,174	3/19/2002	APL-P2768	METHOD AND APPARATUS FOR SUPPORTING DUPLICATE SUPPRESSION WHEN ISSUING MULTICAST QUERIES USING DNS-FORMAT MESSAGE PACKETS
13	10/453,278	6/2/2003	APL-P2872	METHOD AND APPARATUS FOR DISTRIBUTING COMPUTER FILES ACROSS A NETWORK
14	10/406,297	4/2/2003	APL-P3016	METHOD AND APPARATUS FOR COMMUNICATING BETWEEN DEVICE DRIVERS IN A COMPUTER SYSTEM
15	10/421,563	4/23/2003	APL-P3062	APPARATUS AND METHOD FOR INDICATING PASSWORD QUALITY AND VARIETY
16	11/483,750	7/10/2006	APL-P3152-C1	METHOD AND APPARATUS FOR IMPLEMENTING A SLEEP PROXY FOR SERVICES ON A NETWORK
17	11/581,866	10/16/2006	APL-P3152-C2	METHOD AND APPARATUS FOR IMPLEMENTING A SLEEP PROXY FOR SERVICES ON A NETWORK
18	10/676,572	9/30/2003	APL-P3153	METHOD AND APPARATUS FOR ACCELERATING THE EXPIRATION OF RESOURCE RECORDS IN A LOCAL CACHE
19	10/748,024	12/30/2003	APL-P3231	SELF-IDENTIFYING MICROPHONE
20	10/738,055	12/16/2003	APL-P3240	ALMOST-SYMMETRIC MULTIPROCESSOR THAT SUPPORTS HIGH-PERFORMANCE AND ENERGY-EFFICIENT EXECUTION
21	10/746,584	12/23/2003	APL-P3284	METHOD AND APPARATUS FOR ADVERTISING A USER INTERFACE FOR CONFIGURING CONTROLLING AND/OR MONITORING A SERVICE
22	10/916,988	8/11/2004	APL-P3302	I/O DESCRIPTOR CACHE FOR BUS MASTERING I/O CONTROLLERS
23	10/925,173	8/23/2004	APL-P3364	METHOD AND APPARATUS FOR ENCODING MEMORY CONTROL SIGNALS TO REDUCE PIN COUNT
24	10/871,845	6/17/2004	APL-P3389US1	INTERPOSER CONTAINING BYPASS CAPACITORS FOR REDUCING VOLTAGE NOISE IN AN IC DEVICE
25	10/877,401	6/25/2004	APL-P3398	METHOD AND APPARATUS FOR LOOKING UP CONFIGURATION INFORMATION FOR A NETWORK NODE
26	10/877,518	6/25/2004	APL-P3447	METHOD AND APPARATUS FOR FACILITATING LONG-LIVED DNS QUERIES
27	10/877,414	6/25/2004	APL-P3448	METHOD AND APPARATUS FOR UPDATING RESOURCE RECORDS IN

				A NAME-SERVER DATABASE
28	10/877,533	6/25/2004	APL-P3449	METHOD AND APPARATUS FOR PROVIDING LINK-LOCAL IPv4 ADDRESSING ACROSS MULTIPLE INTERFACES OF A NETWORK NODE
29	11/172,332	6/29/2005	APL-P3490US1	METHOD AND APPARATUS FOR INCREASING DATA TRANSFER RATES THROUGH A COMMUNICATION CHANNEL
30	10/925,174	8/23/2004	APL-P3594	REDUCING THE NUMBER OF POWER AND GROUND PINS REQUIRED TO DRIVE ADDRESS SIGNALS TO MEMORY MODULES
31	11/098,135	4/4/2005	APL-P3624US1	METHOD AND APPARATUS FOR DETECTING A ROUTER THAT IMPROPERLY RESPONDS TO ARP REQUESTS
32	11/445,609	6/2/2006	APL-P3624USX1	METHOD AND APPARATUS FOR DETECTING INCORRECT RESPONSES TO NETWORK QUERIES
33	11/159,956	6/22/2005	APL-P3805US1	METHOD AND APPARATUS FOR ESTABLISHING A SECURE CONNECTION
34	11/542,573	10/3/2006	APL-P3805US1-CI	METHOD AND APPARATUS FOR ESTABLISHING A SECURE CONNECTION
35	11/178,152	7/8/2005	APL-P3845US1	SEMICONDUCTOR DIE PACKAGE WITH INTERNAL BYPASS CAPACITORS
36	11/172,333	6/29/2005	APL-P3846US1	METHOD AND APPARATUS FOR PROVIDING WAFER-LEVEL CAPACITIVE DECOUPLING
37	11/210,571	8/23/2005	APL-P3851US1	METHOD AND APPARATUS FOR WAKING UP A SLEEPING SYSTEM
38	11/173,109	7/1/2005	APL-P3869US1	HYBRID VOLTAGE/CURRENT MODE TRANSMISSION LINE DRIVE
39	11/234,789	9/23/2005	APL-P3977US1	COSMETICALLY UNIFORM REFLECTIVE BORDER AREA IN A TRANSFLECTIVE DISPLAY
40	11/304,439	12/14/2005	APL-P3982US1	METHOD AND APPARATUS FOR SELECTIVELY SWITCHING PORTS TO CARD SLOTS
41	11/367,813	3/3/2006	APL-P3987US1	METHOD AND APPARATUS FOR CHANGING THE CLOCK FREQUENCY OF A MEMORY SYSTEM
42	11/354,721	2/15/2006	APL-P4007US1	METHOD AND APPARATUS FOR MEASURING DIE-LEVEL INTEGRATED CIRCUIT POWER VARIATIONS
43	11/303,760	12/15/2005	APL-P4057US1	METHOD AND APPARATUS FOR MASKING ACOUSTIC KEYBOARD EMANATIONS
44	11/327,531	1/5/2006	APL-P4057USX1	METHOD AND APPARATUS FOR MASKING ACOUSTIC KEYBOARD EMANATIONS
45	11/411,363	4/25/2006	APL-P4063US1	METHOD AND APPARATUS FOR FACILITATING DEVICE HIBERNATION
46	11/445,632	6/2/2006	APL-P4064USX1	METHOD AND APPARATUS FOR QUICKLY REANIMATING DEVICES FROM HIBERNATION
47	11/433,620	5/12/2006	APL-P4207US1	METHOD AND APPARATUS FOR COMMUNICATING WITH AN EMBEDDED CONTROLLER WITHIN A COMPUTING DEVICE
48	11/591,804	11/1/2006	APL-P4326US1	INSTRUCTIONS FOR EFFICIENTLY ACCESSING UNALIGNED VECTORS
49	11/500,677	8/8/2006	APL-P4473US1	A DISPLAY THAT EMITS CIRCULARLY-POLARIZED LIGHT
50	11/498,616	8/3/2006	APL-P4481US1	MULTIPLEXED GRAPHICS ARCHITECTURE FOR GRAPHICS POWER MANAGEMENT
51	11/498,617	8/3/2006	APL-P4503US1	METHOD AND APPARATUS FOR USING A 32-BIT OPERATING SYSTEM KERNEL TO SUPPORT 64-BIT APPLICATIONS
52	11/607,571	11/30/2006	APL-P4612US1	ENHANCED VENT FOR OUTLET FOR A COOLING SYSTEM
53	11/449,167	8/4/2006	APL-P4406US1	METHOD AND APPARATUS FOR SWITCHING BETWEEN GRAPHICS SOURCES

Appendix B
Patents

<u>No</u>	<u>Pat No./Appl. No.</u>	<u>Filing Date</u>	<u>Docket No.</u>	<u>Title</u>
54	6,813,719 09/991,092	11/16/2001	APL-P2682	METHOD AND APPARATUS FOR INCREASING THE OPERATING FREQUENCY OF AN ELECTRONIC CIRCUIT
55	6,848,032 10/256,610	9/27/2002	APL-P2767	PIPELINING CACHE-COHERENCE OPERATIONS IN A SHARED-MEMORY MULTIPROCESSING SYSTEM
56	6,889,235 09/991,098	11/16/2001	APL-P2684	METHOD AND APPARATUS FOR QUANTIFYING THE NUMBER OF IDENTICAL CONSECUTIVE DIGITS WITHIN A STRING
57	6,914,551 10/122,056	4/12/2002	APL-P2736	APPARATUS AND METHOD TO FACILITATE UNIVERSAL REMOTE CONTROL
58	6,920,574 10/135,116	4/29/2002	APL-P2766	CONSERVING POWER BY REDUCING VOLTAGE SUPPLIED TO AN INSTRUCTION-PROCESSING PORTION OF A PROCESSOR
59	6,966,008 10/975,520	10/27/2004	APL-P2682-C1	METHOD AND SYSTEM FOR INCREASING THE OPERATING FREQUENCY OF AN ELECTRONIC CIRCUIT
60	6,973,585 11/103,911	4/11/2005	APL-P2766-C1	CONSERVING POWER BY REDUCING VOLTAGE SUPPLIED TO AN INSTRUCTION-PROCESSING PORTION OF A PROCESSOR
61	7,107,442 10/676,573	9/30/2003	APL-P3152	METHOD AND APPARATUS FOR IMPLEMENTING A SLEEP PROXY FOR SERVICES ON A NETWORK
62	7,171,570 10/838,310	5/3/2004	APL-P2682US-X1	METHOD AND APPARATUS FOR SELECTIVELY INCREASING THE OPERATING SPEED OF AN ELECTRONIC CIRCUIT



JULY 03, 2002

PARK, VAUGHAN & FLEMING LLP
EDWARD J. GRUNDLER
508 SECOND STREET, SUITE 201
DAVIS, CA 95616

PTAS

Under Secretary of Commerce For Intellectual Property and
Director of the United States Patent and Trademark Office
Washington, DC 20231
www.uspto.gov

RECEIVED



102080830A

UNITED STATES PATENT AND TRADEMARK OFFICE
NOTICE OF RECORDATION OF ASSIGNMENT DOCUMENT

THE ENCLOSED DOCUMENT HAS BEEN RECORDED BY THE ASSIGNMENT DIVISION OF THE U.S. PATENT AND TRADEMARK OFFICE. A COMPLETE MICROFILM COPY IS AVAILABLE AT THE ASSIGNMENT SEARCH ROOM ON THE REEL AND FRAME NUMBER REFERENCED BELOW.

PLEASE REVIEW ALL INFORMATION CONTAINED ON THIS NOTICE. THE INFORMATION CONTAINED ON THIS RECORDATION NOTICE REFLECTS THE DATA PRESENT IN THE PATENT AND TRADEMARK ASSIGNMENT SYSTEM. IF YOU SHOULD FIND ANY ERRORS OR HAVE QUESTIONS CONCERNING THIS NOTICE, YOU MAY CONTACT THE EMPLOYEE WHOSE NAME APPEARS ON THIS NOTICE AT 703-308-9723. PLEASE SEND REQUEST FOR CORRECTION TO: U.S. PATENT AND TRADEMARK OFFICE, ASSIGNMENT DIVISION, BOX ASSIGNMENTS, CG-4, 1213 JEFFERSON DAVIS HWY, SUITE 300, WASHINGTON, D.C. 20231.

RECORDATION DATE: 04/29/2002

REEL/FRAME: 012856/0922
NUMBER OF PAGES: 2

BRIEF: ASSIGNMENT OF ASSIGNOR'S INTEREST (SEE DOCUMENT FOR DETAILS).

ASSIGNOR:

YOUNGS, LYNN R.

DCC DATE: 04/18/2002

ASSIGNEE:

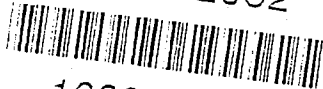
APPLE COMPUTER, INC.
1 INFINITE LOOP
CUPERTINO, CALIFORNIA 95014

SERIAL NUMBER: 10136116
PATENT NUMBER:

FILING DATE: 04/29/2002
ISSUE DATE:

MARY BENTON, EXAMINER
ASSIGNMENT DIVISION
OFFICE OF PUBLIC RECORDS

05-08-2002



Attorney Docket No. APL-P2766

102080880

FORM PTO-1595
1-31-92

RECORDATION FORM COVER SHEET
PATENTS ONLY

U.S. DEPARTMENT OF COMMERCE
Patent and Trademark Office

To the Assistant Commissioner for Patents and Trademarks: Please record the attached original documents or copy thereof.

1. Name of conveying party(ies):
Lynn R. Youngs
Additional name(s) of conveying party(ies) attached? Yes No

05/29/02

2. Name and address of receiving party(ies):
Name: Apple Computer, Inc.
Street Address: 1 Infinite Loop
City: State: Zip: Cupertino, CA 95014

3. Nature of conveyance:
 Assignment Merger
 Security Agreement Change of Name
 Other
Execution Date: April 18, 2002

Additional name(s) & address(es) attached?
 Yes No

4. Application number(s) or patent number(s):
If this document is being filed together with a new application, the execution date of the application is: April 18, 2002
A. Patent Application No.(s):
Additional numbers attached? Yes No

B. Patent No.(s):

5. Name and address of party to whom correspondence concerning document should be mailed:
Name: Edward J. Grundler
Park, Vaughan & Fleming LLP
508 Second Street, Suite 201
Davis, CA 95616
Attorney Docket No.: APL-P2766

6. Total number of applications and patents involved: [1]
7. Total fee (37 CFR 3.41) \$40.00
 Enclosed
 Authorized to be charged to deposit account
8. Deposit account number:

DO NOT USE THIS SPACE

9. Statement and signature.
To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.
Edward J. Grundler
Name of Person Signing
Signature
April 29, 2002
Date

Total number of pages including cover sheet, attachments and document: [2]

10/135116
10/135116
04/29/02

#corporate power of attorney
05/02/2002 EHAILE1 00000065 10135116
02 FC:581 40.00 DP

Patent

ASSIGNMENT

For good and valuable consideration, the receipt of which is hereby acknowledged, I, the undersigned, Lynn R. Youngs do hereby sell, assign and transfer to: Apple Computer, Inc., a corporation of California, having a principal place of business at Cupertino, California ("Assignee"), its successors, assigns, and legal representatives, the entire right, title and interest for the United States and all foreign countries, in and to any improvements which are disclosed in the application for the United States Letters Patent,

which has been executed by the undersigned concurrently herewith.

which was filed on and assigned U.S. Serial No.

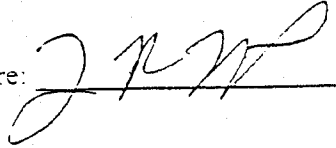
and is entitled:

CONSERVING POWER BY REDUCING VOLTAGE SUPPLIED TO AN INSTRUCTION-PROCESSING PORTION OF A PROCESSOR
and in and to said application and all divisional, continuing, substitute, renewal, reissue, and other applications for Letters Patent which have been or shall be filed in the United States and all foreign countries on any of said improvements; and in and to all original and reissued patents which have been or shall be issued in the United States and all foreign countries on said improvements; and in and to all rights of priority resulting from the filing of said United States application; and

Agree that said Assignee may apply for and receive Letters Patent for said improvements in its own name; and that, when requested, without charge to, but at the expense of, said Assignee, its successors, assigns and legal representatives, to carry out in good faith the intent and purpose of this Assignment, the undersigned will execute all divisional, continuing, substitute, renewal, reissue, and all other patent applications on any and all said improvements; execute all rightful oaths, assignments, powers of attorney and other papers; communicate to said Assignee, its successors, assigns, and representatives, all facts known to the undersigned relating to said improvements and the history thereof; and generally do everything possible which said Assignee, its successors, assigns and legal representatives shall consider desirable for aiding in securing and maintaining proper patent protection for said improvements and for vesting title to said improvements and all applications for patents and all patents on said improvements, in said Assignee, its successors, assigns and legal representatives; and

Covenant with said Assignee, its successors, assigns and legal representatives that no assignment, grant, mortgage, license, or other agreement affecting the rights and property herein conveyed has been made to others by the undersigned, and that full right to convey the same as herein expressed is possessed by the undersigned.

Date: 4/18/02 Name: Lynn R. Youngs

Signature: 

Electronic Patent Application Fee Transmittal

Application Number:	11213215			
Filing Date:	25-Aug-2005			
Title of Invention:	CONSERVING POWER BY REDUCING VOLTAGE SUPPLIED TO AN INSTRUCTION-PROCESSING PORTION OF A PROCESSOR			
First Named Inventor/Applicant Name:	Lynn R. Youngs			
Filer:	A.Richard Park			
Attorney Docket Number:	APL-P2766-C2			
Filed as Large Entity				
Utility under 35 USC 111(a) Filing Fees				
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Basic Filing:				
Pages:				
Claims:				
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				
Extension-of-Time:				

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Processing Fee, except for Provis. apps	1808	1	130	130
Total in USD (\$)				130

Electronic Acknowledgement Receipt

EFS ID:	4062870
Application Number:	11213215
International Application Number:	
Confirmation Number:	9248
Title of Invention:	CONSERVING POWER BY REDUCING VOLTAGE SUPPLIED TO AN INSTRUCTION-PROCESSING PORTION OF A PROCESSOR
First Named Inventor/Applicant Name:	Lynn R. Youngs
Customer Number:	62096
Filer:	A.Richard Park
Filer Authorized By:	
Attorney Docket Number:	APL-P2766-C2
Receipt Date:	06-OCT-2008
Filing Date:	25-AUG-2005
Time Stamp:	18:37:31
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	yes
Payment Type	Credit Card
Payment was successfully received in RAM	\$130
RAM confirmation Number	3536
Deposit Account	
Authorized User	

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
-----------------	----------------------	-----------	-------------------------------------	------------------	------------------

1	Miscellaneous Incoming Letter	Authorization-deposit-account.pdf	20868 1fbc08fb27b5536bb6362a1237cce587abb1f014	no	1
Warnings:					
Information:					
2	Petition for review by the Office of Petitions.	APL-P2766-C2_Petition.pdf	99421 4d5244a96fcc6c848e34bf14eb8720b47a70d492	no	2
Warnings:					
Information:					
3	Petition for review by the Office of Petitions.	APL-P2766-C2_Certificate_of_Correction.pdf	54786 ac77972a6995cb53477c9c117b93bf0ec124f9e	no	1
Warnings:					
Information:					
4	Petition for review by the Office of Petitions.	APL-P2766-C2_Assignment.pdf	837062 ce2cd8b40b9f59f8268b7a55341cbaa3e124c5a9	no	18
Warnings:					
Information:					
5	Fee Worksheet (PTO-06)	fee-info.pdf	30264 cf08f6a638c96e5fb71b86919c2b2bec46e50759	no	2
Warnings:					
Information:					
Total Files Size (in bytes):				1042401	
<p>This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.</p> <p><u>New Applications Under 35 U.S.C. 111</u> If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.</p> <p><u>National Stage of an International Application under 35 U.S.C. 371</u> If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.</p> <p><u>New International Application Filed with the USPTO as a Receiving Office</u> If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.</p>					

AUTHORIZATION TO DEDUCT ANY UNDERPAYMENTS
OR CREDIT ANY OVERPAYMENTS TO DEPOSIT
ACCOUNT 50-1003

Please deduct any underpayments, credit any overpayments, and charge all required extension of time fees associated with attached filing to Deposit Account Number 50-1003.

Park, Vaughan & Fleming LLP
2820 Fifth Street
Davis, CA 95618-7759
Tel: (530) 759-1661
Fax: (530) 759-1665
Email: richard@parklegal.com

Respectfully submitted,

By



A. Richard Park
Registration No. 41,241

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 7,383,453
APPLICATION NO. : 11/213,215
DATED : 3 June 2008
INVENTOR(S) : Lynn R. Youngs

It is certified that an error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

In the Assignee Name (on page 1), please delete "Apple, Inc.".

In the Assignee Name (on page 1), please insert --APPLE INC.--.

MAILING ADDRESS OF SENDER:

A. Richard Park
Park, Vaughan & Fleming LLP
2820 Fifth Street
Davis, CA 95618

PATENT NO: 7,383,453

Burden Hour Statement: This form is estimated to take 1.0 hour to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, P.O. Box 1450, Arlington, VA 22313-1450.



UNITED STATES PATENT AND TRADEMARK OFFICE

Commissioner for Patents
United States Patent and Trademark Office
P.O. Box 1450
Alexandria, VA 22313-1450
www.uspto.gov

PVF -- APPLE INC.
c/o PARK, VAUGHAN & FLEMING LLP
2820 FIFTH STREET
DAVIS CA 95618-7759

COPY MAILED

NOV 10 2008

In re Patent No. 7,383,453 :
Issue Date: June 3, 2008 :
Application No. 11/213,215 : ON PETITION
Filed: August 25, 2005 :
Attorney Docket No. APL-P2766-C2 :

This is a decision on the petition filed October 6, 2008, which is being treated as a request under 37 CFR 3.81(b)¹ to correct the name of the assignee on the front page of the above-identified patent by way of a Certificate of Correction.

The request is **GRANTED**.

Telephone inquiries concerning this decision may be directed to the undersigned at (571) 272-3215. Inquiries regarding the issuance of a certificate of correction should be directed to the Certificate of Correction Branch at (571) 272-4200.

The Certificates of Correction Branch will be notified of this decision granting the petition under 37 CFR 3.81(b) and directing issuance of the requested Certificate of Correction.

Charlema Grant
Petitions Attorney
Office of Petitions

¹ See MPEP 1309, subsection II; and Official Gazette of June 22, 2004.

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,383,453 B2
APPLICATION NO. : 11/213215
DATED : June 3, 2008
INVENTOR(S) : Lynn R. Youngs

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

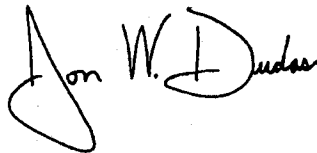
Title Page item [73]

In the Assignee Name (on page 1), please delete "Apple, Inc.".

In the Assignee name (on page 1), please insert --APPLE INC.--.

Signed and Sealed this

Thirteenth Day of January, 2009



JON W. DUDAS
Director of the United States Patent and Trademark Office

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,383,453 B2
APPLICATION NO. : 11/213215
DATED : June 3, 2008
INVENTOR(S) : Lynn R. Youngs

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title page Item [73]

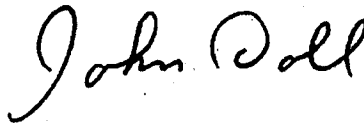
In the Assignee Name (on page 1), please delete "Apple, Inc."

Title page Item [73]

In the Assignee Name (on page 1), please insert --APPLE INC.--.

Signed and Sealed this

Seventeenth Day of February, 2009



JOHN DOLL
Acting Director of the United States Patent and Trademark Office

EXHIBIT H

(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

Page 1 of 4

PATENT NO : 7,383,453 B2
 APPLICATION NO. : 11/213,215
 DATED : 3 June 2008
 INVENTOR(S) : Lynn R. Young

It is certified that an error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

In claim 1 (at column 5, line 36), please delete the word "minimal" and insert the word -- minimized-- so the line reads "A instruction-processing system with minimized static".

In claim 1 (at column 5, line 45), please insert the words, --that is sufficient to maintain the state information of the instruction-processing circuitry-- so the line reads "the core voltage is a first value that is sufficient to maintain the state information of the instruction-processing circuitry".

In claim 1 (at column 5, line 47), please insert the words, --that is sufficient to maintain the data stored in the area-- so the line reads "the area voltage is a second value that is sufficient to maintain the data stored in the area".

In claim 1 (at column 5, line 49), please delete the word "is" and insert the words, --can be-- so the line reads "wherein in a first power-saving mode that can be exited upon".

In claim 1 (at column 5, line 53), please delete the words "equal to or greater than the first value" and insert the words, --sufficient to maintain the state information of the instruction-processing circuitry-- so the line reads "the core voltage is sufficient to maintain the state information of the instruction-processing circuitry".

In claim 1 (at column 5, line 55), please delete the words "equal to or greater than the second value" and insert the words, --sufficient to maintain the data stored in the area-- so the line reads "the area voltage is sufficient to maintain the data stored in the area".

In claim 1 (at column 5, line 61), please delete the words "equal to or greater than the second value" and insert the words, --sufficient to maintain the data stored in the area-- so the line reads "the area voltage is sufficient to maintain the data stored in the area"

MAILING ADDRESS OF SENDER:

Shun Yao
 Park, Vaughan & Fleming LLP
 2820 Fifth Street
 Davis, CA 95618

PATENT NO: 7,383,453 B2

Burden Hour Statement: This form is estimated to take 1.0 hour to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, P.O. Box 1450, Arlington, VA 22313-1450.

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

Page 2 of 4

PATENT NO : 7,383,453 B2
APPLICATION NO. : 11/213,215
DATED : 3 June 2008
INVENTOR(S) : Lynn R. Youngs

It is certified that an error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

In claim 8 (at column 6, line 19), please delete the words "that is" so the line reads "providing the core with a core voltage equal to".

In claim 8 (at column 6, line 20), please insert the words, --that is sufficient to maintain the state information of the instruction-processing circuitry-- so the line reads "a first value that is sufficient to maintain the state information of the instruction-processing circuitry".

In claim 8 (at column 6, line 21), please delete the words "that is" so the line reads "providing the area with an area voltage equal to".

In claim 8 (at column 6, line 22), please insert the words, --that is sufficient to maintain the data stored in the area-- so the line reads "a second value that is sufficient to maintain the data stored in the area".

In claim 8 (at column 6, line 25), please delete the words "equal to" and insert the words, --sufficient to maintain the state information of the instruction-processing circuitry-- so the line reads "providing the core with a core voltage that is sufficient to maintain the state information of the instruction-processing circuitry".

In claim 8 (at column 6, line 26), please delete the words "or greater than the first value".

In claim 8 (at column 6, line 27), please delete the words "equal to" and insert the words, --sufficient to maintain the data stored in the area-- so the line reads "providing the area with an area voltage that is sufficient to maintain the data stored in the area"

In claim 8 (at column 6, line 28), please delete the words "or greater than the second value".

MAILING ADDRESS OF SENDER:

Shun Yao
Park, Vaughan & Fleming LLP
2820 Fifth Street
Davis, CA 95618

PATENT NO: 7,383,453 B2

Burden Hour Statement: This form is estimated to take 1.0 hour to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, P.O. Box 1450, Arlington, VA 22313-1450.

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

Page 3 of 4

PATENT NO : 7,383,453 B2
APPLICATION NO. : 11/213,215
DATED : 3 June 2008
INVENTOR(S) : Lynn R. Youngs

It is certified that an error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

In claim 10 (at column 6, line 42), please delete the words "instruction-processing system" and insert the word --method-- so the line reads "the method of claim 8, wherein".

In claim 15 (at column 6, line 56), please delete the words "containing data repre-" and insert the words, --storing code which represents-- so the line reads "A computer-readable medium storing code which represents".

In claim 15 (at column 6, line 57), please delete the words "senting" and "minimal" and insert the word --minimized-- so the line reads "an instruction-processing system with minimized static".

In claim 15 (at column 6, line 66), please insert the words, --that is sufficient to maintain the state information of the instruction-processing circuitry-- so the line reads "the core voltage is a first value that is sufficient to maintain the state information of the instruction-processing circuitry".

In claim 15 (at column 7, line 1), please insert the words, --that is sufficient to maintain the data stored in the area-- so the line reads "the area voltage is a second value that is sufficient to maintain the data stored in the area".

In claim 15 (at column 7, line 3), please delete the word "is" and insert the words --can be-- so the line reads "wherein in a first power-saving mode that can be exited upon".

In claim 15 (at column 7, line 6), please delete the words "equal to or greater than the first" and insert the words, --sufficient to maintain the state information of the instruction-processing circuitry-- so the line reads "the core voltage is sufficient to maintain the state information of the instruction-processing circuitry".

In claim 15 (at column 7, line 7), please delete the word "value".

MAILING ADDRESS OF SENDER:

Shun Yao
Park, Vaughan & Fleming LLP
2820 Fifth Street
Davis, CA 95618

PATENT NO: 7,383,453 B2

Burden Hour Statement: This form is estimated to take 1.0 hour to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, P.O. Box 1450, Arlington, VA 22313-1450.

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

Page 4 of 4

PATENT NO : 7,383,453 B2
 APPLICATION NO. : 11/213,215
 DATED : 3 June 2008
 INVENTOR(S) : Lynn R. Youngs

It is certified that an error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below.

In claim 15 (at column 7, line 8), please delete the words "equal to or greater than the second" and insert the words --sufficient to maintain the data stored in the area-- so the line reads "the area voltage is sufficient to maintain the data stored in the area".

In claim 15 (at column 7, line 9), please delete the word "value".

In claim 15 (at column 7, line 14), please delete the words "is equal to or greater than the second" and insert the words --sufficient to maintain the data stored in the area-- so the line reads "the area voltage is sufficient to maintain the data stored in the area".

In claim 15, (column 7, line 15) delete the word "value".

In claim 21 (column 8, line 12), please delete the word, --stem-- so the line reads,

"The computer-readable medium of claim 15".

MAILING ADDRESS OF SENDER:

Shun Yao
 Park, Vaughan & Fleming LLP
 2820 Fifth Street
 Davis, CA 95618

PATENT NO: 7,383,453 B2

Burden Hour Statement: This form is estimated to take 1.0 hour to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, P.O. Box 1450, Arlington, VA 22313-1450.