

Exhibit B

I submit this report as a supplement to my infringement report. Specifically, the Defendants and their experts have lodged a number of criticisms regarding my methodology of the tests I presented in my Opening Expert Report. When I served that report, I also produced the modified source code that I tested, the data measured by my tests, my testing protocol, the code for one of the programs that I used to generate traffic, and the specifications of the hardware I was testing. In my opinion, I provided the Defendants and their experts with everything they would need to replicate my tests. In this Supplemental Test Results, I used the same testing methodology to address the Defendants' criticisms and further to quantify and verify my expert report and deposition testimony. I incorporate the tests and discussion of those tests from my Opening Expert Report into this supplement.

The purpose of my tests was to compare the performance results of the modified code with and without the call to subroutine *rt_free()* associated with the *rt_genid* deletion in *rt_intern_hash()*. My testing methodology in these tests is consistent with my original tests. For this test, I used the same parameters and conditions as used in Appendix P of my Opening Report. For the runs without *rt_free()*, I used the same code. For the runs with *rt_free()*, I used the code produced in my Opening Report with the single change that I removed the “/* */” that commented out the call to *rt_free()*. These results demonstrate that, for approximately 80% of the runs, the code without the call to *rt_free()* runs more efficiently. This is consistent with my earlier results, and this is why I disabled the call to *rt_free()* in my original tests. Further, this confirms my analysis that calling *rt_free()* results in additional work that adversely affects performance. While these tests show that the Defendants' criticisms of my tests are incorrect, this exercise has

uncovered a mistake I made in Appendix P of my Opening Report. Specifically, in the test that generated the data for the condition “Cache Enabled” and “120 Disabled,” the memory on the server was running low. This was likely leading to these cases to have slower performance. This mistake is isolated to the data in Appendix P, and I did not form my opinion of the 11-25% performance improvement from the ’120 patent based on these results. The cause of this mistake was that the amount of memory consumed by the routing table was significantly larger in the tests for Appendix P than the tests associated with the other Appendices. The routing cache table associated with Appendix P was sixteen times the size of the routing table in the tests associated with Appendices H and N and four times the size of the routing table in the tests associated with Appendix O. Moreover, I verified that this mistake was isolated to Appendix P by re-examining the remainder of the results in the tests in Appendices H, N, O and P to verify that there was sufficient, available memory.

Executed on March 16, 2011



Mark T. Jones

Hping Interval (μsec)	IP Address	IP Address	Working	No	rt_free	rt_free	Average Number of URL Successes Per Second	CPU Busy Processing Percentage	CPU Interrupt Processing Percentage	Average Number of URL Successes Scaled by CPU Busy Processing Percentage	Percentage Difference Between URL Scaled Results for "rt_free" vs "no rt_free"
64	20	100000	1	0	1	0	2156	23.5	9.3	9161	
16	20	100000	1	0	1	0	2018	25.2	12.2	8022	
4	20	100000	1	0	1	0	2004	28.3	15.4	7075	
0	20	100000	1	0	1	0	2018	30.6	17.8	6603	
64	20	100000	0	1	1	1	2171	24.4	10.1	8891	2.9
16	20	100000	0	1	1	1	2033	25.8	12.9	7880	1.8
4	20	100000	0	1	1	1	2014	29.9	16.8	6745	4.7
0	20	100000	0	1	1	1	2014	31.4	18.4	6418	2.8
64	20	150000	1	0	1	0	2163	21.6	8.7	10023	
16	20	150000	1	0	1	0	2027	23.8	12.4	8514	
4	20	150000	1	0	1	0	1994	32.5	18.8	6143	
0	20	150000	1	0	1	0	1994	31.6	19.0	6301	
64	20	150000	0	1	1	1	2167	25.1	11.3	8619	14.0
16	20	150000	0	1	1	1	2056	27.5	14.0	7474	12.2
4	20	150000	0	1	1	1	2034	30.9	17.9	6582	-7.1
0	20	150000	0	1	1	1	2020	33.9	21.4	5955	5.5
64	20	250000	1	0	1	0	2119	25.2	10.9	8402	
16	20	250000	1	0	1	0	1969	30.3	17.0	6499	
4	20	250000	1	0	1	0	1959	34.1	20.6	5749	
0	20	250000	1	0	1	0	1858	40.1	27.5	4635	
64	20	250000	0	1	1	1	2172	26.1	12.5	8315	1.0
16	20	250000	0	1	1	1	2027	32.1	19.1	6305	3.0
4	20	250000	0	1	1	1	2021	39.3	26.3	5138	10.6
0	20	250000	0	1	1	1	2023	44.7	31.3	4529	2.3
64	40	100000	1	0	1	0	2146	23.4	9.3	9169	
16	40	100000	1	0	1	0	2047	25.0	11.7	8178	
4	40	100000	1	0	1	0	2010	28.1	15.1	7165	
0	40	100000	1	0	1	0	2019	31.0	17.8	6508	
64	40	100000	0	1	1	1	2166	23.6	9.4	9187	-0.2
16	40	100000	0	1	1	1	2027	25.4	11.9	7974	2.5
4	40	100000	0	1	1	1	2010	27.9	15.4	7200	-0.5
0	40	100000	0	1	1	1	2009	30.2	17.5	6658	-2.3
64	40	150000	1	0	1	0	2164	23.0	9.5	9404	
16	40	150000	1	0	1	0	2010	25.7	12.0	7832	
4	40	150000	1	0	1	0	2008	30.4	16.1	6608	
0	40	150000	1	0	1	0	2016	31.0	18.0	6493	
64	40	150000	0	1	1	1	2169	24.0	10.3	9045	3.8

16	40	150000	0	1	2053	26.7	13.0	7678	2.0
4	40	150000	0	1	2040	30.3	17.1	6726	-1.8
0	40	150000	0	1	2043	33.2	19.9	6149	5.3
64	40	250000	1	0	2098	24.4	10.8	8584	
16	40	250000	1	0	1945	30.0	16.8	6477	
4	40	250000	1	0	1936	34.6	22.1	5593	
0	40	250000	1	0	1886	39.6	26.7	4759	
64	40	250000	0	1	2167	26.9	12.8	8054	6.2
16	40	250000	0	1	2028	32.1	19.4	6313	2.5
4	40	250000	0	1	2023	38.2	25.3	5290	5.4
0	40	250000	0	1	2034	44.3	31.0	4588	3.6